



SPEC[®] CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML10 Gen9

(3.30 GHz, Intel Xeon E3-1225 v5)

SPECfp[®]2006 =

96.1

SPECfp_base2006 =

93.9

CPU2006 license: 3

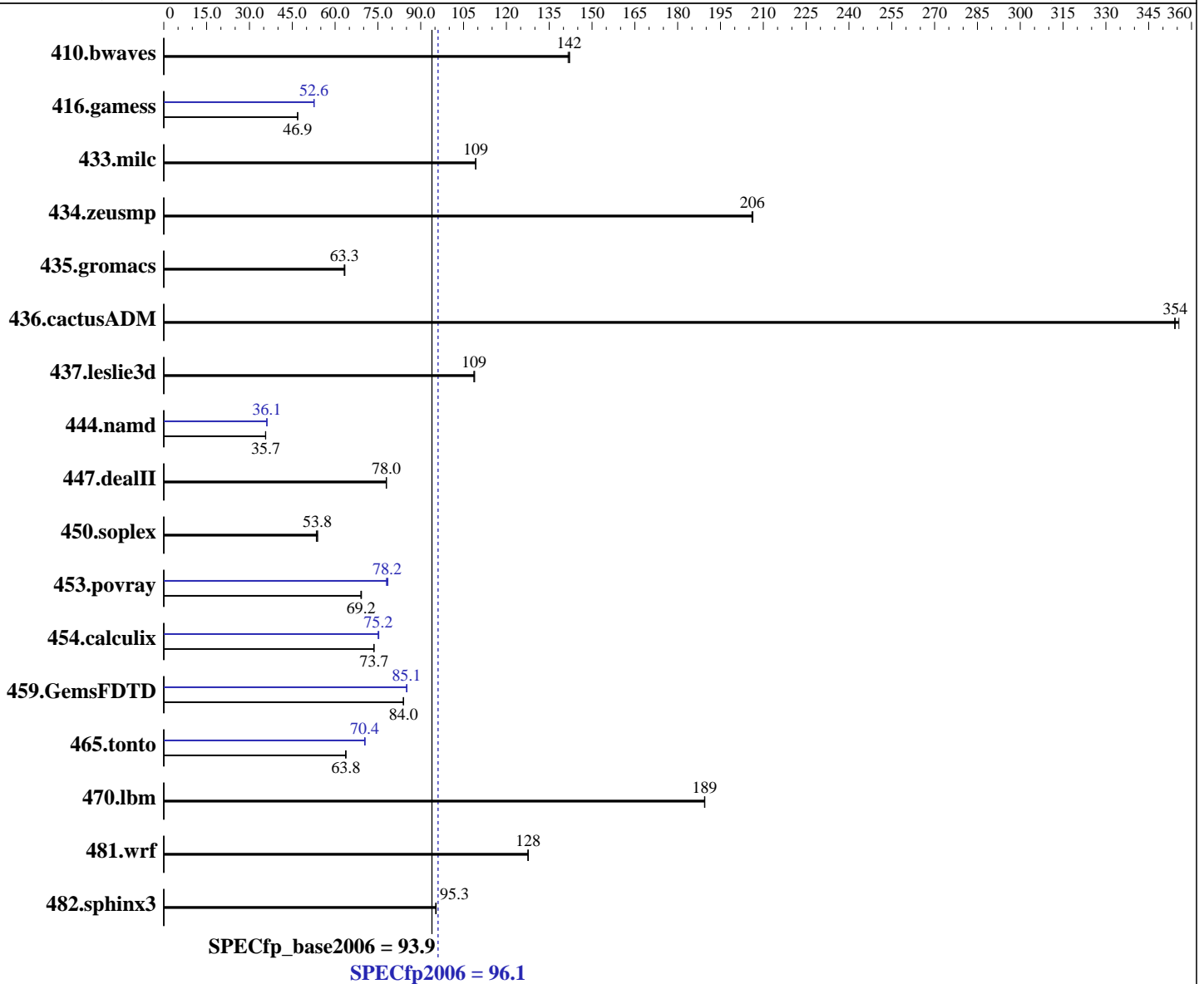
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Feb-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E3-1225 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
 Kernel 3.10.0-327.el7.x86_64
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: xfs

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx4 PC4-2133P-U)
Disk Subsystem: 1 x 400 GB SATA, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	95.6	142	96.0	142	<u>95.7</u>	<u>142</u>	95.6	142	96.0	142	<u>95.7</u>	<u>142</u>
416.gamess	418	46.9	<u>418</u>	<u>46.9</u>	418	46.9	372	52.7	<u>372</u>	<u>52.6</u>	372	52.6
433.milc	<u>84.0</u>	<u>109</u>	84.0	109	84.1	109	<u>84.0</u>	<u>109</u>	84.0	109	84.1	109
434.zeusmp	44.2	206	<u>44.1</u>	<u>206</u>	44.1	206	44.2	206	<u>44.1</u>	<u>206</u>	44.1	206
435.gromacs	113	63.5	113	63.2	<u>113</u>	<u>63.3</u>	113	63.5	113	63.2	<u>113</u>	<u>63.3</u>
436.cactusADM	33.6	356	<u>33.7</u>	<u>354</u>	33.8	354	33.6	356	<u>33.7</u>	<u>354</u>	33.8	354
437.leslie3d	86.3	109	<u>86.5</u>	<u>109</u>	86.5	109	86.3	109	<u>86.5</u>	<u>109</u>	86.5	109
444.namd	225	35.7	<u>225</u>	<u>35.7</u>	225	35.6	<u>222</u>	<u>36.1</u>	222	36.1	222	36.1
447.dealII	147	77.9	<u>147</u>	<u>78.0</u>	147	78.0	147	77.9	<u>147</u>	<u>78.0</u>	147	78.0
450.soplex	155	53.9	156	53.4	<u>155</u>	<u>53.8</u>	155	53.9	156	53.4	<u>155</u>	<u>53.8</u>
453.povray	77.1	69.0	<u>76.9</u>	<u>69.2</u>	76.8	69.3	68.2	78.0	67.8	78.5	<u>68.0</u>	<u>78.2</u>
454.calculix	112	73.7	<u>112</u>	<u>73.7</u>	112	73.5	110	75.1	110	75.3	<u>110</u>	<u>75.2</u>
459.GemsFDTD	126	83.9	<u>126</u>	<u>84.0</u>	126	84.0	125	85.0	125	85.2	<u>125</u>	<u>85.1</u>
465.tonto	154	63.8	<u>154</u>	<u>63.8</u>	155	63.7	140	70.5	<u>140</u>	<u>70.4</u>	140	70.3
470.lbm	72.5	190	72.5	189	<u>72.5</u>	<u>189</u>	72.5	190	72.5	189	<u>72.5</u>	<u>189</u>
481.wrf	<u>87.5</u>	<u>128</u>	87.6	128	87.5	128	<u>87.5</u>	<u>128</u>	87.6	128	87.5	128
482.sphinx3	<u>204</u>	<u>95.3</u>	205	95.2	204	95.4	<u>204</u>	<u>95.3</u>	205	95.2	204	95.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Mar 1 07:51:18 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E3-1225 v5 @ 3.30GHz

1 "physical id"s (chips)

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4

siblings : 4

physical 0: cores 0 1 2 3

cache size : 8192 KB

From /proc/meminfo

MemTotal: 32676372 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="7.2"

PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29

EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 23 17:04

SPEC is set to: /home/cpu2006

Filesystem Type Size Used Avail Use% Mounted on

/dev/mapper/rhel-home xfs 384G 18G 366G 5% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 5.11 09/11/2015

Memory:

4x SK Hynix HMA41GU7AFR8N-TF 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

OMP_NUM_THREADS = "4"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Submitted_by: "Bucek, James" <james.bucek@hpe.com>

Submitted: Mon Mar 7 18:24:48 EST 2016

Submission: cpu2006-20160307-39069.sub

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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