



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

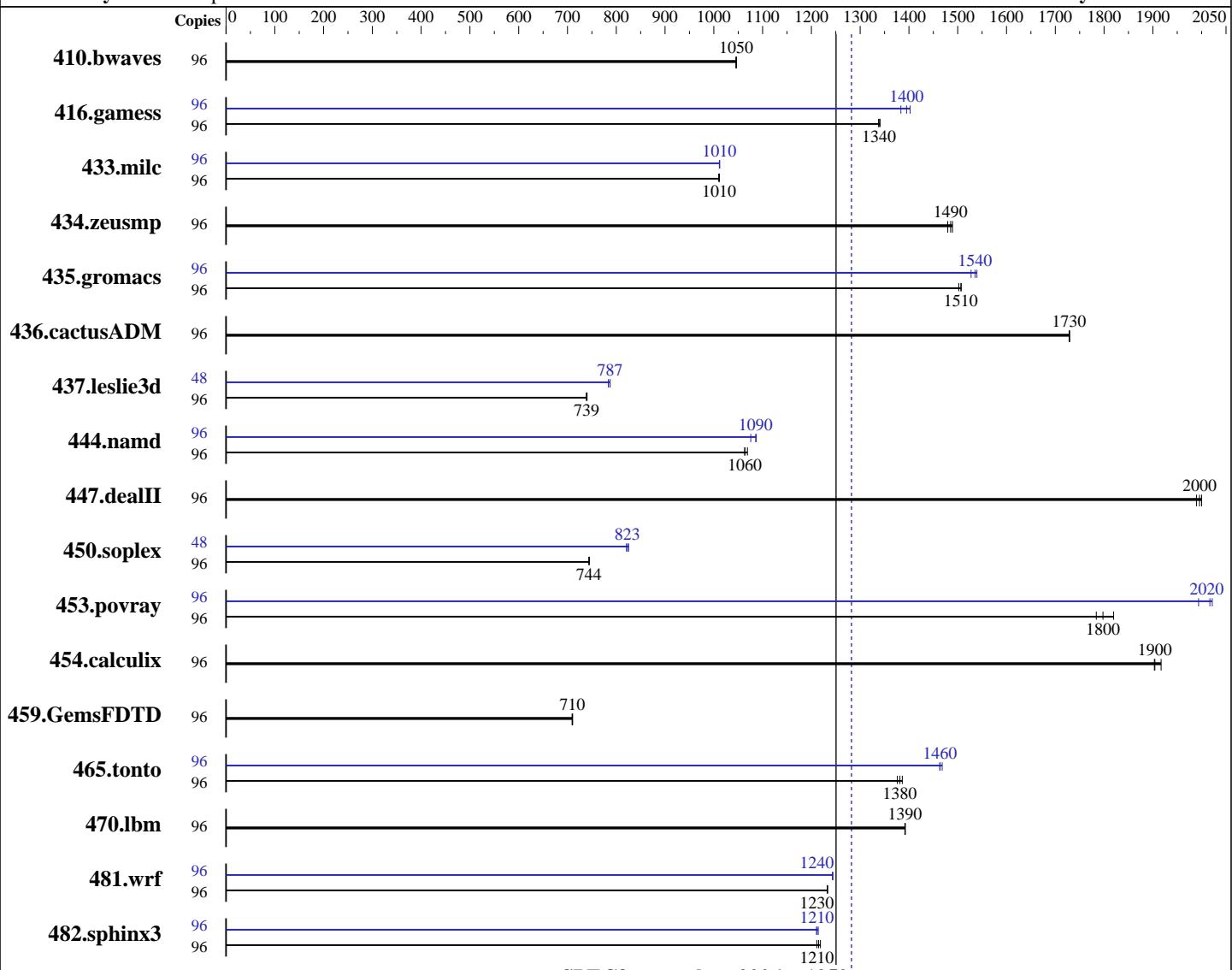
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014



SPECfp_rate_base2006 = 1250

SPECfp_rate2006 = 1280

Hardware

CPU Name: Intel Xeon E5-4640 v3
CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz
CPU MHz: 1900
FPU: Integrated
CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12, Kernel 3.12.28-4-default
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)
Disk Subsystem: 1 x 600 GB SATA II, SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	96	<u>1247</u>	<u>1050</u>	1248	1050	1247	1050	96	<u>1247</u>	<u>1050</u>	1248	1050	1247	1050
416.gamess	96	1405	1340	1402	1340	<u>1404</u>	<u>1340</u>	96	1359	1380	<u>1347</u>	<u>1400</u>	1340	1400
433.milc	96	<u>872</u>	<u>1010</u>	871	1010	873	1010	96	871	1010	<u>871</u>	<u>1010</u>	871	1010
434.zeusmp	96	590	1480	587	1490	<u>588</u>	<u>1490</u>	96	590	1480	587	1490	<u>588</u>	<u>1490</u>
435.gromacs	96	<u>455</u>	<u>1510</u>	456	1500	455	1510	96	449	1530	445	1540	<u>446</u>	<u>1540</u>
436.cactusADM	96	664	1730	<u>664</u>	<u>1730</u>	663	1730	96	664	1730	<u>664</u>	<u>1730</u>	663	1730
437.leslie3d	96	1219	740	<u>1221</u>	<u>739</u>	1222	739	48	573	787	576	784	<u>573</u>	<u>787</u>
444.namd	96	720	1070	<u>724</u>	<u>1060</u>	724	1060	96	<u>709</u>	<u>1090</u>	708	1090	716	1080
447.dealII	96	<u>550</u>	<u>2000</u>	549	2000	552	1990	96	<u>550</u>	<u>2000</u>	549	2000	552	1990
450.soplex	96	1075	745	<u>1076</u>	<u>744</u>	1076	744	48	485	825	488	821	<u>486</u>	<u>823</u>
453.povray	96	286	1780	281	1820	<u>284</u>	<u>1800</u>	96	<u>253</u>	<u>2020</u>	253	2020	256	1990
454.calculix	96	413	1920	<u>416</u>	<u>1900</u>	416	1900	96	413	1920	<u>416</u>	<u>1900</u>	416	1900
459.GemsFDTD	96	1436	709	<u>1434</u>	<u>710</u>	1433	711	96	1436	709	<u>1434</u>	<u>710</u>	1433	711
465.tonto	96	<u>684</u>	<u>1380</u>	681	1390	686	1380	96	643	1470	<u>645</u>	<u>1460</u>	646	1460
470.lbm	96	948	1390	<u>948</u>	<u>1390</u>	947	1390	96	948	1390	<u>948</u>	<u>1390</u>	947	1390
481.wrf	96	869	1230	<u>870</u>	<u>1230</u>	870	1230	96	862	1240	863	1240	<u>862</u>	<u>1240</u>
482.sphinx3	96	1535	1220	<u>1540</u>	<u>1210</u>	1545	1210	96	1541	1210	<u>1544</u>	<u>1210</u>	1546	1210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
COD Enable = Enable
Early Snoop = Disable

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Platform Notes (Continued)

Enforce POR = Disabled

Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1
running on 18-216 Sat Jun 20 02:45:18 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4640 v3 @ 1.90GHz
 4 "physical id"s (chips)
 96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 12
 siblings : 24
 physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
 physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
 physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13
 physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

From /proc/meminfo
MemTotal: 529330024 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*
SuSE-release:
 SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
This file is deprecated and will be removed in a future service pack or release.
Please check /etc/os-release for details about this release.
os-release:
 NAME="SLES"
 VERSION="12"
 VERSION_ID="12"
 PRETTY_NAME="SUSE Linux Enterprise Server 12"
 ID="sles"
 ANSI_COLOR="0;32"
 CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux 18-216 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
x86_64 x86_64 x86_64 GNU/Linux

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Platform Notes (Continued)

run-level 5 Jun 19 11:48

```
SPEC is set to: /home/SPEC2K6/SPEC2006-V12
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda3        ext4  529G  89G  439G  17% /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.00 05/28/2015

Memory:

```
16x NO DIMM NO DIMM
7x Samsung(data:13/51) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at
1866 MHz
3x Samsung(data:14/16) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at
1866 MHz
8x Samsung(data:14/17) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at
1866 MHz
6x Samsung(data:14/25) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at
1866 MHz
8x Samsung(data:14/26) M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at
1866 MHz
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014

Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

fort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2015

Hardware Availability: Jun-2015

Software Availability: Oct-2014

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Supermicro

SuperServer 2048U-RTR4
(X10QRH+, Intel Xeon E5-4640 v3)

SPECfp_rate2006 = 1280

SPECfp_rate_base2006 = 1250

CPU2006 license: 001176

Test date: Jun-2015

Test sponsor: Supermicro

Hardware Availability: Jun-2015

Tested by: Supermicro

Software Availability: Oct-2014

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revG.20141230.00.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Aug 6 13:25:34 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 6 August 2015.