



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint®_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3

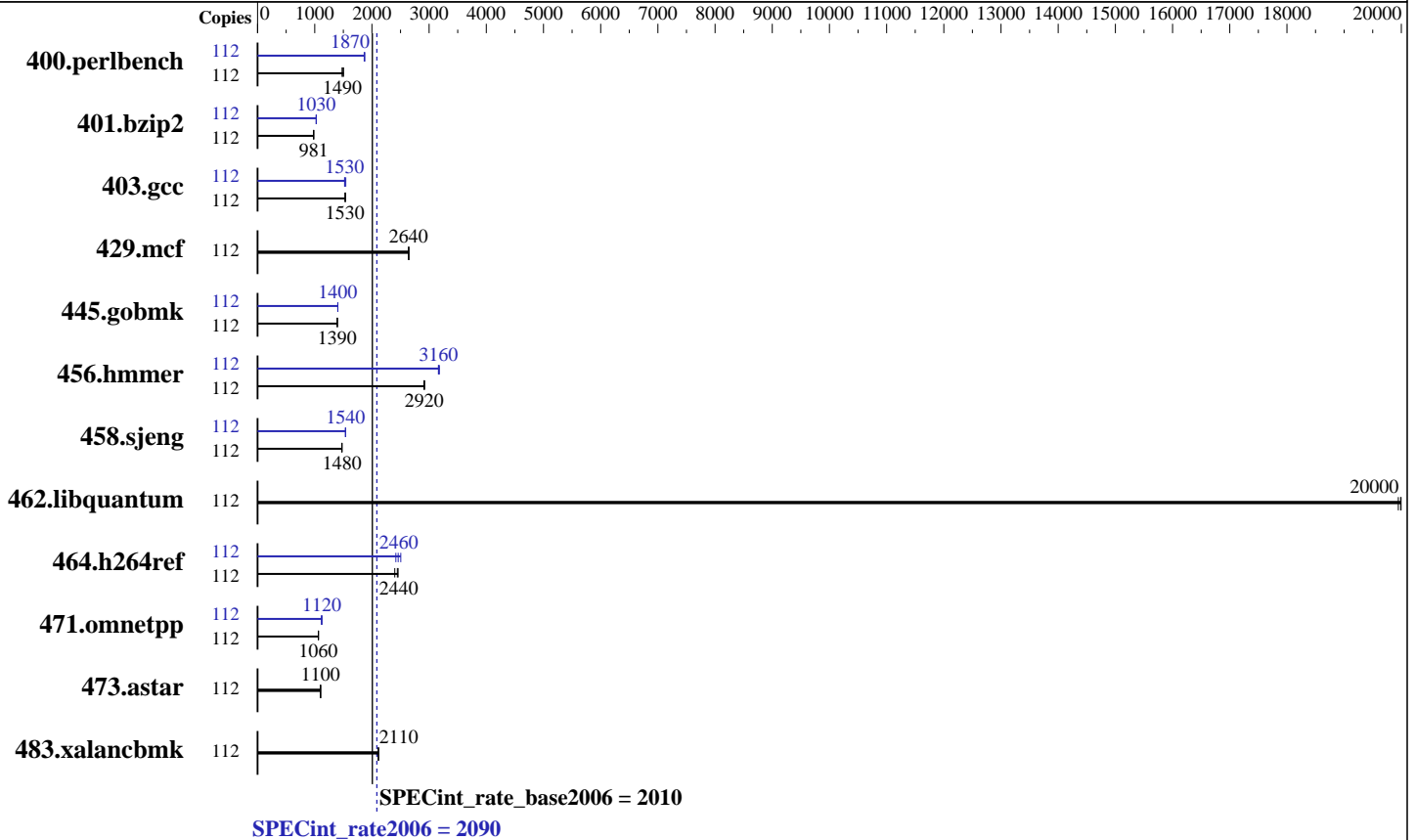
Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014



Hardware

CPU Name: Intel Xeon E5-4660 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.28-4-default
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3

Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	112	728	1500	740	1480	<u>737</u>	<u>1490</u>	112	<u>585</u>	<u>1870</u>	584	1870	587	1860		
401.bzip2	112	1100	982	<u>1102</u>	<u>981</u>	1102	980	112	1053	1030	<u>1053</u>	<u>1030</u>	1054	1030		
403.gcc	112	589	1530	<u>589</u>	<u>1530</u>	586	1540	112	<u>591</u>	<u>1530</u>	584	1540	593	1520		
429.mcf	112	386	2650	<u>387</u>	<u>2640</u>	387	2640	112	386	2650	<u>387</u>	<u>2640</u>	387	2640		
445.gobmk	112	<u>843</u>	<u>1390</u>	842	1400	843	1390	112	837	1400	838	1400	<u>837</u>	<u>1400</u>		
456.hammer	112	<u>358</u>	<u>2920</u>	359	2910	358	2920	112	330	3160	329	3180	<u>330</u>	<u>3160</u>		
458.sjeng	112	<u>919</u>	<u>1480</u>	919	1480	918	1480	112	<u>880</u>	<u>1540</u>	883	1540	880	1540		
462.libquantum	112	116	20000	<u>116</u>	<u>20000</u>	116	19900	112	116	20000	<u>116</u>	<u>20000</u>	116	19900		
464.h264ref	112	<u>1014</u>	<u>2440</u>	1033	2400	1009	2460	112	990	2500	1025	2420	<u>1008</u>	<u>2460</u>		
471.omnetpp	112	<u>658</u>	<u>1060</u>	658	1060	657	1060	112	622	1130	627	1120	<u>624</u>	<u>1120</u>		
473.astar	112	<u>713</u>	<u>1100</u>	711	1110	716	1100	112	<u>713</u>	<u>1100</u>	711	1110	716	1100		
483.xalanbmk	112	366	2110	366	2110	<u>366</u>	<u>2110</u>	112	366	2110	366	2110	<u>366</u>	<u>2110</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Energy/Performance Bias set to Maximum Performance
Collaborative Power Control set to Enabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Platform Notes (Continued)

Sysinfo program /root/cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-wzg5 Thu May 14 00:32:39 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4660 v3 @ 2.10GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      529171220 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12"
  VERSION_ID="12"
  PRETTY_NAME="SUSE Linux Enterprise Server 12"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12"
```

```
uname -a:
Linux linux-wzg5 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 13 00:39

SPEC is set to: /root/cpu2006

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdb4	xfs	300G	8.8G	292G	3%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I38 03/05/2015
Memory:
32x HP 752369-081 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006/libs/32:/root/cpu2006/libs/64:/root/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
C++ benchmarks:
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2015
Hardware Availability: Jun-2015
Software Availability: Oct-2014

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmcr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 2090

ProLiant BL660c Gen9
(2.10 GHz, Intel Xeon E5-4660 v3)

SPECint_rate_base2006 = 2010

CPU2006 license: 3

Test date: May-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2015

Tested by: Hewlett-Packard Company

Software Availability: Oct-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 2 13:49:24 2015 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 June 2015.