



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 61.2**

PRIMERGY BX2560 M1, Intel Xeon E5-2660 v3, 2.6 GHz

**SPECint\_base2006 = 58.6**

CPU2006 license: 19

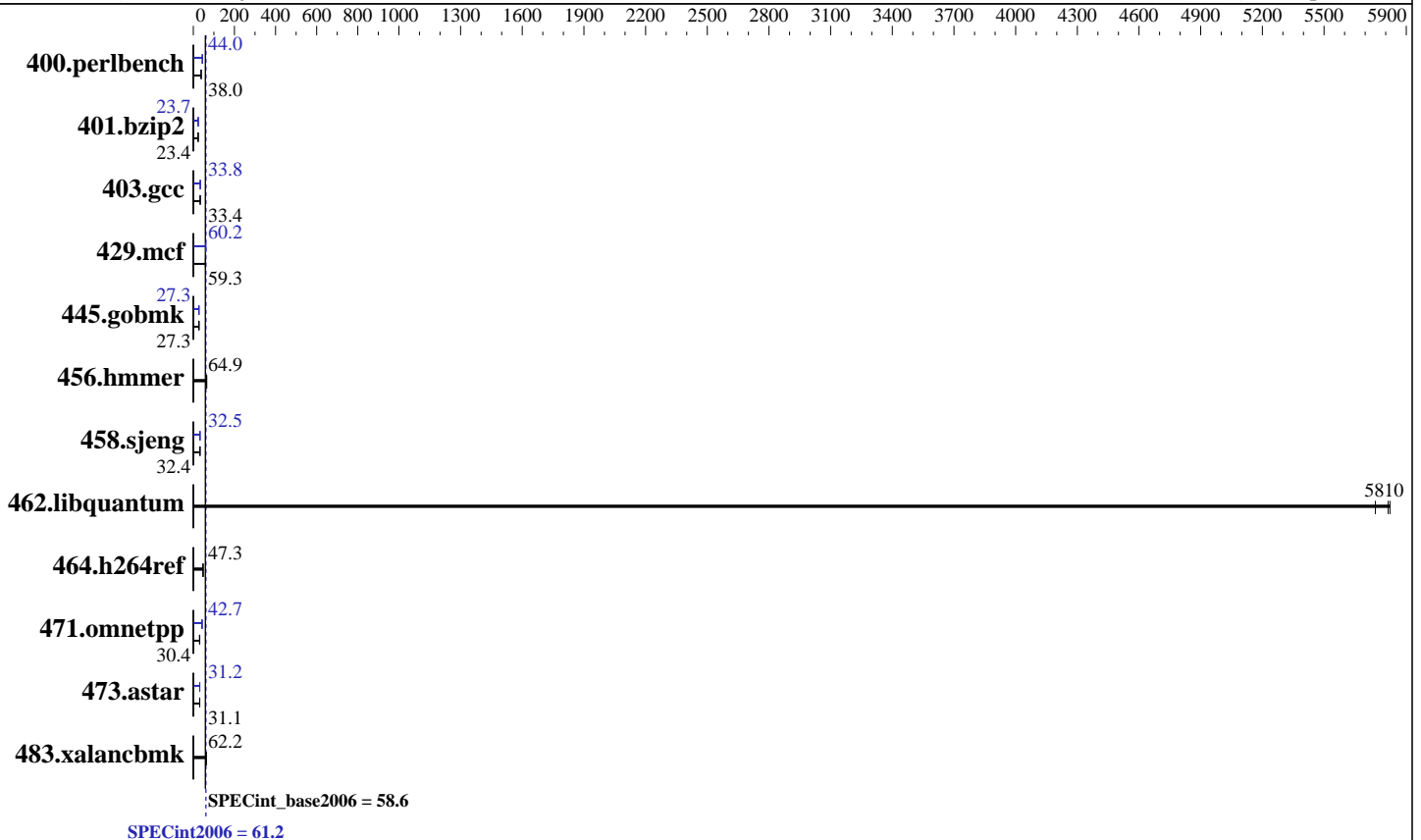
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2660 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.8.1.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	257	38.0	<u>257</u>	<u>38.0</u>	257	38.1	<u>222</u>	<u>44.0</u>	222	44.0	222	44.0
401.bzip2	412	23.4	413	23.4	<u>412</u>	<u>23.4</u>	<u>407</u>	<u>23.7</u>	407	23.7	407	23.7
403.gcc	<u>241</u>	<u>33.4</u>	241	33.4	242	33.3	239	33.7	<u>238</u>	<u>33.8</u>	238	33.9
429.mcf	154	59.0	<u>154</u>	<u>59.3</u>	152	60.0	<u>151</u>	<u>60.2</u>	152	60.1	149	61.1
445.gobmk	<u>384</u>	<u>27.3</u>	385	27.2	383	27.4	384	27.3	385	27.3	<u>385</u>	<u>27.3</u>
456.hammer	<u>144</u>	<u>64.9</u>	144	64.9	144	64.8	<u>144</u>	<u>64.9</u>	144	64.9	144	64.8
458.sjeng	374	32.3	<u>374</u>	<u>32.4</u>	373	32.4	373	32.4	<u>373</u>	<u>32.5</u>	372	32.5
462.libquantum	3.56	5820	3.60	5750	<u>3.57</u>	<u>5810</u>	3.56	5820	3.60	5750	<u>3.57</u>	<u>5810</u>
464.h264ref	<u>467</u>	<u>47.3</u>	467	47.4	468	47.3	<u>467</u>	<u>47.3</u>	467	47.4	468	47.3
471.omnetpp	206	30.4	<u>206</u>	<u>30.4</u>	199	31.3	146	42.7	148	42.2	<u>147</u>	<u>42.7</u>
473.astar	223	31.4	<u>226</u>	<u>31.1</u>	228	30.8	226	31.1	<u>225</u>	<u>31.2</u>	223	31.5
483.xalancbmk	111	62.4	111	62.1	<u>111</u>	<u>62.2</u>	111	62.4	111	62.1	<u>111</u>	<u>62.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance  
Utilization Profile = Unbalanced  
QPI snoop mode: Home Snoop  
COD Enable = Disabled, Early Snoop = Disabled  
CPU ClE Support = Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"  
OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

For information about Fujitsu please visit: <http://www.fujitsu.com>



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias`

456.hmmmer: `basepeak = yes`

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## Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Mar 10 16:04:00 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 March 2015.