



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant BL460c Gen9
(3.40 GHz, Intel Xeon E5-2643 v3)

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

SPECfp®2006 =

SPECfp_base2006 = NC

Test date: Nov-2014

Hardware Availability: Sep-2014

Software Availability: Sep-2014

SPEC has determined that this result is not in compliance with the SPEC OSG Guidelines for General Availability and the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a memory configuration that is not supported by Hewlett-Packard with the given processor configuration.

Hardware

CPU Name: Intel Xeon E5-2643 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2400 MHz PC4-2133P-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: g++ (GCC) 4.8.2 20140120 (Red Hat 4.8.2-16)
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	NC	NC										
416.gamess	NC	NC										
433.milc	NC	NC										
434.zeusmp	NC	NC										
435.gromacs	NC	NC										
436.cactusADM	NC	NC										
437.leslie3d	NC	NC										
444.namd	NC	NC										
447.dealII	NC	NC										
450.soplex	NC	NC										
453.povray	NC	NC										
454.calculix	NC	NC										
459.GemsFDTD	NC	NC										
465.tonto	NC	NC										
470.lbm	NC	NC										
481.wrf	NC	NC										
482.splices	NC	NC										

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```



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Platform Notes

BIOS Configuration:

HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core State set to C6 State
Minimum Processor Idle Power Package State set to No Package State
QPI Snoop Configuration set to Home Snoop
Thermal Configuration set to Maximum Cooling
Collaborative Power Control set to Disabled
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh
Intel Hyperthreading Option set to Disabled
Sysinfo program /cpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 ## e3fbb8667b5a285932ceab81e28219e1
running on A-BL460cGen9-VP7 Fri Nov 7 22:33:17 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v3 @ 3.40GHz
 2 "physical id"s (chips)
 2 "processor"
cores:siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 6
 siblings : 6
 physical 0: cores 0 1 2 3 4 5
 physical 1: cores 0 1 2 3 4 5
 cache size : 20480 KB

From /proc/meminfo
MemTotal: 263847380 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:

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Platform Notes (Continued)

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux A-BL460cGen9-VP2 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT
2014 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 7 12:29
```

```
SPEC is set to: /cpu/06
```

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhe_root ext4 310G 116G 179G 40% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately identified", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HP I36 08/26/2014
```

```
Memory:
16x H 752369-081 16 GB 2 rank 2133 MHz
```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel i5-4670K CPU + 16GB memory using RedHat EL 7.0

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
423.milc: -DSPEC_CPU_LP64
434.zerompi: -DSPEC_CPU_LP64
435.groniacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_BASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmark using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx: basepeak = yes

C++ benchmarks:

44.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

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Peak Optimization Flags (Continued)

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -loop-rep

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-falign-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-l-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags source by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-l-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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