



# SPEC<sup>®</sup> CFP2006 Result

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## Huawei

SPECfp<sup>®</sup>2006 = **93.0**

### Huawei CH221 (Intel Xeon E5-2660 V2)

SPECfp\_base2006 = **89.1**

CPU2006 license: 3175

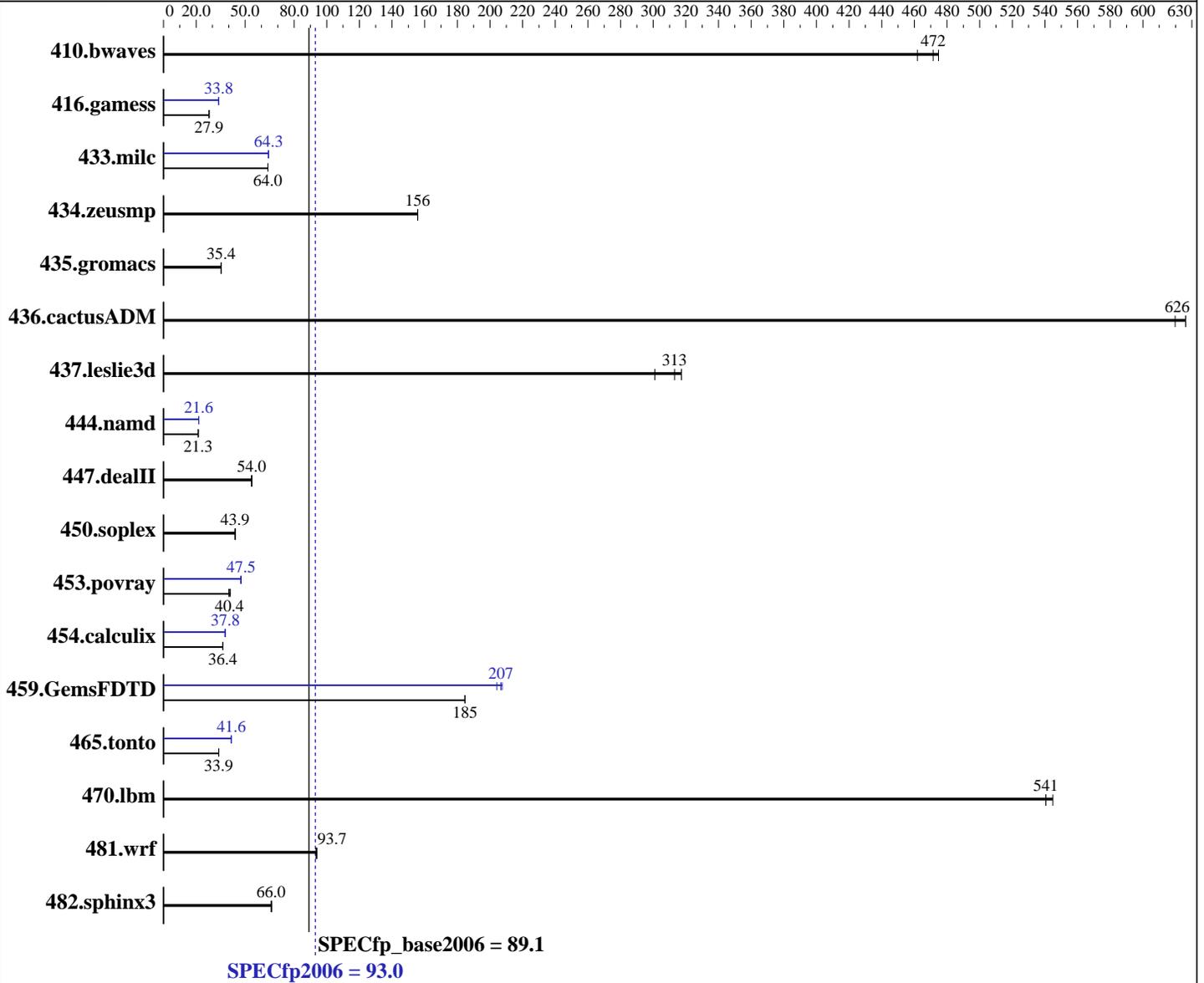
Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



**Hardware**

CPU Name: Intel Xeon E5-2660 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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**Software**

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	28.6	475	29.4	462	<b><u>28.8</u></b>	<b><u>472</u></b>	28.6	475	29.4	462	<b><u>28.8</u></b>	<b><u>472</u></b>
416.gamess	<b><u>702</u></b>	<b><u>27.9</u></b>	702	27.9	701	27.9	579	33.8	579	33.8	<b><u>579</u></b>	<b><u>33.8</u></b>
433.milc	<b><u>144</u></b>	<b><u>64.0</u></b>	144	64.0	144	64.0	143	64.3	<b><u>143</u></b>	<b><u>64.3</u></b>	143	64.3
434.zeusmp	58.4	156	<b><u>58.4</u></b>	<b><u>156</u></b>	58.4	156	58.4	156	<b><u>58.4</u></b>	<b><u>156</u></b>	58.4	156
435.gromacs	<b><u>202</u></b>	<b><u>35.4</u></b>	202	35.4	202	35.3	<b><u>202</u></b>	<b><u>35.4</u></b>	202	35.4	202	35.3
436.cactusADM	19.3	620	19.1	626	<b><u>19.1</u></b>	<b><u>626</u></b>	19.3	620	19.1	626	<b><u>19.1</u></b>	<b><u>626</u></b>
437.leslie3d	<b><u>30.0</u></b>	<b><u>313</u></b>	29.6	317	31.2	301	<b><u>30.0</u></b>	<b><u>313</u></b>	29.6	317	31.2	301
444.namd	<b><u>377</u></b>	<b><u>21.3</u></b>	377	21.3	377	21.3	371	21.6	<b><u>371</u></b>	<b><u>21.6</u></b>	371	21.6
447.dealII	212	54.0	212	54.1	<b><u>212</u></b>	<b><u>54.0</u></b>	212	54.0	212	54.1	<b><u>212</u></b>	<b><u>54.0</u></b>
450.soplex	<b><u>190</u></b>	<b><u>43.9</u></b>	190	43.8	189	44.1	<b><u>190</u></b>	<b><u>43.9</u></b>	190	43.8	189	44.1
453.povray	130	40.9	<b><u>132</u></b>	<b><u>40.4</u></b>	133	40.0	112	47.6	112	47.3	<b><u>112</u></b>	<b><u>47.5</u></b>
454.calculix	<b><u>227</u></b>	<b><u>36.4</u></b>	227	36.4	227	36.3	<b><u>218</u></b>	<b><u>37.8</u></b>	218	37.8	218	37.8
459.GemsFDTD	<b><u>57.5</u></b>	<b><u>185</u></b>	57.5	185	57.5	185	<b><u>51.3</u></b>	<b><u>207</u></b>	51.9	204	51.1	207
465.tonto	<b><u>291</u></b>	<b><u>33.9</u></b>	291	33.9	291	33.9	<b><u>237</u></b>	<b><u>41.6</u></b>	238	41.4	236	41.6
470.lbm	25.4	541	25.2	545	<b><u>25.4</u></b>	<b><u>541</u></b>	25.4	541	25.2	545	<b><u>25.4</u></b>	<b><u>541</u></b>
481.wrf	119	94.0	<b><u>119</u></b>	<b><u>93.7</u></b>	120	93.3	119	94.0	<b><u>119</u></b>	<b><u>93.7</u></b>	120	93.3
482.sphinx3	296	65.9	294	66.3	<b><u>295</u></b>	<b><u>66.0</u></b>	296	65.9	294	66.3	<b><u>295</u></b>	<b><u>66.0</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
 Set Hyper-Threading to Disabled  
 Sysinfo program /spec/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on localhost.localdomain Sun Sep 21 18:37:49 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Sep-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013

## Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v2 @ 2.20GHz
 2 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 19 18:13

SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  265G  15G  237G   6% /

Additional information from dmidecode:
Memory:
 16x Micron 36JSF2G72PZ-1G9E1 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

```

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "20"

```

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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<b>Test sponsor:</b> Huawei	<b>Hardware Availability:</b> Sep-2013
<b>Tested by:</b> Huawei	<b>Software Availability:</b> Nov-2013

## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120912.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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