



# SPEC® CINT2006 Result

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## ACTION S.A.

ACTINA SOLAR 220 X5 (Intel Xeon E5-2670 v2, 2.50 GHz)

SPECint®\_rate2006 = 818

SPECint\_rate\_base2006 = 791

CPU2006 license: 9008

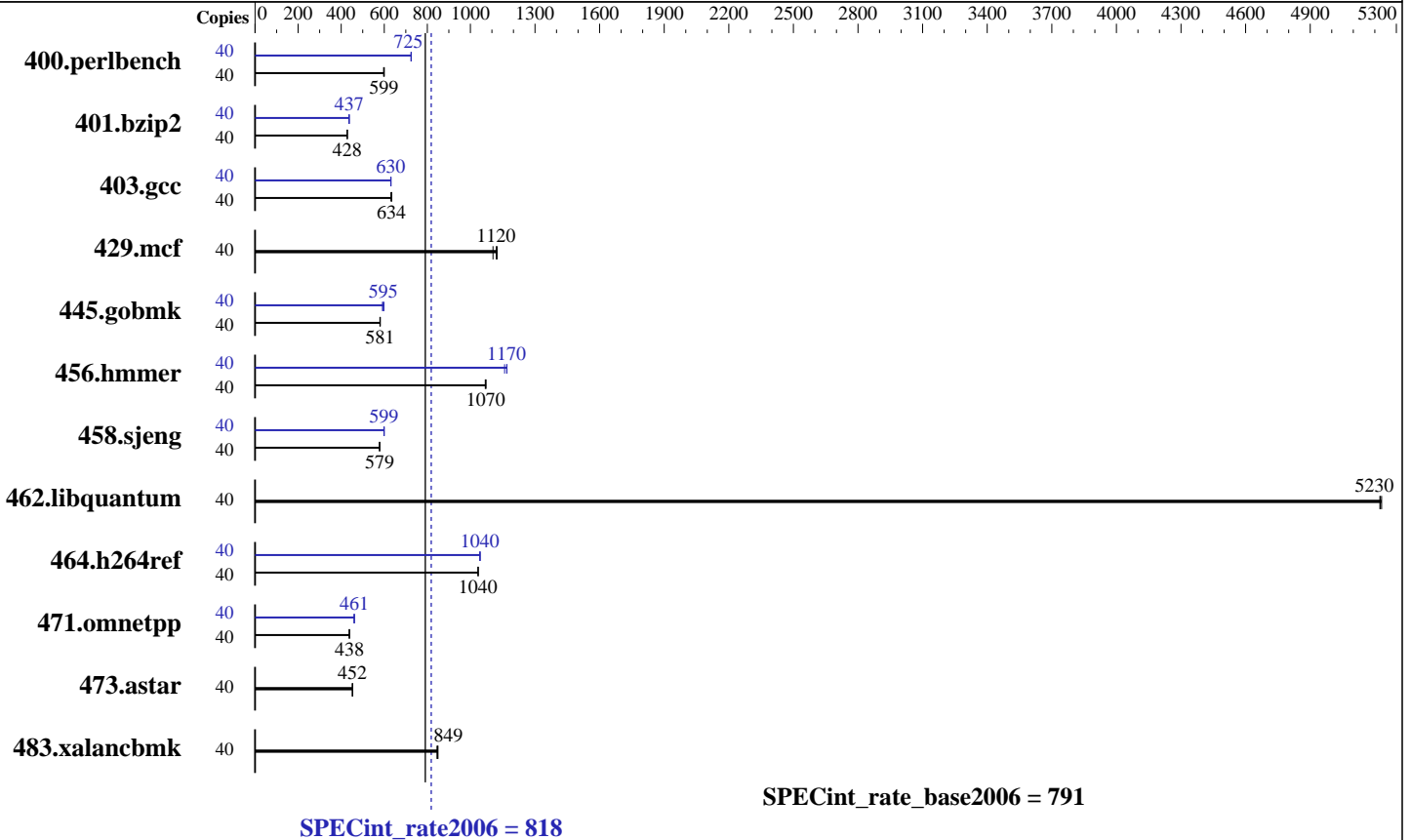
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Mar-2014

Hardware Availability: Oct-2013

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2670 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 x 240 GB SATA II SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.11.1.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	40	652	600	<b>652</b>	<b>599</b>	655	597	40	539	725	<b>539</b>	<b>725</b>	539	725		
401.bzip2	40	901	428	902	428	<b>902</b>	<b>428</b>	40	883	437	884	437	<b>884</b>	<b>437</b>		
403.gcc	40	<b>508</b>	<b>634</b>	510	631	507	635	40	511	630	<b>511</b>	<b>630</b>	510	632		
429.mcf	40	325	1120	<b>326</b>	<b>1120</b>	330	1100	40	325	1120	<b>326</b>	<b>1120</b>	330	1100		
445.gobmk	40	724	580	<b>722</b>	<b>581</b>	722	581	40	701	598	<b>705</b>	<b>595</b>	709	592		
456.hammer	40	348	1070	349	1070	<b>348</b>	<b>1070</b>	40	322	1160	<b>319</b>	<b>1170</b>	319	1170		
458.sjeng	40	836	579	837	578	<b>837</b>	<b>579</b>	40	809	598	807	600	<b>808</b>	<b>599</b>		
462.libquantum	40	158	5230	<b>159</b>	<b>5230</b>	159	5220	40	158	5230	<b>159</b>	<b>5230</b>	159	5220		
464.h264ref	40	<b>855</b>	<b>1040</b>	854	1040	855	1040	40	<b>847</b>	<b>1040</b>	847	1050	848	1040		
471.omnetpp	40	571	438	571	438	<b>571</b>	<b>438</b>	40	<b>543</b>	<b>461</b>	542	461	543	460		
473.astar	40	621	452	<b>621</b>	<b>452</b>	620	453	40	621	452	<b>621</b>	<b>452</b>	620	453		
483.xalancbmk	40	325	849	<b>325</b>	<b>849</b>	327	845	40	325	849	<b>325</b>	<b>849</b>	327	845		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### Bios Settings:

System Acoustic and Performance Configuration = Performance  
Select Memory RAS Configuration = Maximum Performance  
Intel(R) Turbo Boost Technology = Enabled  
Processor C3 = Disabled  
Processor C6 = Disabled

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Tue Mar 25 09:20:33 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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### Platform Notes (Continued)

```

model name : Intel(R) Xeon(R) CPU E5-2670 v2 @ 2.50GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

```

```

From /proc/meminfo
MemTotal:      132124488 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

```

```

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

```

uname -a:
Linux localhost.localdomain 2.6.32-358.11.1.el6.x86_64 #1 SMP Tue Nov 19
17:43:04 CET 2013 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 25 09:18

```

SPEC is set to: /cpu2006.1.2
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdal       ext4      193G      46G  137G  26% /

```

```

Additional information from dmidecode:
BIOS American Megatrends Inc. 3.0a 02/13/2014
Memory:
 16x   8 GB
 14x Hynix Semiconducto HMT31GR7EFR4C 8 GB 1866 MHz 2 rank
 2x Hynix Semiconductor HMT31GR7EFR4C- 8 GB 1866 MHz 2 rank

```

```

(End of data from sysinfo program)
dmidecode does not properly detect memory modules
16 modules of 8 GB were used to run the test (128 GB total)

```

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
Binaries compiled on a system with 2x Xeon E5-2650 v2 chips + 256 GB memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap

## Base Other Flags

C benchmarks:  
  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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**Tested by:** ACTION S.A.

**Software Availability:** Sep-2013

## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/cpu2006.1.2/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-apr-2014-For-Intel-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-apr-2014-For-Intel-Platform.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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