



# SPEC® CINT2006 Result

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## IBM Corporation

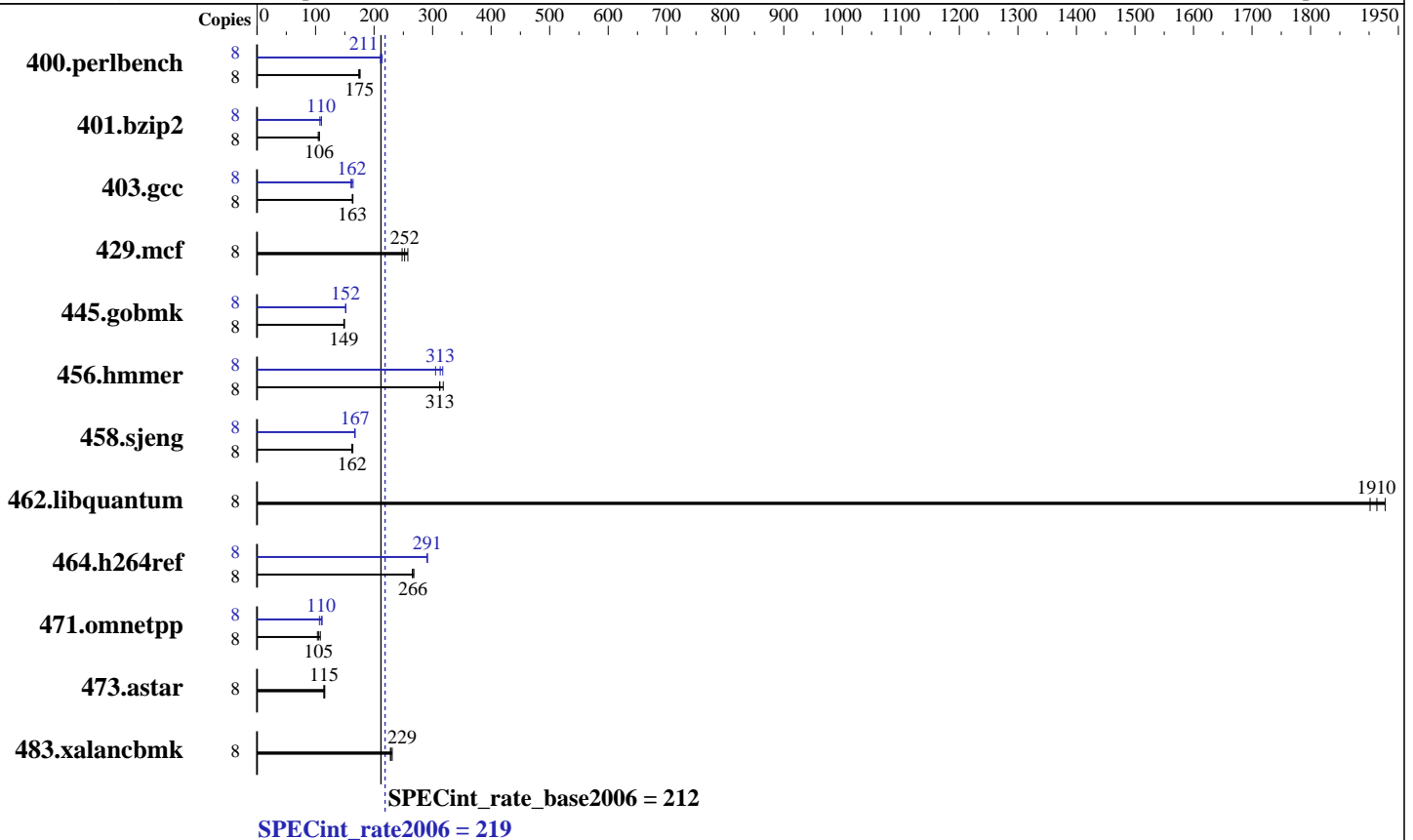
IBM System x3250 M5  
(Intel Xeon E3-1270 v3, 3.50 GHz)

SPECint®\_rate2006 = 219

SPECint\_rate\_base2006 = 212

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Feb-2014  
Hardware Availability: Dec-2013  
Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E3-1270 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	444	176	<b>446</b>	<b>175</b>	449	174	8	<b>370</b>	<b>211</b>	370	211	365	214
401.bzip2	8	<b>727</b>	<b>106</b>	725	106	738	105	8	<b>703</b>	<b>110</b>	719	107	702	110
403.gcc	8	393	164	395	163	<b>395</b>	<b>163</b>	8	401	161	392	164	<b>398</b>	<b>162</b>
429.mcf	8	<b>289</b>	<b>252</b>	294	248	283	258	8	<b>289</b>	<b>252</b>	294	248	283	258
445.gobmk	8	566	148	561	150	<b>564</b>	<b>149</b>	8	552	152	<b>554</b>	<b>152</b>	554	151
456.hammer	8	235	318	<b>239</b>	<b>313</b>	239	312	8	<b>238</b>	<b>313</b>	245	305	235	317
458.sjeng	8	<b>596</b>	<b>162</b>	591	164	596	162	8	578	167	<b>578</b>	<b>167</b>	580	167
462.libquantum	8	<b>86.6</b>	<b>1910</b>	86.0	1930	87.2	1900	8	<b>86.6</b>	<b>1910</b>	86.0	1930	87.2	1900
464.h264ref	8	<b>665</b>	<b>266</b>	661	268	667	265	8	<b>609</b>	<b>291</b>	609	291	608	291
471.omnetpp	8	485	103	462	108	<b>474</b>	<b>105</b>	8	<b>455</b>	<b>110</b>	449	111	469	107
473.astar	8	485	116	491	114	<b>490</b>	<b>115</b>	8	485	116	491	114	<b>490</b>	<b>115</b>
483.xalancbmk	8	242	228	<b>241</b>	<b>229</b>	239	231	8	242	228	<b>241</b>	<b>229</b>	239	231

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-new/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on x3250M5 Tue Feb 18 15:28:56 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E3-1270 v3 @ 3.50GHz  
1 "physical id"s (chips)  
8 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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### Platform Notes (Continued)

```
cpu cores : 4
siblings  : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16299396 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3250M5 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Feb 18 15:27
```

```
SPEC is set to: /home/SPECcpu-new
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3250m5-lv_home
ext4            852G    14G  795G   2% /home
```

```
Additional information from dmidecode:
BIOS IBM -[JUE1090US-1.00]- 11/20/2013
Memory:
2x Micron 18KSF1G72AZ-1G6E1 8 GB 1600 MHz 2 rank
2x Not Specified Not Specified
```

(End of data from sysinfo program)  
"Not Specified" memory information from dmidecode indicates unused DIMM slots.

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu-new/libs/32:/home/SPECcpu-new/libs/64:/home/SPECcpu-new/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32  
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
429.mcf: basepeak = yes  
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3  
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

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## Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-HSW-A.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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