



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 82.4

PowerEdge R520 (Intel Xeon E5-2450 v2, 2.50 GHz)

SPECfp\_base2006 = 78.1

CPU2006 license: 55

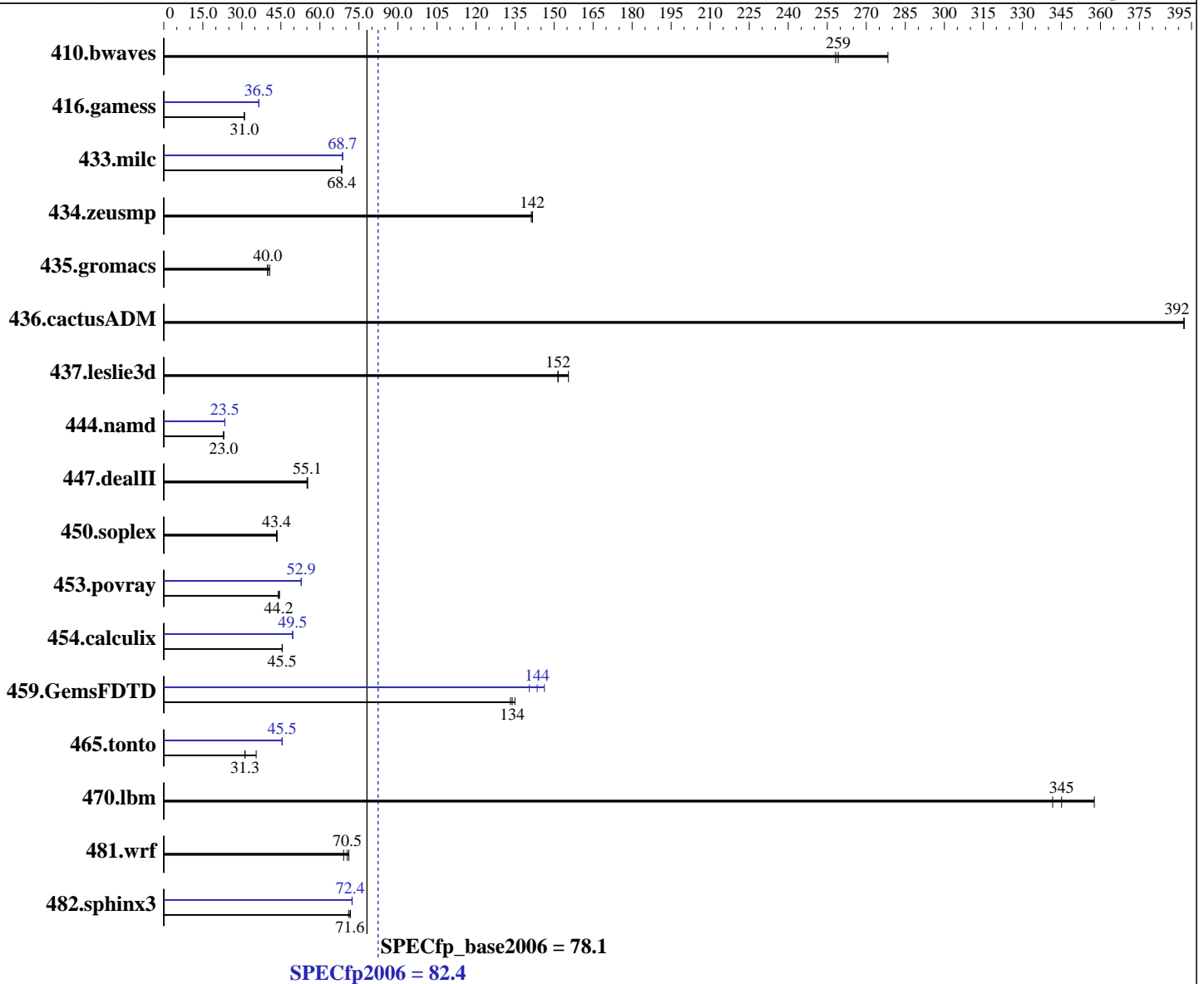
Test date: Oct-2013

Test sponsor: Dell Inc.

Hardware Availability: Jan-2014

Tested by: Dell Inc.

Software Availability: Sep-2013



**Hardware**

CPU Name: Intel Xeon E5-2450 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

**Software**

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 3.0.76-0.11-default  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 192 GB (12 x 16 GB 2Rx4 PC3L-12800R-11, ECC)  
Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>52.4</u>	<u>259</u>	52.6	258	48.8	278	<u>52.4</u>	<u>259</u>	52.6	258	48.8	278
416.gamess	633	30.9	<u>632</u>	<u>31.0</u>	632	31.0	536	36.5	<u>536</u>	<u>36.5</u>	536	36.6
433.milc	<u>134</u>	<u>68.4</u>	134	68.4	134	68.5	134	68.7	<u>134</u>	<u>68.7</u>	134	68.7
434.zeusmp	64.4	141	64.2	142	<u>64.2</u>	<u>142</u>	64.4	141	64.2	142	<u>64.2</u>	<u>142</u>
435.gromacs	175	40.7	<u>179</u>	<u>40.0</u>	179	39.9	175	40.7	<u>179</u>	<u>40.0</u>	179	39.9
436.cactusADM	<u>30.5</u>	<u>392</u>	30.5	392	30.5	392	<u>30.5</u>	<u>392</u>	30.5	392	30.5	392
437.leslie3d	62.0	152	<u>62.0</u>	<u>152</u>	60.4	156	62.0	152	<u>62.0</u>	<u>152</u>	60.4	156
444.namd	349	23.0	349	23.0	<u>349</u>	<u>23.0</u>	<u>342</u>	<u>23.5</u>	342	23.5	342	23.5
447.dealII	208	55.1	207	55.3	<u>207</u>	<u>55.1</u>	208	55.1	207	55.3	<u>207</u>	<u>55.1</u>
450.soplex	<u>192</u>	<u>43.4</u>	191	43.6	192	43.4	<u>192</u>	<u>43.4</u>	191	43.6	192	43.4
453.povray	121	44.0	120	44.5	<u>120</u>	<u>44.2</u>	<u>101</u>	<u>52.9</u>	101	52.9	101	52.8
454.calculix	181	45.6	<u>181</u>	<u>45.5</u>	181	45.5	167	49.5	<u>167</u>	<u>49.5</u>	167	49.5
459.GemsFDTD	79.6	133	78.6	135	<u>79.2</u>	<u>134</u>	75.5	140	<u>73.9</u>	<u>144</u>	72.5	146
465.tonto	<u>315</u>	<u>31.3</u>	316	31.2	277	35.5	216	45.5	<u>216</u>	<u>45.5</u>	216	45.5
470.lbm	38.4	358	40.2	342	<u>39.8</u>	<u>345</u>	38.4	358	40.2	342	<u>39.8</u>	<u>345</u>
481.wrf	<u>158</u>	<u>70.5</u>	162	69.1	157	71.1	<u>158</u>	<u>70.5</u>	162	69.1	157	71.1
482.sphinx3	<u>272</u>	<u>71.6</u>	274	71.0	272	71.7	269	72.3	<u>269</u>	<u>72.4</u>	269	72.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Virtualization Technology disabled  
Execute Disable disabled  
Logical Processor disabled  
System Profile set to Performance  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on linux Thu Oct 31 18:39:58 2013

Continued on next page



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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2450 v2 @ 2.50GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 8
  siblings     : 8
  physical 0   : cores 0 1 2 3 4 5 6 7
  physical 1   : cores 0 1 2 3 4 5 6 7
cache size    : 20480 KB

```

```

From /proc/meminfo
MemTotal:      198410440 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Oct 31 12:45 last=S

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  267G  9.4G  257G   4% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.21 09/23/2013
Memory:
 1x 00AD00B300AD HMT42GR7MFR4A-PB 16 GB 1600 MHz
 11x 00AD04B300AD HMT42GR7AFR4A-PB 16 GB 1600 MHz

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "16"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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