



# SPEC® CINT2006 Result

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## ACTION S.A.

SPECint®\_rate2006 = 223

ACTINA SOLAR 200 S5 (Intel Xeon E5-2630)

SPECint\_rate\_base2006 = 214

CPU2006 license: 9008

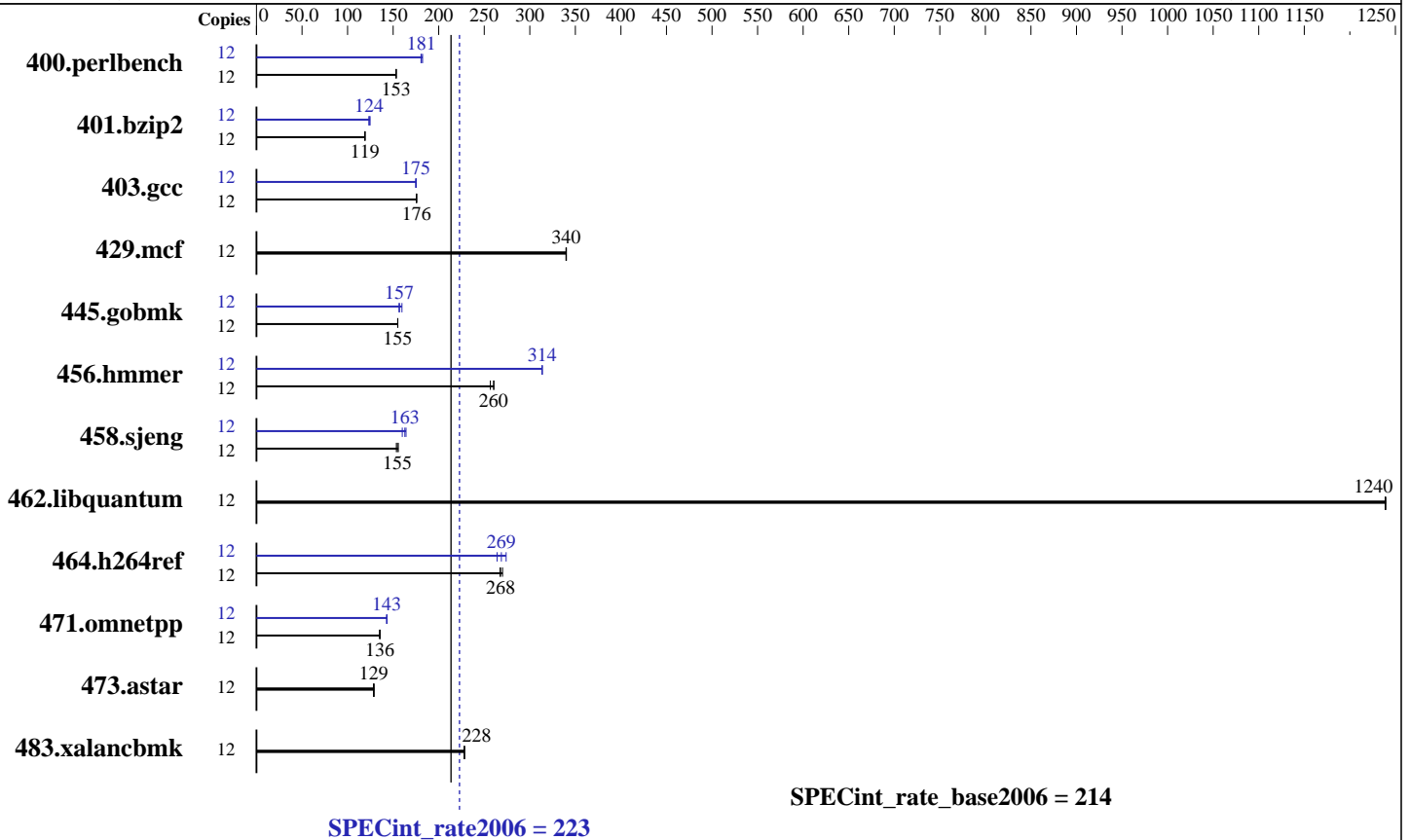
Test date: Oct-2012

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2012

Tested by: ACTION S.A.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2630  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz and CL9)  
 Disk Subsystem: 1 x 2 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	764	153	766	153	<b>764</b>	<b>153</b>	12	<b>647</b>	<b>181</b>	649	181	643	182
401.bzip2	12	974	119	<b>972</b>	<b>119</b>	971	119	12	939	123	929	125	<b>935</b>	<b>124</b>
403.gcc	12	549	176	551	175	<b>550</b>	<b>176</b>	12	<b>552</b>	<b>175</b>	551	175	553	175
429.mcf	12	<b>322</b>	<b>340</b>	322	340	322	340	12	<b>322</b>	<b>340</b>	322	340	322	340
445.gobmk	12	811	155	<b>812</b>	<b>155</b>	812	155	12	788	160	<b>802</b>	<b>157</b>	805	156
456.hammer	12	436	257	<b>430</b>	<b>260</b>	430	261	12	<b>357</b>	<b>314</b>	357	313	357	314
458.sjeng	12	946	154	933	156	<b>939</b>	<b>155</b>	12	<b>892</b>	<b>163</b>	885	164	907	160
462.libquantum	12	<b>201</b>	<b>1240</b>	201	1240	201	1240	12	<b>201</b>	<b>1240</b>	201	1240	201	1240
464.h264ref	12	983	270	993	267	<b>991</b>	<b>268</b>	12	<b>988</b>	<b>269</b>	1006	264	969	274
471.omnetpp	12	<b>553</b>	<b>136</b>	555	135	553	136	12	525	143	523	143	<b>524</b>	<b>143</b>
473.astar	12	652	129	<b>655</b>	<b>129</b>	656	128	12	652	129	<b>655</b>	<b>129</b>	656	128
483.xalancbmk	12	363	228	363	228	<b>363</b>	<b>228</b>	12	363	228	363	228	<b>363</b>	<b>228</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on linux-k141 Mon Sep 10 01:14:57 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2630 0 @ 2.30GHz  
1 "physical id"s (chips)  
12 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5  
cache size : 15360 KB

From /proc/meminfo  
MemTotal: 65951348 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
SUSE Linux Enterprise Server 11 (x86\_64)

From /etc/\*release\* /etc/\*version\*  
SuSE-release:  
SUSE Linux Enterprise Server 11 (x86\_64)  
VERSION = 11  
PATCHLEVEL = 2

uname -a:  
Linux linux-k141 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012  
(d73692b) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Sep 10 01:13 last=S

SPEC is set to: /cpu2006.1.2  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 ext3 1.8T 7.4G 1.8T 1% /

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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