



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

SPECfp<sup>®</sup>\_rate2006 = 296

IBM System x3630 M4 (Intel Xeon E5-2430L)

SPECfp\_rate\_base2006 = 287

CPU2006 license: 11

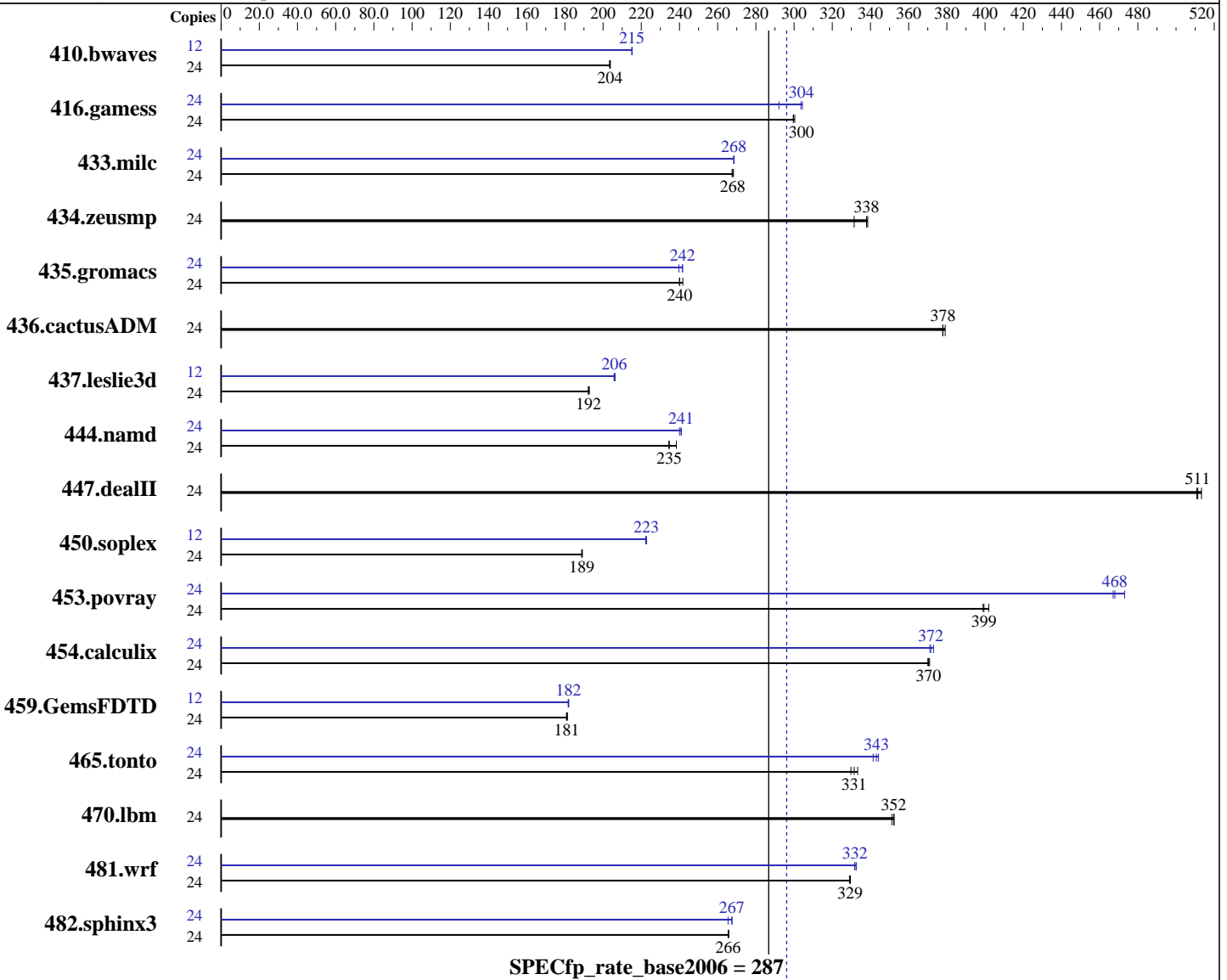
Test date: Jul-2012

Test sponsor: IBM Corporation

Hardware Availability: May-2012

Tested by: IBM Corporation

Software Availability: Dec-2011



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### Hardware

CPU Name: Intel Xeon E5-2430L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	24	1601	204	<b><u>1601</u></b>	<b><u>204</u></b>	1604	203	12	758	215	758	215	<b><u>758</u></b>	<b><u>215</u></b>		
416.gamess	24	<b><u>1568</u></b>	<b><u>300</u></b>	1569	300	1565	300	24	1609	292	1544	304	<b><u>1548</u></b>	<b><u>304</u></b>		
433.milc	24	824	268	<b><u>822</u></b>	<b><u>268</u></b>	821	268	24	821	268	<b><u>821</u></b>	<b><u>268</u></b>	820	269		
434.zeusmp	24	<b><u>646</u></b>	<b><u>338</u></b>	659	331	645	339	24	<b><u>646</u></b>	<b><u>338</u></b>	659	331	645	339		
435.gromacs	24	709	242	714	240	<b><u>714</u></b>	<b><u>240</u></b>	24	709	242	<b><u>709</u></b>	<b><u>242</u></b>	715	240		
436.cactusADM	24	<b><u>759</u></b>	<b><u>378</u></b>	759	378	757	379	24	<b><u>759</u></b>	<b><u>378</u></b>	759	378	757	379		
437.leslie3d	24	<b><u>1173</u></b>	<b><u>192</u></b>	1170	193	1173	192	12	547	206	548	206	<b><u>548</u></b>	<b><u>206</u></b>		
444.namd	24	<b><u>820</u></b>	<b><u>235</u></b>	821	234	807	238	24	802	240	<b><u>799</u></b>	<b><u>241</u></b>	799	241		
447.dealII	24	537	511	535	513	<b><u>537</u></b>	<b><u>511</u></b>	24	537	511	535	513	<b><u>537</u></b>	<b><u>511</u></b>		
450.soplex	24	1058	189	<b><u>1060</u></b>	<b><u>189</u></b>	1060	189	12	450	222	<b><u>450</u></b>	<b><u>223</u></b>	449	223		
453.povray	24	320	399	318	402	<b><u>320</u></b>	<b><u>399</u></b>	24	273	467	270	473	<b><u>273</u></b>	<b><u>468</u></b>		
454.calculix	24	<b><u>535</u></b>	<b><u>370</u></b>	535	370	534	371	24	<b><u>533</u></b>	<b><u>372</u></b>	534	371	531	373		
459.GemsFDTD	24	1404	181	1408	181	<b><u>1407</u></b>	<b><u>181</u></b>	12	701	182	699	182	<b><u>700</u></b>	<b><u>182</u></b>		
465.tonto	24	709	333	<b><u>712</u></b>	<b><u>331</u></b>	716	330	24	692	341	<b><u>688</u></b>	<b><u>343</u></b>	686	344		
470.lbm	24	936	352	<b><u>936</u></b>	<b><u>352</u></b>	939	351	24	936	352	<b><u>936</u></b>	<b><u>352</u></b>	939	351		
481.wrf	24	814	329	<b><u>814</u></b>	<b><u>329</u></b>	815	329	24	<b><u>808</u></b>	<b><u>332</u></b>	806	333	808	332		
482.sphinx3	24	<b><u>1762</u></b>	<b><u>266</u></b>	1760	266	1762	265	24	<b><u>1749</u></b>	<b><u>267</u></b>	1762	265	1749	268		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Zone reclaim mode enabled with:  
 echo 1 > /proc/sys/vm/zone\_reclaim\_mode



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Test date: Jul-2012  
Hardware Availability: May-2012  
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## Platform Notes

BIOS setting:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on x3630m4-rhel62 Thu Jul 12 07:37:17 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2430L 0 @ 2.00GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 6
  siblings     : 12
  physical 0:  cores 0 1 2 3 4 5
  physical 1:  cores 0 1 2 3 4 5
cache size     : 15360 KB
```

```
From /proc/meminfo
MemTotal:      99043512 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux x3630m4-rhel62 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 12 07:34
```

```
SPEC is set to: /home/SPECcpu-v1.2
Filesystem      Type      Size Used Avail Use% Mounted on
/dev/mapper/vg_x3630m4rhel62-lv_home
  ext4          383G  4.4G  359G   2% /home
```

```
Additional information from dmidecode:
Memory:
 12x Samsung M393B1K70DH0-CK0 8 GB 1333 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
 -opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
 -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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