



# SPEC<sup>®</sup> CFP2006 Result

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## Supermicro

### SPECfp<sup>®</sup>\_rate2006 = 139

### Motherboard X9SCA-F (Intel Xeon E3-1280 v2)

### SPECfp\_rate\_base2006 = 134

CPU2006 license: 001176

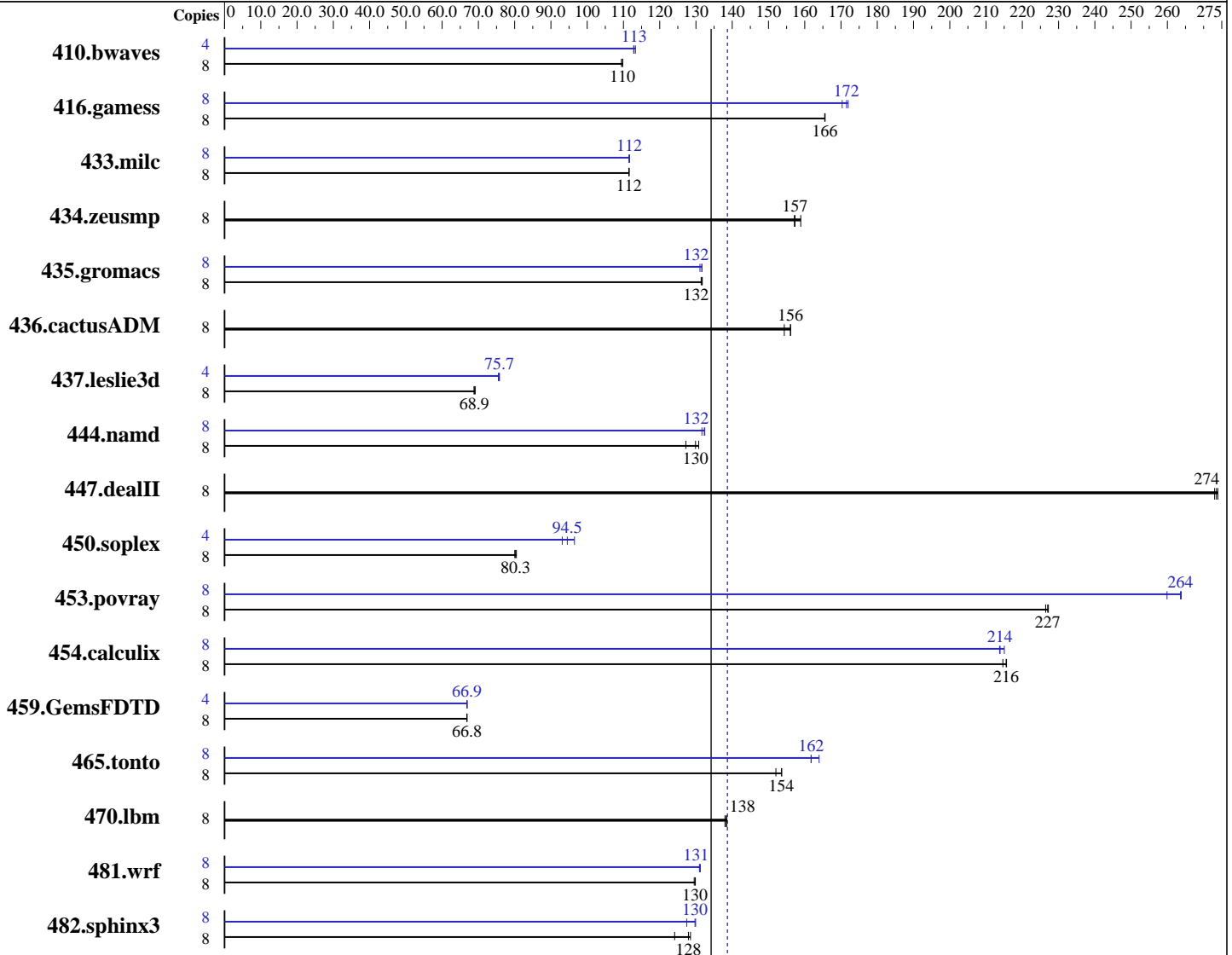
Test date: Jun-2012

Test sponsor: Supermicro

Hardware Availability: May-2012

Tested by: Supermicro

Software Availability: Dec-2011



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### Hardware

CPU Name: Intel Xeon E3-1280 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago), Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	990	110	<u>991</u>	<u>110</u>	993	109	4	482	113	480	113	<u>481</u>	<u>113</u>
416.gamess	8	946	166	946	165	<u>946</u>	<u>166</u>	8	<u>913</u>	<u>172</u>	911	172	920	170
433.milc	8	<u>658</u>	<u>112</u>	658	112	659	111	8	<u>658</u>	<u>112</u>	659	112	658	112
434.zeusmp	8	458	159	<u>463</u>	<u>157</u>	463	157	8	458	159	<u>463</u>	<u>157</u>	463	157
435.gromacs	8	435	131	<u>434</u>	<u>132</u>	434	132	8	<u>434</u>	<u>132</u>	436	131	434	132
436.cactusADM	8	<u>613</u>	<u>156</u>	612	156	619	154	8	<u>613</u>	<u>156</u>	612	156	619	154
437.leslie3d	8	1093	68.8	1088	69.1	<u>1092</u>	<u>68.9</u>	4	496	75.8	498	75.5	<u>496</u>	<u>75.7</u>
444.namd	8	504	127	491	131	<u>494</u>	<u>130</u>	8	487	132	484	132	<u>485</u>	<u>132</u>
447.dealII	8	334	274	335	273	<u>335</u>	<u>274</u>	8	334	274	335	273	<u>335</u>	<u>274</u>
450.soplex	8	829	80.5	834	80.0	<u>831</u>	<u>80.3</u>	4	358	93.1	<u>353</u>	<u>94.5</u>	346	96.5
453.povray	8	188	226	<u>188</u>	<u>227</u>	187	227	8	164	260	<u>161</u>	<u>264</u>	161	264
454.calculix	8	<u>306</u>	<u>216</u>	306	216	307	215	8	309	214	307	215	<u>309</u>	<u>214</u>
459.GemsFDTD	8	1271	66.8	<u>1270</u>	<u>66.8</u>	1269	66.9	4	634	66.9	<u>635</u>	<u>66.9</u>	635	66.8
465.tonto	8	<u>513</u>	<u>154</u>	518	152	512	154	8	<u>487</u>	<u>162</u>	487	162	480	164
470.lbm	8	794	139	<u>796</u>	<u>138</u>	797	138	8	794	139	<u>796</u>	<u>138</u>	797	138
481.wrf	8	688	130	<u>689</u>	<u>130</u>	690	130	8	<u>682</u>	<u>131</u>	682	131	681	131
482.sphinx3	8	1256	124	1213	128	<u>1219</u>	<u>128</u>	8	1223	127	<u>1202</u>	<u>130</u>	1200	130

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages disabled with:  
echo never > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

### C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-static -auto-ilp32 -opt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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