



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant BL660c Gen8
(2.70 GHz, Intel Xeon E5-4650)

SPECint®2006 = 46.9

SPECint_base2006 = 42.9

CPU2006 license: 3

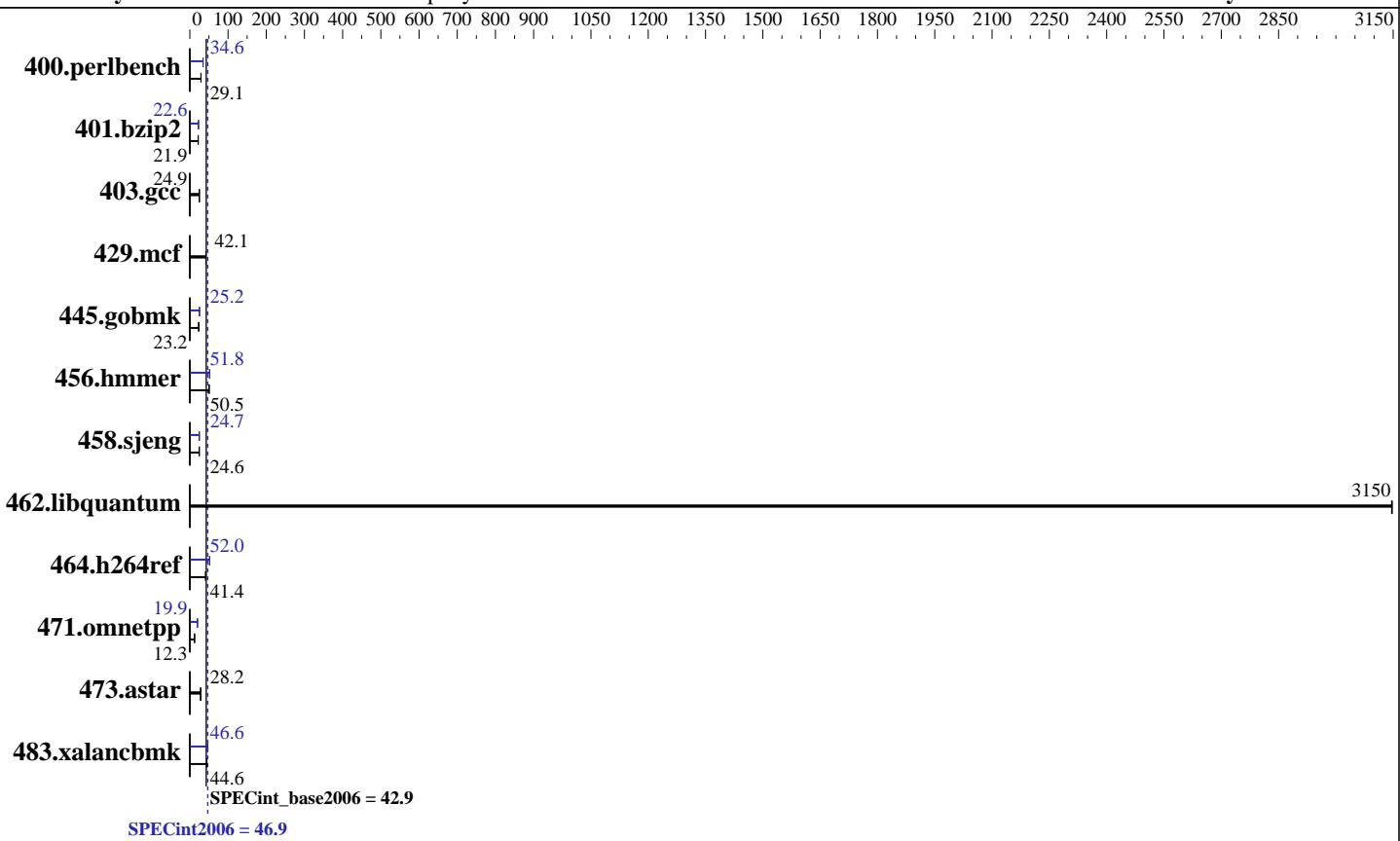
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: May-2012

Hardware Availability: Sep-2012

Software Availability: Mar-2012



Hardware

CPU Name: Intel Xeon E5-4650
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz: 2700
FPU: Integrated
CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
CPU(s) orderable: 2,4 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 2 x 146 GB 15 K SAS, RAID 1
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2, (Santiago)
Compiler: Kernel 2.6.32-220.el6.x86_64
Auto Parallel: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01
HP Array Configuration Utility, CLI version



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	337	29.0	334	29.3	<u>335</u>	<u>29.1</u>	281	34.7	283	34.5	<u>283</u>	<u>34.6</u>
401.bzip2	441	21.9	441	21.9	<u>441</u>	<u>21.9</u>	428	22.6	<u>428</u>	<u>22.6</u>	428	22.6
403.gcc	<u>323</u>	<u>24.9</u>	323	24.9	326	24.7	<u>323</u>	<u>24.9</u>	323	24.9	326	24.7
429.mcf	216	42.2	<u>217</u>	<u>42.1</u>	219	41.7	216	42.2	<u>217</u>	<u>42.1</u>	219	41.7
445.gobmk	452	23.2	<u>452</u>	<u>23.2</u>	453	23.2	416	25.2	<u>416</u>	<u>25.2</u>	416	25.2
456.hmmer	185	50.5	185	50.3	<u>185</u>	<u>50.5</u>	180	51.8	<u>180</u>	<u>51.8</u>	180	51.8
458.sjeng	492	24.6	491	24.6	<u>491</u>	<u>24.6</u>	490	24.7	<u>490</u>	<u>24.7</u>	490	24.7
462.libquantum	<u>6.59</u>	<u>3150</u>	6.59	3150	6.58	3150	<u>6.59</u>	<u>3150</u>	6.59	3150	6.58	3150
464.h264ref	533	41.5	<u>535</u>	<u>41.4</u>	535	41.3	432	51.2	<u>426</u>	<u>52.0</u>	424	52.2
471.omnetpp	506	12.4	<u>506</u>	<u>12.3</u>	508	12.3	315	19.9	<u>314</u>	<u>19.9</u>	313	20.0
473.astar	250	28.1	248	28.3	<u>249</u>	<u>28.2</u>	250	28.1	248	28.3	<u>249</u>	<u>28.2</u>
483.xalancbmk	154	44.8	155	44.6	<u>155</u>	<u>44.6</u>	<u>148</u>	<u>46.6</u>	148	46.6	148	46.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

echo 1 > /proc/sys/vm/drop_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Drive Write Cache set to Enabled in HP Array Configuration Utility, CLI version

Accelerator Ratio for Reads/Writes set to = 100% Read / 0% Write in HP Array Configuration Utility, CLI version

Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

Energy/Performance Bias is set to Maximum Performance

Thermal Configuration set to Maximum Cooling

Collaborative Power Control set to Disabled

Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /cpu2006/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 ## 6f2ebdff5032aaa42e583f96b07f99d3

running on BL660G8 Sun May 20 23:05:40 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

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Platform Notes (Continued)

```
model name : Intel(R) Xeon(R) CPU E5-4650 0 @ 2.70GHz
        4 "physical id"s (chips)
        32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
physical 2: cores 0 1 2 3 4 5 6 7
physical 3: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal:      264634472 kB
HugePages_Total:      0
Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux BL660G8 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
x86_64 x86_64 GNU/Linux

run-level 3 May 20 15:42

SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext4  133G  8.7G  118G   7%  /

Additional information from dmidecode:
BIOS HP I32 04/11/2012
Memory:
32x HP Not Specified 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/lib32:/cpu2006/lib64"

OMP_NUM_THREADS = "32"

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General Notes (Continued)

Binaries compiled on a system with 2x Xeon E5-2667 CPU + 256GB memory using SLES11 SP2,RC3

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
 -Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

 403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias
```

```
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
               -opt-prefetch -ansi-alias
```

403.gcc: basepeak = yes

429.mcf: basepeak = yes

```
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias
```

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Peak Optimization Flags (Continued)

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120605.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-A.20120605.xml>



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