



# SPEC<sup>®</sup> CINT2006 Result

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## ACTION S.A.

SPECint<sup>®</sup>\_rate2006 = 388

ACTINA SOLAR 220 X5 (Intel Xeon E5-2630L)

SPECint\_rate\_base2006 = 370

CPU2006 license: 9008

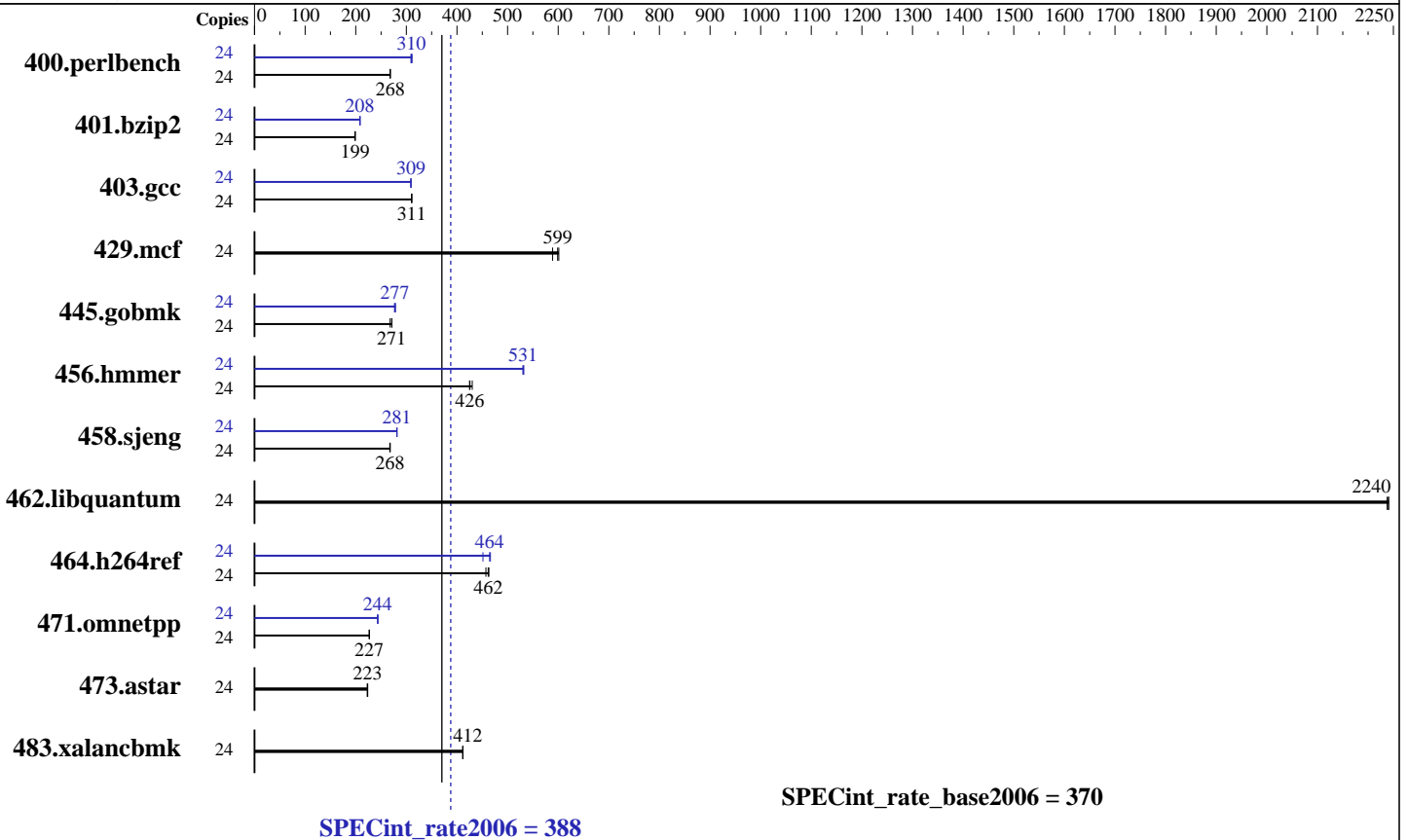
Test date: Mar-2012

Test sponsor: ACTION S.A.

Hardware Availability: Mar-2012

Tested by: ACTION S.A.

Software Availability: Oct-2011



### Hardware

CPU Name: Intel Xeon E5-2630L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 2 TB SATA 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.9-default  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b>875</b>	<b>268</b>	875	268	874	268	24	753	311	<b>756</b>	<b>310</b>	759	309
401.bzip2	24	1163	199	<b>1165</b>	<b>199</b>	1169	198	24	1113	208	<b>1112</b>	<b>208</b>	1109	209
403.gcc	24	623	310	<b>622</b>	<b>311</b>	622	311	24	625	309	625	309	<b>625</b>	<b>309</b>
429.mcf	24	364	601	<b>365</b>	<b>599</b>	372	589	24	364	601	<b>365</b>	<b>599</b>	372	589
445.gobmk	24	<b>929</b>	<b>271</b>	940	268	928	271	24	903	279	910	277	<b>909</b>	<b>277</b>
456.hammer	24	521	430	527	425	<b>526</b>	<b>426</b>	24	421	532	<b>422</b>	<b>531</b>	422	530
458.sjeng	24	1087	267	<b>1084</b>	<b>268</b>	1083	268	24	1031	282	<b>1033</b>	<b>281</b>	1033	281
462.libquantum	24	<b>222</b>	<b>2240</b>	222	2240	222	2240	24	<b>222</b>	<b>2240</b>	222	2240	222	2240
464.h264ref	24	1162	457	1147	463	<b>1150</b>	<b>462</b>	24	1139	466	<b>1144</b>	<b>464</b>	1177	451
471.omnetpp	24	661	227	<b>661</b>	<b>227</b>	662	227	24	<b>616</b>	<b>244</b>	616	243	616	244
473.astar	24	757	223	<b>754</b>	<b>223</b>	754	224	24	757	223	<b>754</b>	<b>223</b>	754	224
483.xalancbmk	24	403	411	402	412	<b>402</b>	<b>412</b>	24	403	411	402	412	<b>402</b>	<b>412</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
           -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

```



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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