



# SPEC® CFP2006 Result

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Fujitsu

**SPECfp®\_rate2006 = 514**

CELSIUS R920 (2x Intel Xeon E5-2687W)

**SPECfp\_rate\_base2006 = 500**

CPU2006 license: 19

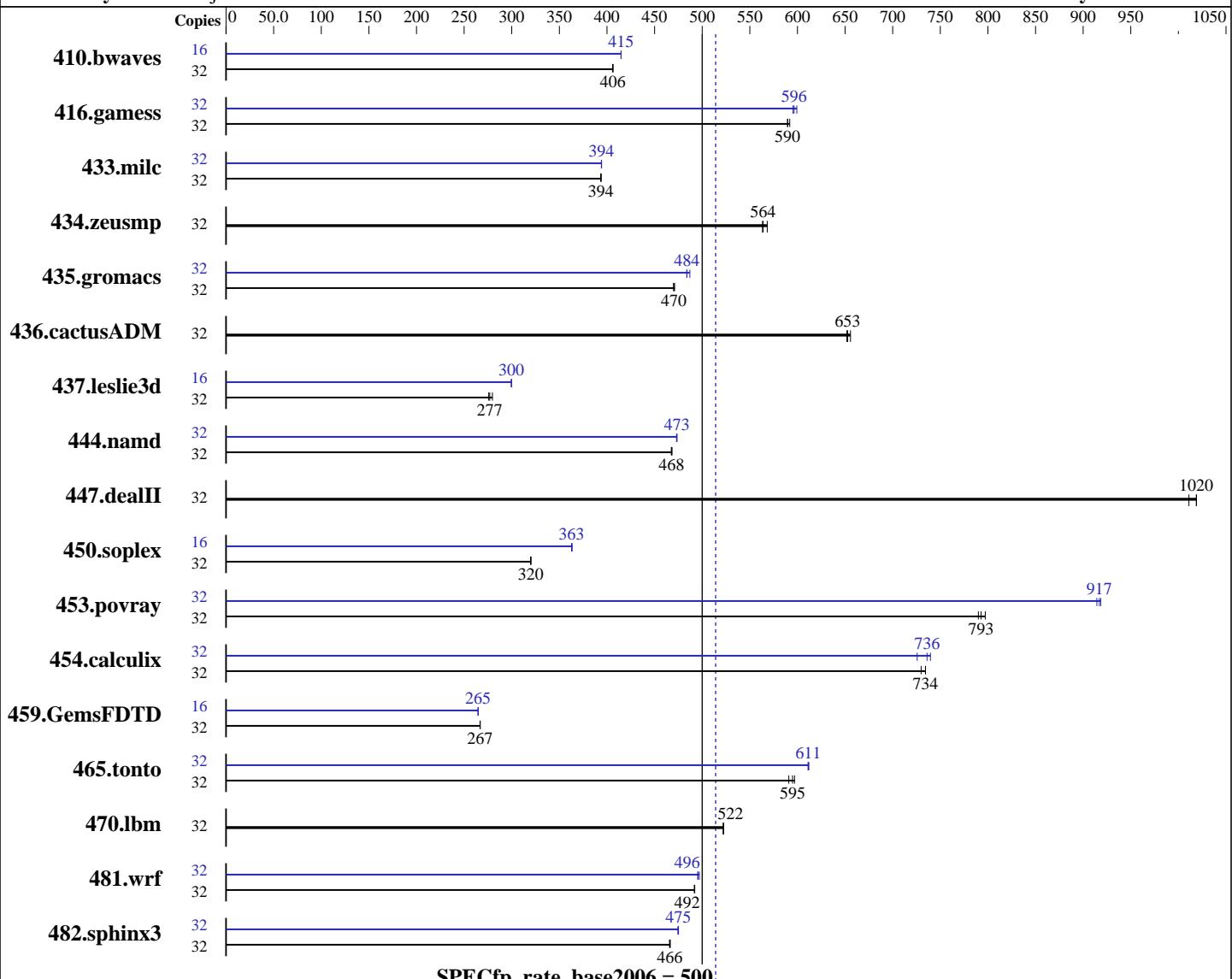
Test date: Mar-2012

Test sponsor: Fujitsu

Hardware Availability: Mar-2012

Tested by: Fujitsu

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon E5-2687W  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 3100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86\_64  
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi - user)

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L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	<b>1070</b>	<b>406</b>	1070	406	1071	406	16	524	415	<b>524</b>	<b>415</b>	524	415
416.gamess	32	1059	592	<b>1063</b>	<b>590</b>	1063	590	32	1052	595	<b>1050</b>	<b>596</b>	1045	599
433.milc	32	746	394	747	394	<b>746</b>	<b>394</b>	32	745	394	745	394	<b>745</b>	<b>394</b>
434.zeusmp	32	517	563	512	568	<b>516</b>	<b>564</b>	32	517	563	512	568	<b>516</b>	<b>564</b>
435.gromacs	32	<b>486</b>	<b>470</b>	486	470	485	471	32	469	487	472	484	<b>472</b>	<b>484</b>
436.cactusADM	32	583	656	<b>586</b>	<b>653</b>	587	652	32	583	656	<b>586</b>	<b>653</b>	587	652
437.leslie3d	32	1075	280	1091	276	<b>1086</b>	<b>277</b>	16	502	300	<b>502</b>	<b>300</b>	502	300
444.namd	32	548	468	<b>549</b>	<b>468</b>	549	468	32	<b>542</b>	<b>473</b>	543	473	542	473
447.dealII	32	<b>359</b>	<b>1020</b>	362	1010	359	1020	32	<b>359</b>	<b>1020</b>	362	1010	359	1020
450.soplex	32	833	320	<b>834</b>	<b>320</b>	834	320	16	368	363	<b>368</b>	<b>363</b>	367	363
453.povray	32	216	790	214	797	<b>215</b>	<b>793</b>	32	<b>186</b>	<b>917</b>	186	914	185	918
454.calculix	32	362	730	<b>359</b>	<b>734</b>	359	734	32	357	740	<b>359</b>	<b>736</b>	364	726
459.GemsFDTD	32	<b>1273</b>	<b>267</b>	1273	267	1272	267	16	642	265	641	265	<b>641</b>	<b>265</b>
465.tonto	32	533	591	527	597	<b>529</b>	<b>595</b>	32	515	611	<b>515</b>	<b>611</b>	515	612
470.lbm	32	<b>842</b>	<b>522</b>	842	522	842	522	32	<b>842</b>	<b>522</b>	842	522	842	522
481.wrf	32	<b>726</b>	<b>492</b>	726	492	727	492	32	<b>721</b>	<b>496</b>	722	495	719	497
482.sphinx3	32	1337	466	<b>1338</b>	<b>466</b>	1339	466	32	<b>1315</b>	<b>474</b>	1313	475	<b>1313</b>	<b>475</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Frequency Floor Override = Enabled



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

Binaries compiled on a system with  
Red Hat Enterprise Linux Server release 6.1 (Santiago)  
Added glibc-static-2.12-1.25.el6.x86\_64.rpm  
to enable static linking  
Transparent Huge Pages disabled with:  
echo never > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
    433.milc: -DSPEC\_CPU\_LP64  
    434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
    437.leslie3d: -DSPEC\_CPU\_LP64  
        444.namd: -DSPEC\_CPU\_LP64  
        447.dealII: -DSPEC\_CPU\_LP64  
        450.soplex: -DSPEC\_CPU\_LP64  
        453.povray: -DSPEC\_CPU\_LP64  
        454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
    465.tonto: -DSPEC\_CPU\_LP64  
    470.lbm: -DSPEC\_CPU\_LP64  
        481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

482.sphinx3: 

```
icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

450.soplex: 

```
icpc -m32
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

## Peak Portability Flags

410.bwaves: 

```
-DSPEC_CPU_LP64
```

  
416.gamess: 

```
-DSPEC_CPU_LP64
```

  
433.milc: 

```
-DSPEC_CPU_LP64
```

  
434.zeusmp: 

```
-DSPEC_CPU_LP64
```

  
435.gromacs: 

```
-DSPEC_CPU_LP64 -nofor_main
```

  
436.cactusADM: 

```
-DSPEC_CPU_LP64 -nofor_main
```

  
437.leslie3d: 

```
-DSPEC_CPU_LP64
```

  
444.namd: 

```
-DSPEC_CPU_LP64
```

  
447.dealII: 

```
-DSPEC_CPU_LP64
```

  
453.povray: 

```
-DSPEC_CPU_LP64
```

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## Peak Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xsSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3  
-unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static  
-auto-p32 -ansi-alias -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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