



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Intel DH61WW motherboard (Intel Pentium G860)

SPECint®2006 = 34.6

SPECint_base2006 = 31.9

CPU2006 license: 13

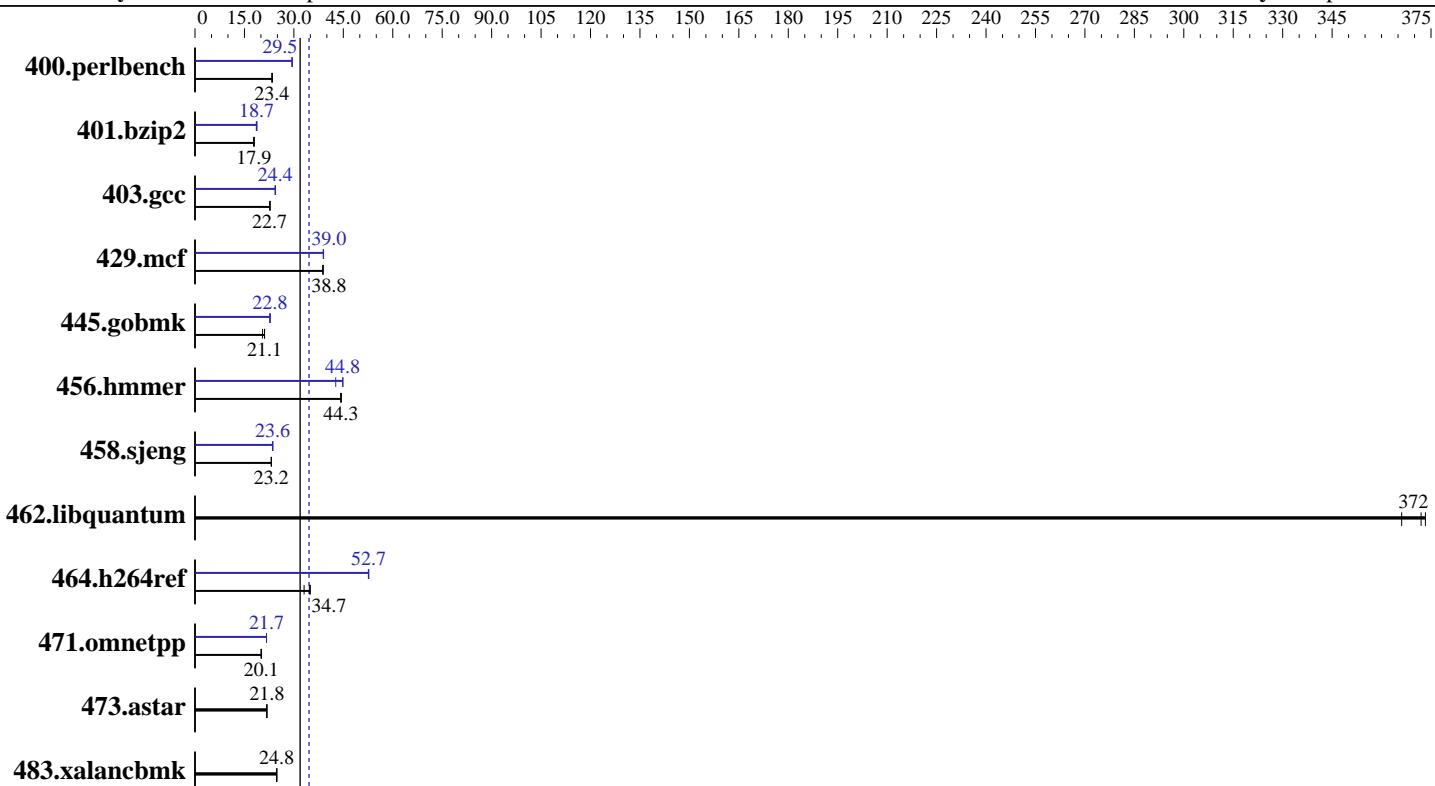
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Oct-2011

Hardware Availability: Sep-2011

Software Availability: Apr-2011



Hardware

CPU Name:	Intel Pentium G860
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)
Disk Subsystem:	1 TB Seagate SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows;
	Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	420	23.3	417	23.4	416	23.5	332	29.5	333	29.4	332	29.5
401.bzip2	541	17.8	538	17.9	537	18.0	513	18.8	518	18.6	516	18.7
403.gcc	354	22.8	354	22.7	354	22.7	331	24.3	330	24.4	330	24.4
429.mcf	235	38.8	235	38.8	235	38.9	234	39.0	234	39.0	234	38.9
445.gobmk	512	20.5	497	21.1	498	21.1	461	22.8	461	22.8	461	22.7
456.hmmer	211	44.2	210	44.4	210	44.3	208	44.8	219	42.7	208	44.9
458.sjeng	523	23.1	522	23.2	523	23.2	514	23.5	514	23.6	513	23.6
462.libquantum	56.6	366	55.7	372	55.5	373	56.6	366	55.7	372	55.5	373
464.h264ref	668	33.1	637	34.7	633	35.0	421	52.6	420	52.7	420	52.7
471.omnetpp	311	20.1	310	20.1	311	20.1	288	21.7	288	21.7	288	21.7
473.astar	322	21.8	322	21.8	322	21.8	322	21.8	322	21.8	322	21.8
483.xalancbmk	278	24.8	278	24.8	278	24.8	278	24.8	278	24.8	278	24.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Component Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

General Notes

Binaries compiled on a system with 1x Intel Core i7-860 CPU
+ 8GB memory using Windows 7 Enterprise 64-bit
OMP_NUM_THREADS set to number of processor cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

 icl -Qvc9 -Qstd=c99

C++ benchmarks:

 icl -Qvc9

Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
 -DSPEC_CPU_NO_NEED_VA_COPY

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Base Portability Flags (Continued)

```
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hammer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption_cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000
```

C++ benchmarks:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib           -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
    -Qauto-ilp32 /F512000000 shlw64M.lib
    -link /FORCE:MULTIPLE
```

```
401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
    -Qauto-ilp32 /F512000000
```

```
403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000
```

```
429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
    -Qauto-ilp32 /F512000000
```

```
445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32
    /F512000000
```

```
456.hmmr: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
    -Qauto-ilp32 /F512000000
```

```
458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000
```

462.libquantum: basepeak = yes

```
464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
    -Qauto-ilp32 /F512000000
```

C++ benchmarks:

```
471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
    -Qipo -O3 -Qprec-div- -Qansi-alias
    -Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
    shlw64M.lib
    -link /FORCE:MULTIPLE
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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