



SPEC[®] CFP2006 Result

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ASUSTeK Computer Inc.
(Test Sponsor: Intel Corporation)

SPECfp[®]_rate2006 = 51.4

ASUSTek M4A89GTD PRO/USB3 (Phenom II X3 740)

SPECfp_rate_base2006 = 50.8

CPU2006 license: 13

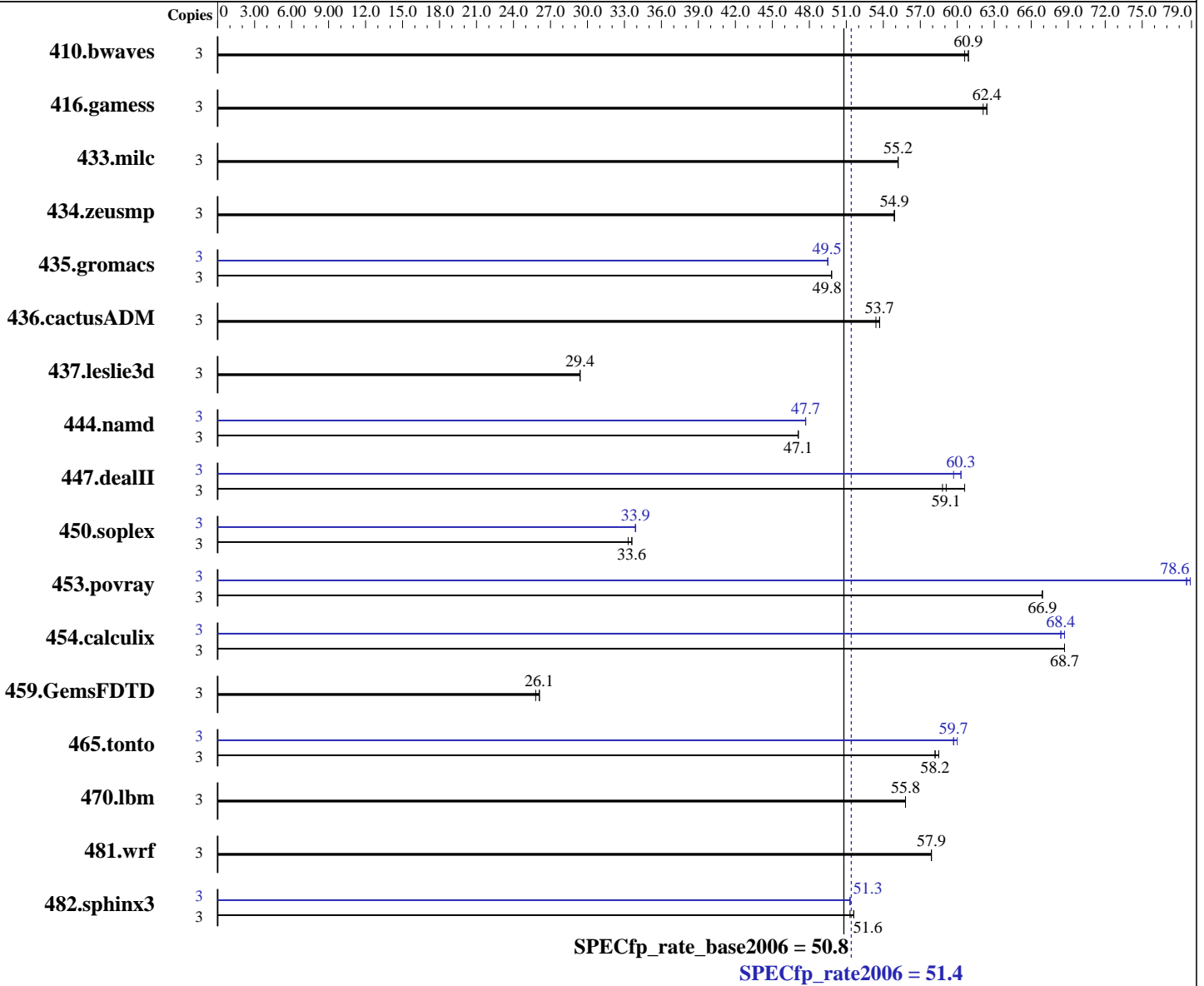
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Apr-2011



Hardware

CPU Name: AMD Phenom II X3 740
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 3 cores, 1 chip, 3 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Intel Visual Fortran Compiler XE for Intel 64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)

Auto Parallel: No
 File System: NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	3	670	60.9	670	60.9	672	60.6	3	670	60.9	670	60.9	672	60.6
416.gamess	3	944	62.4	944	62.4	944	62.1	3	944	62.4	944	62.4	944	62.1
433.milc	3	499	55.2	499	55.2	500	55.2	3	499	55.2	499	55.2	500	55.2
434.zeusmp	3	497	54.9	497	54.9	498	54.9	3	497	54.9	497	54.9	498	54.9
435.gromacs	3	430	49.8	430	49.8	431	49.8	3	432	49.5	432	49.5	432	49.5
436.cactusADM	3	669	53.7	669	53.7	672	53.4	3	669	53.7	669	53.7	672	53.4
437.leslie3d	3	961	29.4	962	29.4	958	29.4	3	961	29.4	962	29.4	958	29.4
444.namd	3	509	47.1	510	47.1	510	47.1	3	504	47.7	504	47.7	504	47.7
447.dealII	3	580	59.1	567	60.6	582	58.8	3	570	60.3	574	59.7	570	60.3
450.soplex	3	748	33.6	747	33.6	749	33.3	3	738	33.9	738	33.9	738	33.9
453.povray	3	239	66.9	239	66.9	239	66.9	3	203	78.9	203	78.6	203	78.6
454.calculix	3	361	68.7	360	68.7	360	68.7	3	362	68.4	361	68.7	362	68.4
459.GemsFDTD	3	1215	26.1	1217	26.1	1234	25.8	3	1215	26.1	1217	26.1	1234	25.8
465.tonto	3	506	58.5	508	58.2	508	58.2	3	494	59.7	495	59.7	493	60.0
470.lbm	3	737	55.8	739	55.8	739	55.8	3	737	55.8	739	55.8	739	55.8
481.wrf	3	578	57.9	578	57.9	579	57.9	3	578	57.9	578	57.9	579	57.9
482.sphinx3	3	1137	51.3	1134	51.6	1136	51.6	3	1139	51.3	1139	51.3	1138	51.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
The start command with the /affinity switch was used to bind processes to cores

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE



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Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99
C++ benchmarks:
icl -Qvc9
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: /arch:SSE3 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE
C++ benchmarks:
444.namd: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE
447.deaIII: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE
450.soplex: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE
453.povray: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

- 410.bwaves: basepeak = yes
- 416.gamess: basepeak = yes
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: basepeak = yes
- 465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

- 435.gromacs: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
- 436.cactusADM: basepeak = yes
- 454.calculix: /arch:SSE3 -Qipo -O3 -Qprec-div- -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
- 481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>
<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>
<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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