



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 238

PowerEdge R610 (Intel Xeon X5687, 3.60 GHz)

SPECfp_rate_base2006 = 230

CPU2006 license: 55

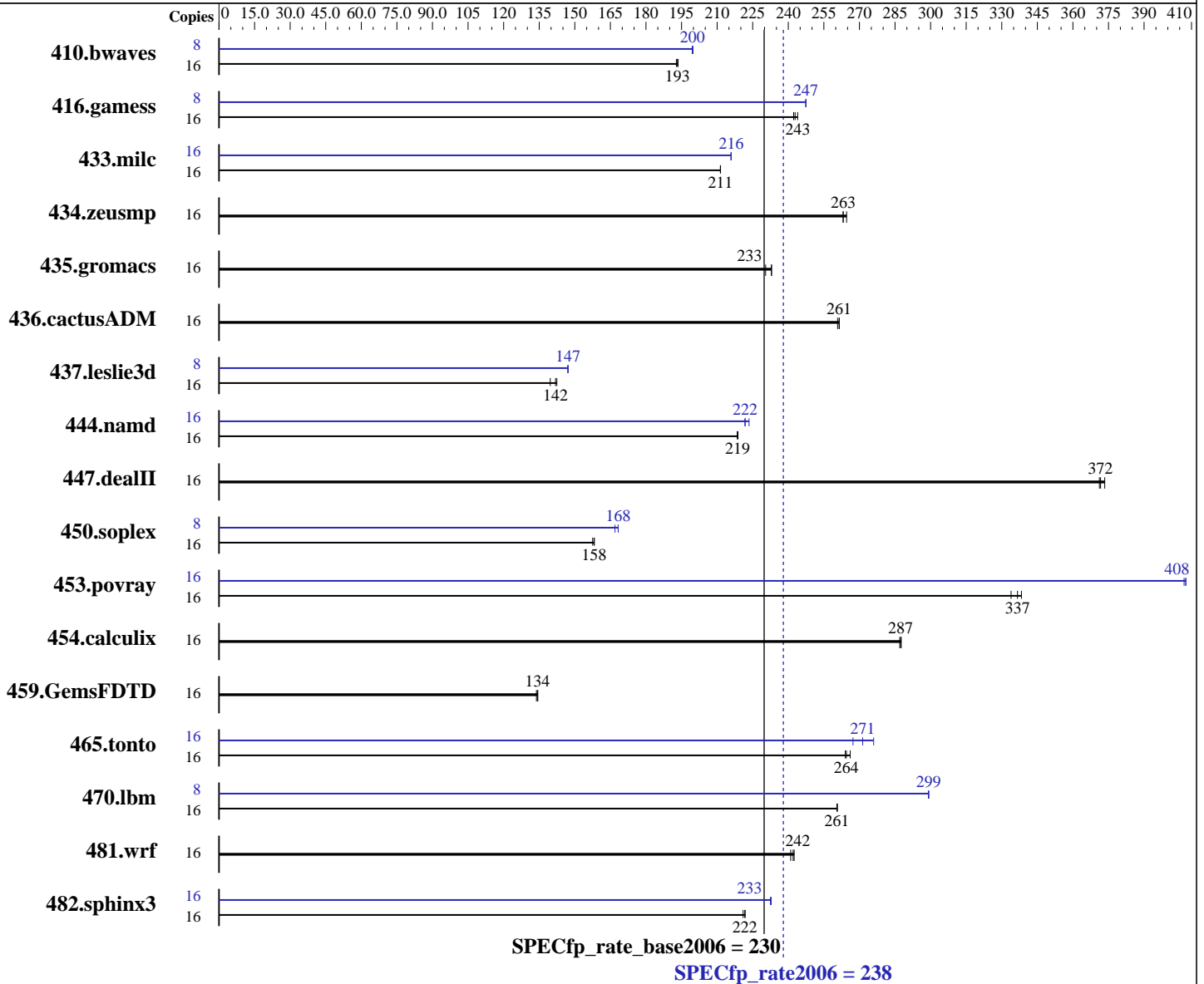
Test date: Feb-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5687
 CPU Characteristics: Intel Turbo Boost Technology up to 3.86 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 300 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1128	193	<u>1125</u>	<u>193</u>	1124	194	8	545	200	544	200	<u>545</u>	<u>200</u>
416.gamess	16	1293	242	<u>1290</u>	<u>243</u>	1284	244	8	633	247	<u>633</u>	<u>247</u>	633	247
433.milc	16	<u>695</u>	<u>211</u>	695	211	695	211	16	681	216	<u>680</u>	<u>216</u>	680	216
434.zeusmp	16	553	263	550	265	<u>553</u>	<u>263</u>	16	553	263	550	265	<u>553</u>	<u>263</u>
435.gromacs	16	496	230	490	233	<u>491</u>	<u>233</u>	16	496	230	490	233	<u>491</u>	<u>233</u>
436.cactusADM	16	<u>731</u>	<u>261</u>	731	261	733	261	16	<u>731</u>	<u>261</u>	731	261	733	261
437.leslie3d	16	<u>1060</u>	<u>142</u>	1056	142	1077	140	8	511	147	511	147	<u>511</u>	<u>147</u>
444.namd	16	<u>587</u>	<u>219</u>	586	219	587	219	16	574	223	<u>578</u>	<u>222</u>	579	222
447.dealII	16	<u>493</u>	<u>372</u>	493	371	490	373	16	<u>493</u>	<u>372</u>	493	371	490	373
450.soplex	16	<u>844</u>	<u>158</u>	847	158	843	158	8	400	167	<u>396</u>	<u>168</u>	396	168
453.povray	16	252	338	<u>253</u>	<u>337</u>	255	334	16	209	408	<u>209</u>	<u>408</u>	209	407
454.calculix	16	460	287	459	288	<u>459</u>	<u>287</u>	16	460	287	459	288	<u>459</u>	<u>287</u>
459.GemsFDTD	16	1263	134	<u>1265</u>	<u>134</u>	1269	134	16	1263	134	<u>1265</u>	<u>134</u>	1269	134
465.tonto	16	592	266	<u>595</u>	<u>264</u>	596	264	16	570	276	589	267	<u>580</u>	<u>271</u>
470.lbm	16	843	261	<u>844</u>	<u>261</u>	844	260	8	367	299	368	299	<u>367</u>	<u>299</u>
481.wrf	16	737	242	741	241	<u>738</u>	<u>242</u>	16	737	242	741	241	<u>738</u>	<u>242</u>
482.sphinx3	16	1406	222	<u>1407</u>	<u>222</u>	1411	221	16	<u>1340</u>	<u>233</u>	1341	232	1340	233

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```



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Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)

Data Reuse = Disabled (Default = Enabled)

General Notes

The Dell PowerEdge R610 and the Bull NovaScale R440 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R610 model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml>

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