



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECfp®2006 = **60.4**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

SPECfp\_base2006 = **57.1**

CPU2006 license: 3

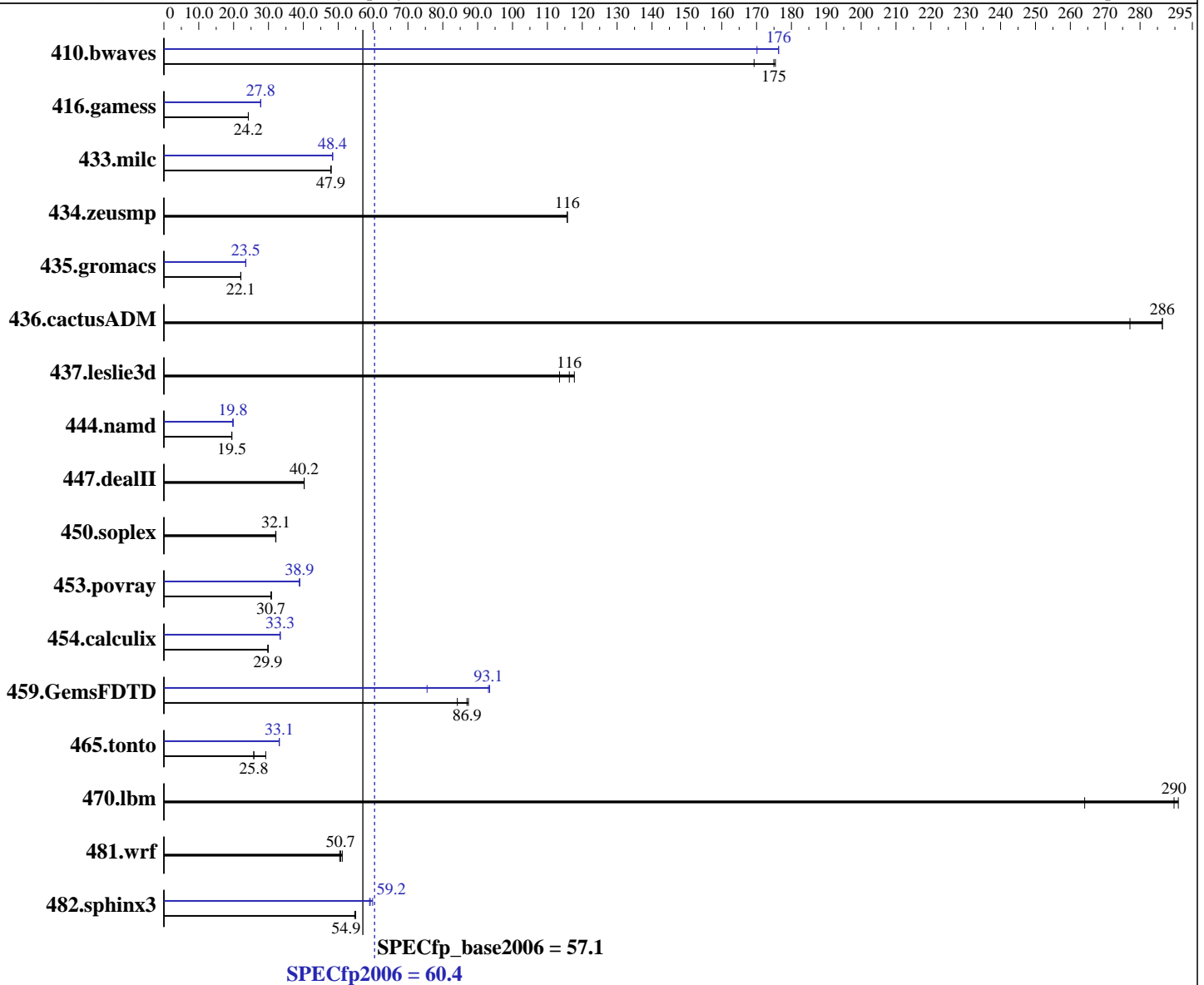
Test date: Jan-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Feb-2011

Tested by: Hewlett-Packard Company

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECfp2006 = **60.4**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

SPECfp\_base2006 = **57.1**

CPU2006 license: 3

Test date: Jan-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Feb-2011

Tested by: Hewlett-Packard Company

Software Availability: Apr-2011

L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15 K SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V9.01

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>77.7</u>	<u>175</u>	77.5	175	80.3	169	<u>77.1</u>	<u>176</u>	77.1	176	79.9	170
416.gamess	808	24.2	809	24.2	<u>809</u>	<u>24.2</u>	705	27.8	705	27.8	<u>705</u>	<u>27.8</u>
433.milc	<u>192</u>	<u>47.9</u>	191	48.0	192	47.9	<u>190</u>	<u>48.4</u>	190	48.4	190	48.4
434.zeusmp	78.7	116	<u>78.7</u>	<u>116</u>	78.7	116	<u>78.7</u>	116	<u>78.7</u>	<u>116</u>	78.7	116
435.gromacs	<u>324</u>	<u>22.1</u>	323	22.1	324	22.0	304	23.5	304	23.5	<u>304</u>	<u>23.5</u>
436.cactusADM	<u>41.7</u>	<u>286</u>	41.7	286	43.1	277	<u>41.7</u>	<u>286</u>	41.7	286	43.1	277
437.leslie3d	82.8	113	79.8	118	<u>80.8</u>	<u>116</u>	82.8	113	79.8	118	<u>80.8</u>	<u>116</u>
444.namd	411	19.5	<u>412</u>	<u>19.5</u>	412	19.5	405	19.8	404	19.8	<u>405</u>	<u>19.8</u>
447.dealII	284	40.2	284	40.2	<u>284</u>	<u>40.2</u>	284	40.2	284	40.2	<u>284</u>	<u>40.2</u>
450.soplex	260	32.0	260	32.1	<u>260</u>	<u>32.1</u>	260	32.0	260	32.1	<u>260</u>	<u>32.1</u>
453.povray	173	30.7	172	30.8	<u>173</u>	<u>30.7</u>	137	38.9	<u>137</u>	<u>38.9</u>	137	38.9
454.calculix	<u>276</u>	<u>29.9</u>	277	29.8	276	29.9	247	33.3	<u>247</u>	<u>33.3</u>	247	33.4
459.GemsFDTD	121	87.4	126	84.2	<u>122</u>	<u>86.9</u>	114	93.4	<u>114</u>	<u>93.1</u>	141	75.5
465.tonto	<u>381</u>	<u>25.8</u>	337	29.2	382	25.8	298	33.1	297	33.1	<u>297</u>	<u>33.1</u>
470.lbm	47.2	291	<u>47.4</u>	<u>290</u>	52.0	264	47.2	291	<u>47.4</u>	<u>290</u>	52.0	264
481.wrf	222	50.4	<u>220</u>	<u>50.7</u>	218	51.2	222	50.4	<u>220</u>	<u>50.7</u>	218	51.2
482.sphinx3	354	55.0	356	54.7	<u>355</u>	<u>54.9</u>	<u>329</u>	<u>59.2</u>	330	59.0	326	59.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECfp2006 = 60.4**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

**SPECfp\_base2006 = 57.1**

**CPU2006 license:** 3

**Test date:** Jan-2011

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Feb-2011

**Tested by:** Hewlett-Packard Company

**Software Availability:** Apr-2011

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECfp2006 = 60.4**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

**SPECfp\_base2006 = 57.1**

**CPU2006 license:** 3

**Test date:** Jan-2011

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Feb-2011

**Tested by:** Hewlett-Packard Company

**Software Availability:** Apr-2011

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

453.povray: icpc -m64

-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks (except as noted below):

ifort -m64

459.GemsFDTD: ifort -m64

-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: ifort -m64

-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

**SPECfp2006 = 60.4**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

**SPECfp\_base2006 = 57.1**

**CPU2006 license:** 3  
**Test sponsor:** Hewlett-Packard Company  
**Tested by:** Hewlett-Packard Company

**Test date:** Jan-2011  
**Hardware Availability:** Feb-2011  
**Software Availability:** Apr-2011

## Peak Optimization Flags (Continued)

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.html>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101109.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.xml>  
<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20101109.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

ProLiant DL180 G6  
(3.06 GHz, Intel Xeon X5675)

**SPECfp2006 = 60.4**

**SPECfp\_base2006 = 57.1**

**CPU2006 license:** 3

**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

**Test date:** Jan-2011

**Hardware Availability:** Feb-2011

**Software Availability:** Apr-2011

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 16:51:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 8 March 2011.