



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp[®]_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19

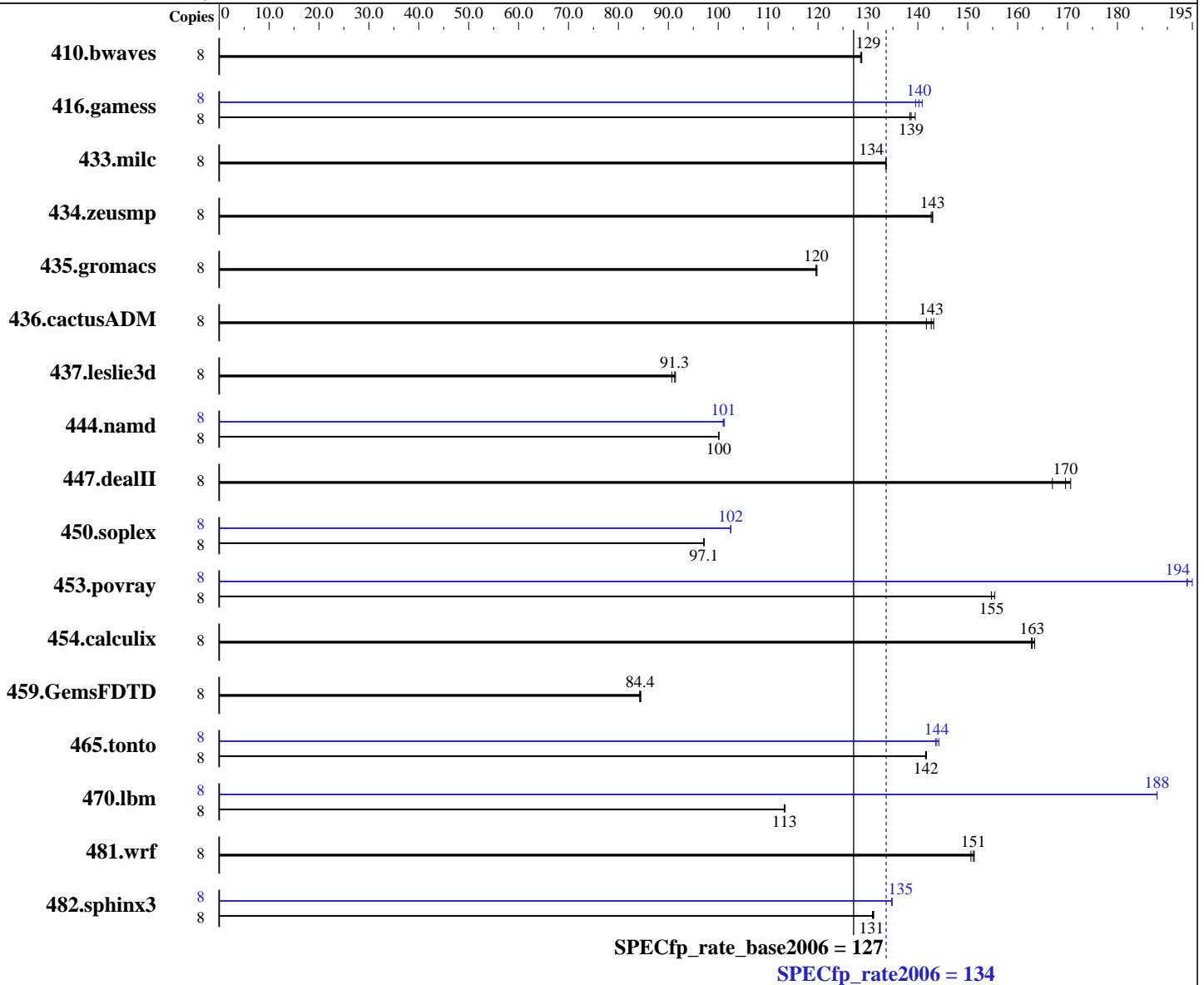
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Jul-2010

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon E5606
 CPU Characteristics: 2133
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.0.082 Build 20101006
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19

Test date: Dec-2010

Test sponsor: Fujitsu

Hardware Availability: Jul-2010

Tested by: Fujitsu

Software Availability: Nov-2010

L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: --

Peak Pointers: 32/64-bit
 Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	846	129	845	129	844	129	8	846	129	845	129	844	129
416.gamess	8	1132	138	1130	139	1123	139	8	1122	140	1117	140	1112	141
433.milc	8	550	134	550	134	549	134	8	550	134	550	134	549	134
434.zeusmp	8	510	143	509	143	509	143	8	510	143	509	143	509	143
435.gromacs	8	477	120	478	120	477	120	8	477	120	478	120	477	120
436.cactusADM	8	668	143	670	143	675	142	8	668	143	670	143	675	142
437.leslie3d	8	822	91.4	829	90.7	824	91.3	8	822	91.4	829	90.7	824	91.3
444.namd	8	641	100	641	100	641	100	8	635	101	634	101	635	101
447.dealII	8	540	170	536	171	548	167	8	540	170	536	171	548	167
450.soplex	8	687	97.1	687	97.1	686	97.2	8	651	102	651	102	651	103
453.povray	8	274	155	275	155	275	155	8	218	195	219	194	219	194
454.calculix	8	404	163	405	163	405	163	8	404	163	405	163	405	163
459.GemsFDTD	8	1006	84.4	1007	84.3	1005	84.5	8	1006	84.4	1007	84.3	1005	84.5
465.tonto	8	556	142	555	142	556	142	8	546	144	548	144	547	144
470.lbm	8	970	113	969	113	970	113	8	585	188	585	188	585	188
481.wrf	8	593	151	591	151	591	151	8	593	151	591	151	591	151
482.sphinx3	8	1190	131	1191	131	1189	131	8	1156	135	1156	135	1157	135

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
 Data Reuse Optimization = Disable



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2010
Hardware Availability: Jul-2010
Software Availability: Nov-2010

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2010
Hardware Availability: Jul-2010
Software Availability: Nov-2010

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Jul-2010

Software Availability: Nov-2010

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp_rate2006 = 134

PRIMERGY TX200 S6, Intel Xeon E5606, 2.13 GHz

SPECfp_rate_base2006 = 127

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Jul-2010

Software Availability: Nov-2010

Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:06:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 February 2011.