SPEC® CFP2006 Result
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Fujitsu

PRIMERGY TX200 S6, Intel Xeon E5507, 2.26 GHz

$SPECfp^®_rate2006 = $ 123

$SPECfp_rate_base2006 = $ 119

CPU2006 license: 19　　　　　　　　　　　　　　　　**Test date:** Aug-2010
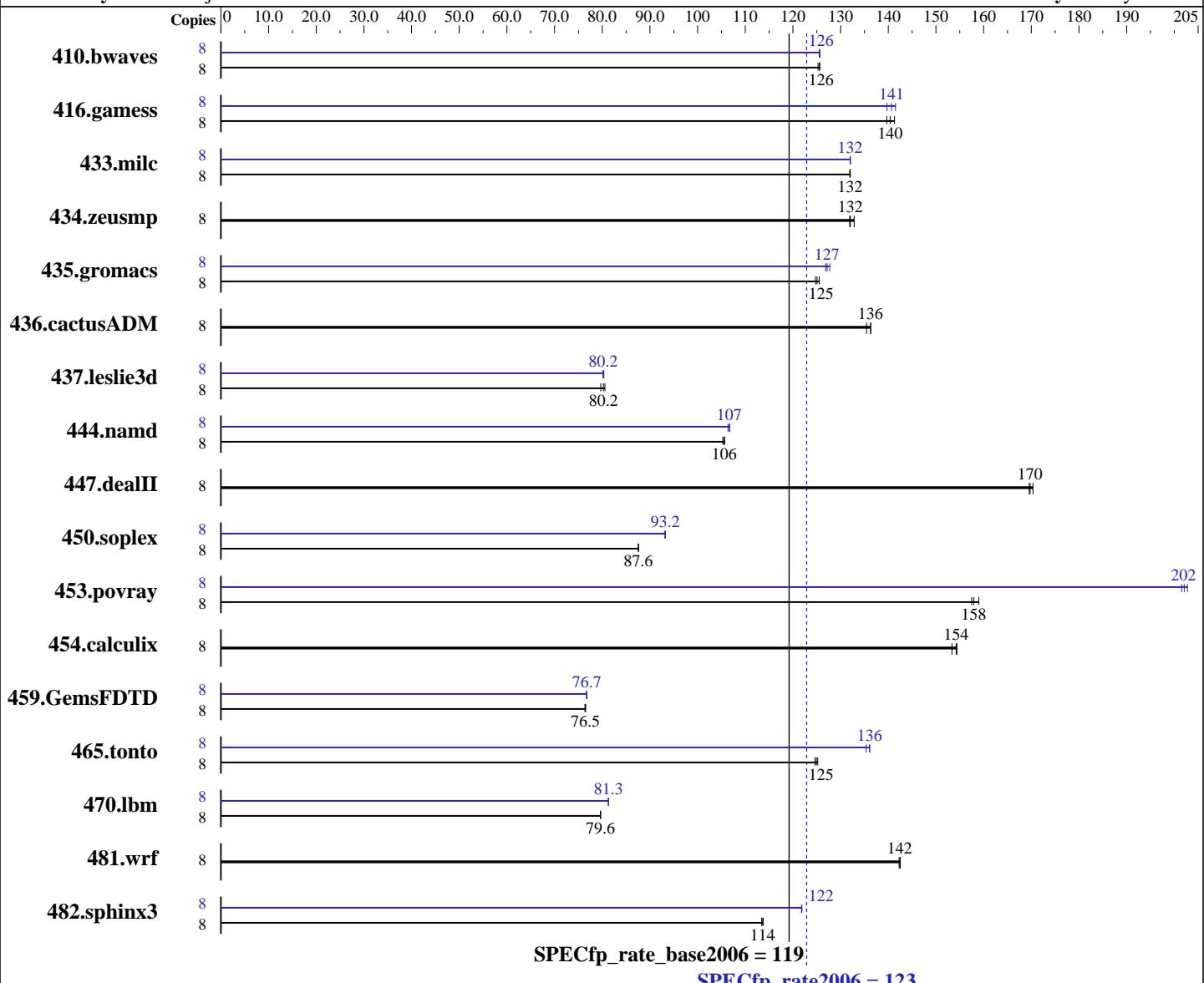**Test sponsor:** Fujitsu　　　　　　　　　　　　　**Hardware Availability:** Jul-2010
Tested by: Fujitsu　　　　　　　　　　　　　　**Software Availability:** May-2010

Copies 0　10.0　20.0　30.0　40.0　50.0　60.0　70.0　80.0　90.0　100　110　120　130　140　150　160　170　180　190　205

- 410.bwaves — 8: 126 / 8: 126
- 416.gamess — 8: 141 / 8: 140
- 433.milc — 8: 132 / 8: 132
- 434.zeusmp — 8: 132
- 435.gromacs — 8: 127 / 8: 125
- 436.cactusADM — 8: 136
- 437.leslie3d — 8: 80.2 / 8: 80.2
- 444.namd — 8: 107 / 8: 106
- 447.dealII — 8: 170
- 450.soplex — 8: 93.2 / 8: 87.6
- 453.povray — 8: 202 / 8: 158
- 454.calculix — 8: 154
- 459.GemsFDTD — 8: 76.7 / 8: 76.5
- 465.tonto — 8: 136 / 8: 125
- 470.lbm — 8: 81.3 / 8: 79.6
- 481.wrf — 8: 142
- 482.sphinx3 — 8: 122 / 8: 114

SPECfp_rate_base2006 = 119

SPECfp_rate2006 = 123

Hardware

CPU Name:	Intel Xeon E5507
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache:	4 MB I+D on chip per chip	Base Pointers:	64-bit
Other Cache:	None	Peak Pointers:	32/64-bit
Memory:	48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)	Other Software:	None
Disk Subsystem:	1 x SATA, 160 GB, 5.4 krpm		
Other Hardware:	None		

Results Table

	Base						Peak					
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	868	125	865	126	**866**	**126**	8	866	126	**866**	**126**	865	126
416.gamess	8	1108	141	1121	140	**1116**	**140**	8	1121	140	1107	142	**1113**	**141**
433.milc	8	557	132	556	132	**556**	**132**	8	556	132	556	132	**556**	**132**
434.zeusmp	8	552	132	**551**	**132**	548	133	8	552	132	**551**	**132**	548	133
435.gromacs	8	455	126	458	125	**457**	**125**	8	450	127	447	128	**449**	**127**
436.cactusADM	8	**701**	**136**	701	136	706	135	8	**701**	**136**	701	136	706	135
437.leslie3d	8	933	80.6	**937**	**80.2**	944	79.7	8	**937**	**80.2**	938	80.2	936	80.3
444.namd	8	609	105	607	106	**608**	**106**	8	**601**	**107**	603	106	601	107
447.dealII	8	537	170	540	170	**539**	**170**	8	537	170	540	170	**539**	**170**
450.soplex	8	761	87.6	762	87.6	**762**	**87.6**	8	**716**	**93.2**	716	93.2	716	93.2
453.povray	8	270	157	**269**	**158**	268	159	8	211	202	210	203	**211**	**202**
454.calculix	8	427	154	**428**	**154**	430	153	8	427	154	**428**	**154**	430	153
459.GemsFDTD	8	1112	76.4	1110	76.5	**1110**	**76.5**	8	1106	76.7	1107	76.7	**1106**	**76.7**
465.tonto	8	629	125	632	125	**630**	**125**	8	578	136	**578**	**136**	582	135
470.lbm	8	1381	79.6	**1380**	**79.6**	1380	79.7	8	1352	81.3	1352	81.3	**1352**	**81.3**
481.wrf	8	628	142	627	143	**627**	**142**	8	628	142	627	143	**627**	**142**
482.sphinx3	8	1371	114	**1372**	**114**	1375	113	8	1279	122	**1280**	**122**	1280	122

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    The system automatically configures the memory to run at 800 MHz.
    BIOS configuration:
    Data Reuse Optimization = Disable

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Test date: Aug-2010
Hardware Availability: Jul-2010
Software Availability: May-2010

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc   -m64

C++ benchmarks:
    icpc   -m64

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc   -m64 ifort -m64

Base Portability Flags

        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
         450.soplex: -DSPEC_CPU_LP64
         453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
          465.tonto: -DSPEC_CPU_LP64
           470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
        482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static

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CPU2006 license: 19		**Test date:**	Aug-2010
Test sponsor:	Fujitsu	**Hardware Availability:**	Jul-2010
Tested by:	Fujitsu	**Software Availability:**	May-2010

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
482.sphinx3: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
450.soplex: icpc -m32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

Fujitsu

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Test date: Aug-2010
Hardware Availability: Jul-2010
Software Availability: May-2010

Peak Optimization Flags (Continued)

```
     433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -fno-alias -opt-prefetch

      470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-malloc-options=3 -ansi-alias -auto-ilp32

   482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -fno-alias -auto-ilp32

   447.dealII: basepeak = yes

   450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -opt-malloc-options=3

   453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll4 -ansi-alias
```

Fortran benchmarks:

```
   410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

   416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll2 -Ob0 -ansi-alias -scalar-rep-

   434.zeusmp: basepeak = yes

   437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

 459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll2 -Ob0

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
               -unroll4 -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
   435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-prefetch -auto-ilp32
```

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

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