



# SPEC® CINT2006 Result

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## Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon X5670, 2.93GHz)

**SPECint®\_rate2006 = 357**

**SPECint\_rate\_base2006 = 334**

CPU2006 license: 97

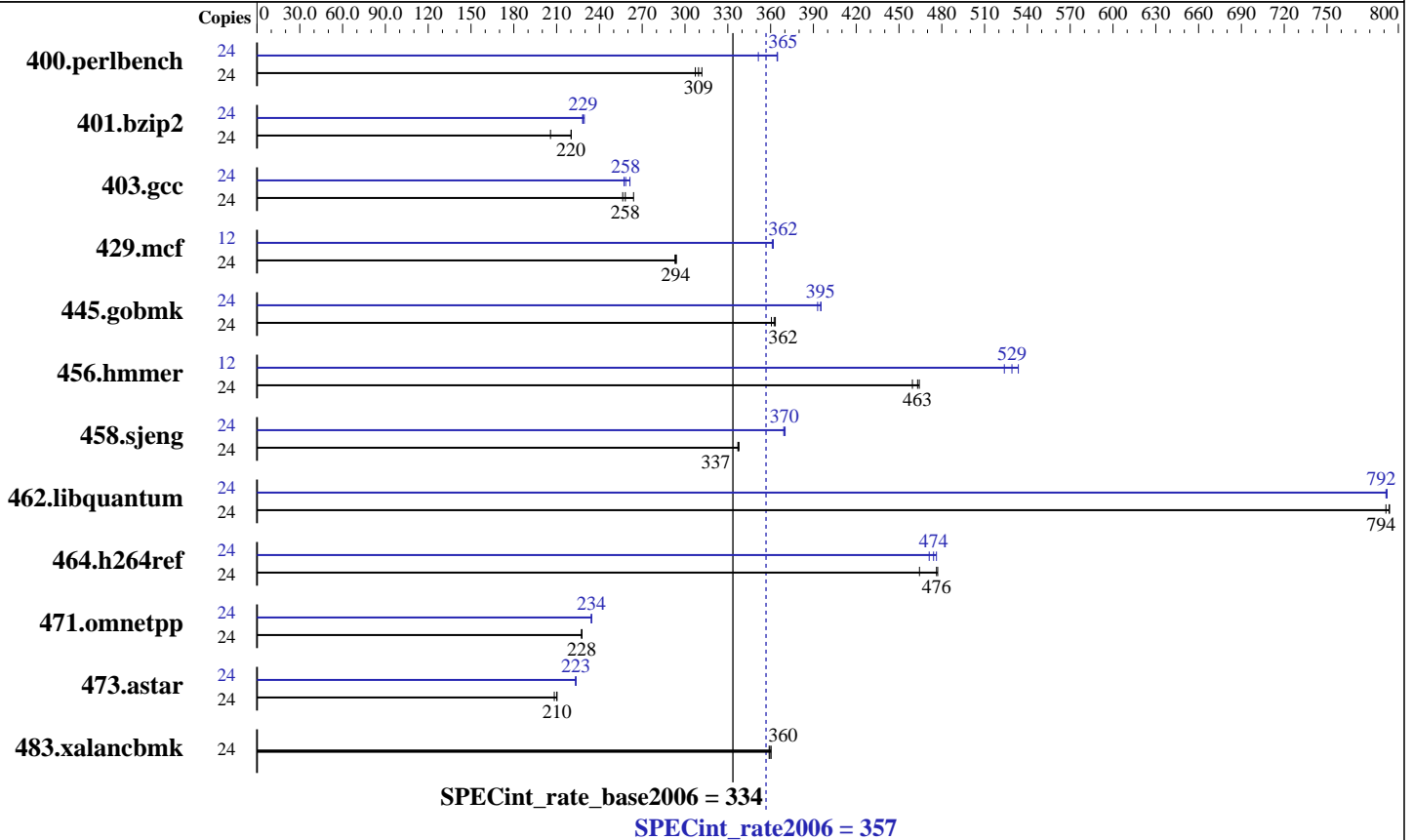
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5670  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC)  
 Disk Subsystem: 1 x 1000 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

| Benchmark      | Base   |                   |                   |                    |                   |                    |                   | Peak   |                    |                   |                   |                   |                   |                   |
|----------------|--------|-------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------|--------------------|-------------------|-------------------|-------------------|-------------------|-------------------|
|                | Copies | Seconds           | Ratio             | Seconds            | Ratio             | Seconds            | Ratio             | Copies | Seconds            | Ratio             | Seconds           | Ratio             | Seconds           | Ratio             |
| 400.perlbench  | 24     | 763               | 307               | <b><u>758</u></b>  | <b><u>309</u></b> | 752                | 312               | 24     | 667                | 351               | 643               | 365               | <b><u>643</u></b> | <b><u>365</u></b> |
| 401.bzip2      | 24     | 1126              | 206               | 1052               | 220               | <b><u>1052</u></b> | <b><u>220</u></b> | 24     | <b><u>1014</u></b> | <b><u>229</u></b> | 1009              | 229               | 1015              | 228               |
| 403.gcc        | 24     | 732               | 264               | 754                | 256               | <b><u>748</u></b>  | <b><u>258</u></b> | 24     | 739                | 261               | 751               | 257               | <b><u>748</u></b> | <b><u>258</u></b> |
| 429.mcf        | 24     | 747               | 293               | 745                | 294               | <b><u>746</u></b>  | <b><u>294</u></b> | 12     | <b><u>303</u></b>  | <b><u>362</u></b> | 302               | 362               | 303               | 361               |
| 445.gobmk      | 24     | <b><u>695</u></b> | <b><u>362</u></b> | 693                | 363               | 698                | 361               | 24     | <b><u>637</u></b>  | <b><u>395</u></b> | 641               | 393               | 637               | 395               |
| 456.hammer     | 24     | 487               | 459               | <b><u>484</u></b>  | <b><u>463</u></b> | 482                | 464               | 12     | <b><u>212</u></b>  | <b><u>529</u></b> | 214               | 524               | 210               | 534               |
| 458.sjeng      | 24     | 861               | 337               | <b><u>861</u></b>  | <b><u>337</u></b> | 860                | 338               | 24     | 787                | 369               | 784               | 370               | <b><u>786</u></b> | <b><u>370</u></b> |
| 462.libquantum | 24     | 628               | 791               | <b><u>627</u></b>  | <b><u>794</u></b> | 626                | 794               | 24     | 628                | 791               | <b><u>628</u></b> | <b><u>792</u></b> | 628               | 792               |
| 464.h264ref    | 24     | 1113              | 477               | <b><u>1115</u></b> | <b><u>476</u></b> | 1144               | 464               | 24     | <b><u>1120</u></b> | <b><u>474</u></b> | 1115              | 476               | 1127              | 471               |
| 471.omnetpp    | 24     | <b><u>659</u></b> | <b><u>228</u></b> | 659                | 227               | 659                | 228               | 24     | <b><u>640</u></b>  | <b><u>234</u></b> | 640               | 234               | 640               | 235               |
| 473.astar      | 24     | 801               | 210               | <b><u>802</u></b>  | <b><u>210</u></b> | 809                | 208               | 24     | <b><u>754</u></b>  | <b><u>223</u></b> | 755               | 223               | 754               | 224               |
| 483.xalancbmk  | 24     | 460               | 360               | 461                | 359               | <b><u>461</u></b>  | <b><u>360</u></b> | 24     | 460                | 360               | 461               | 359               | <b><u>461</u></b> | <b><u>360</u></b> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent.  
This result was measured on Gateway GW2000ht-GW170ht F1.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 11:40:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 August 2010.