



# SPEC® CFP2006 Result

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## Acer Incorporated

## SPECfp®\_rate2006 = 201

## Gateway GT150 F1 (Intel Xeon X5570)

## SPECfp\_rate\_base2006 = 195

CPU2006 license: 97

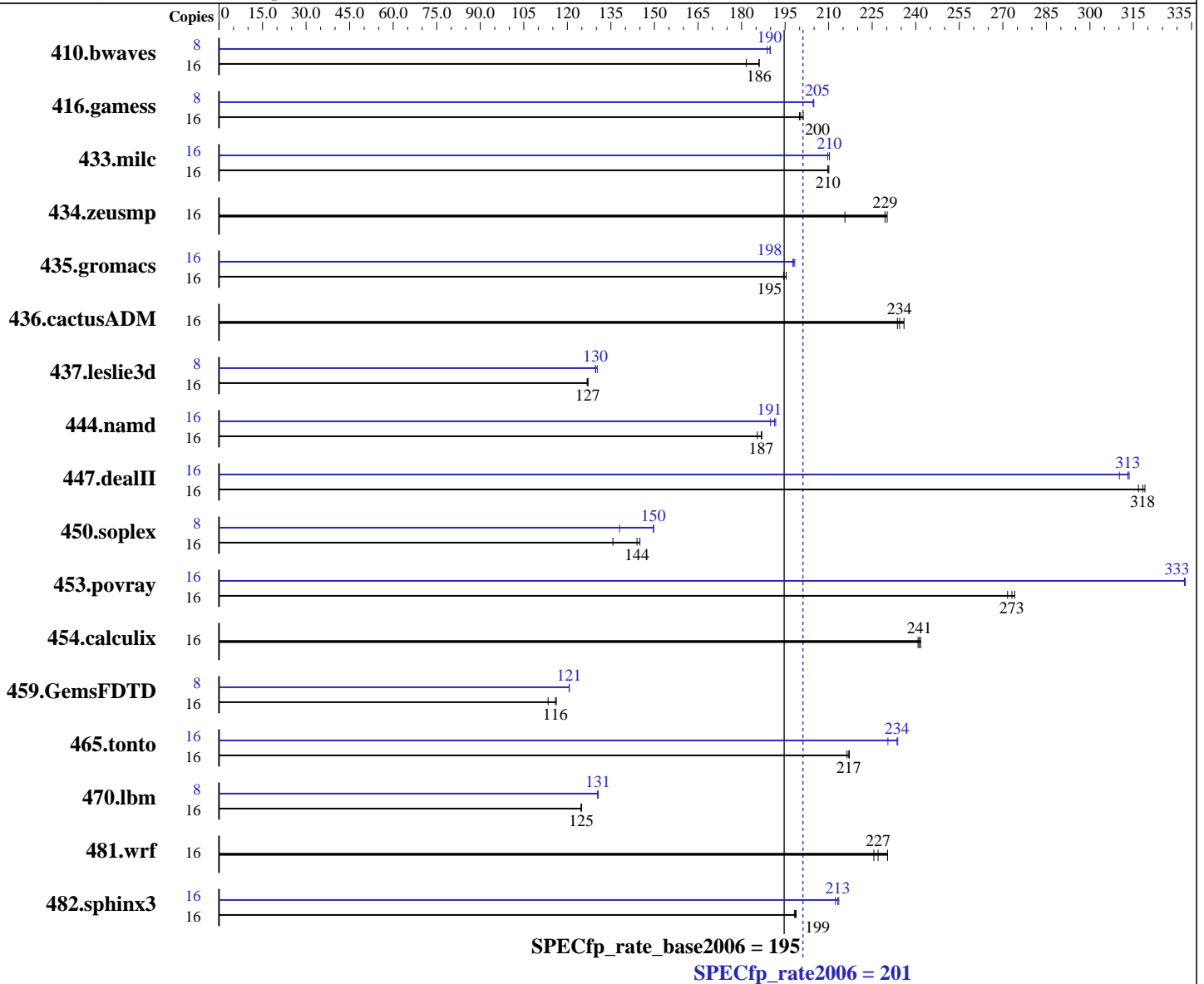
Test date: Apr-2010

Test sponsor: Acer Incorporated

Hardware Availability: May-2010

Tested by: Acer Incorporated

Software Availability: Dec-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)  
Disk Subsystem: 1000 GB SATAII, 7200 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1197	182	1168	186	<u>1169</u>	<u>186</u>	8	<u>573</u>	<u>190</u>	572	190	576	189
416.gamess	16	<u>1564</u>	<u>200</u>	1557	201	1567	200	8	765	205	<u>765</u>	<u>205</u>	764	205
433.milc	16	<u>700</u>	<u>210</u>	699	210	700	210	16	698	210	700	210	<u>699</u>	<u>210</u>
434.zeusmp	16	633	230	675	216	<u>635</u>	<u>229</u>	16	633	230	675	216	<u>635</u>	<u>229</u>
435.gromacs	16	585	195	<u>587</u>	<u>195</u>	587	194	16	578	198	576	198	<u>577</u>	<u>198</u>
436.cactusADM	16	810	236	<u>816</u>	<u>234</u>	818	234	16	810	236	<u>816</u>	<u>234</u>	818	234
437.leslie3d	16	1183	127	<u>1185</u>	<u>127</u>	1186	127	8	<u>580</u>	<u>130</u>	577	130	580	130
444.namd	16	692	185	<u>687</u>	<u>187</u>	686	187	16	676	190	669	192	<u>671</u>	<u>191</u>
447.dealII	16	<u>575</u>	<u>318</u>	578	317	574	319	16	584	314	<u>585</u>	<u>313</u>	590	310
450.soplex	16	983	136	<u>927</u>	<u>144</u>	920	145	8	483	138	<u>446</u>	<u>150</u>	446	150
453.povray	16	313	272	<u>312</u>	<u>273</u>	311	274	16	<u>256</u>	<u>333</u>	256	333	256	333
454.calculix	16	546	242	548	241	<u>547</u>	<u>241</u>	16	546	242	548	241	<u>547</u>	<u>241</u>
459.GemsFDTD	16	1497	113	<u>1465</u>	<u>116</u>	1461	116	8	<u>703</u>	<u>121</u>	704	121	703	121
465.tonto	16	<u>726</u>	<u>217</u>	725	217	728	216	16	683	230	<u>674</u>	<u>234</u>	673	234
470.lbm	16	<u>1761</u>	<u>125</u>	1763	125	1761	125	8	<u>842</u>	<u>131</u>	843	130	842	131
481.wrf	16	776	230	<u>787</u>	<u>227</u>	792	226	16	776	230	<u>787</u>	<u>227</u>	792	226
482.sphinx3	16	1573	198	<u>1571</u>	<u>199</u>	1568	199	16	1469	212	1461	214	<u>1463</u>	<u>213</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

This result was measured on the Gateway GT150 F1.  
The Acer AT150 F1 and Gateway GT150 F1 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.lelie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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## Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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