



# SPEC® CINT2006 Result

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## Fujitsu

## SPECint®\_rate2006 = 123

## CELSIUS M470, Intel Xeon W3550

## SPECint\_rate\_base2006 = 115

CPU2006 license: 19

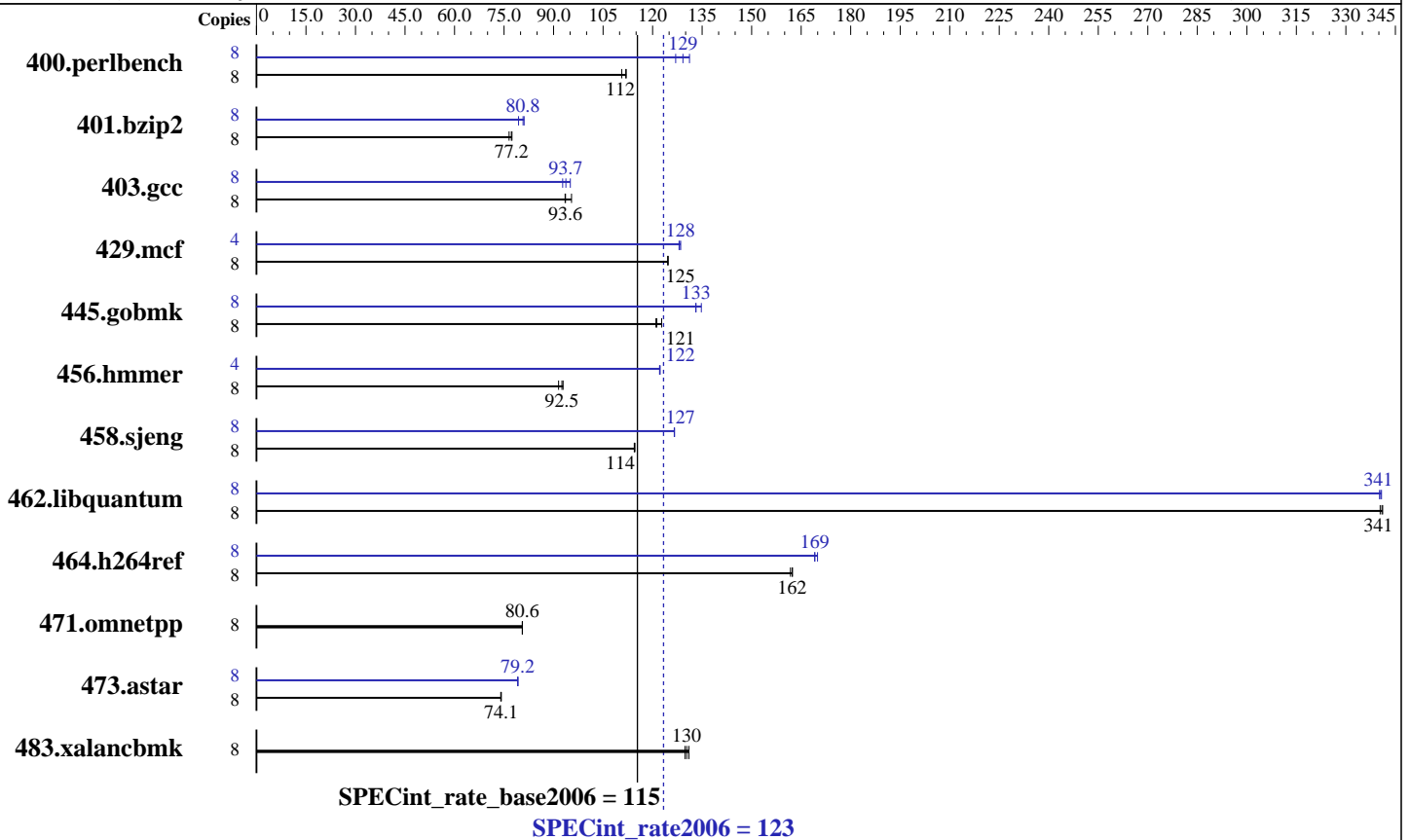
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2009

Hardware Availability: Aug-2009

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon W3550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 12 GB (6x2 GB PC3 10600E, 2 rank, CL9, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64) SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	698	112	706	111	<b>699</b>	<b>112</b>	8	<b>605</b>	<b>129</b>	596	131	615	127
401.bzip2	8	<b>1000</b>	<b>77.2</b>	1009	76.5	997	77.4	8	972	79.4	<b>956</b>	<b>80.8</b>	952	81.1
403.gcc	8	689	93.5	675	95.4	<b>688</b>	<b>93.6</b>	8	677	95.1	694	92.8	<b>687</b>	<b>93.7</b>
429.mcf	8	585	125	586	125	<b>585</b>	<b>125</b>	4	<b>285</b>	<b>128</b>	284	129	285	128
445.gobmk	8	694	121	684	123	<b>692</b>	<b>121</b>	8	623	135	631	133	<b>630</b>	<b>133</b>
456.hammer	8	804	92.9	816	91.5	<b>807</b>	<b>92.5</b>	4	306	122	<b>305</b>	<b>122</b>	305	122
458.sjeng	8	844	115	846	114	<b>846</b>	<b>114</b>	8	<b>765</b>	<b>127</b>	764	127	765	127
462.libquantum	8	486	341	<b>486</b>	<b>341</b>	487	340	8	<b>487</b>	<b>341</b>	487	340	486	341
464.h264ref	8	1094	162	1090	162	<b>1091</b>	<b>162</b>	8	1042	170	1047	169	<b>1047</b>	<b>169</b>
471.omnetpp	8	621	80.5	<b>621</b>	<b>80.6</b>	621	80.6	8	621	80.5	<b>621</b>	<b>80.6</b>	621	80.6
473.astar	8	<b>758</b>	<b>74.1</b>	757	74.2	759	74.0	8	709	79.2	710	79.1	<b>709</b>	<b>79.2</b>
483.xalancbmk	8	425	130	421	131	<b>423</b>	<b>130</b>	8	425	130	421	131	<b>423</b>	<b>130</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Memory speed set to "Max Performance" (Switch in "Advanced Memory Options")

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090915.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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