



SPEC[®] CFP2006 Result

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Bull SAS

NovaScale R480 E1
(Intel Xeon E7430, 2.13 GHz)

SPECfp[®]2006 = 19.9

SPECfp_base2006 = 18.9

CPU2006 license: 20

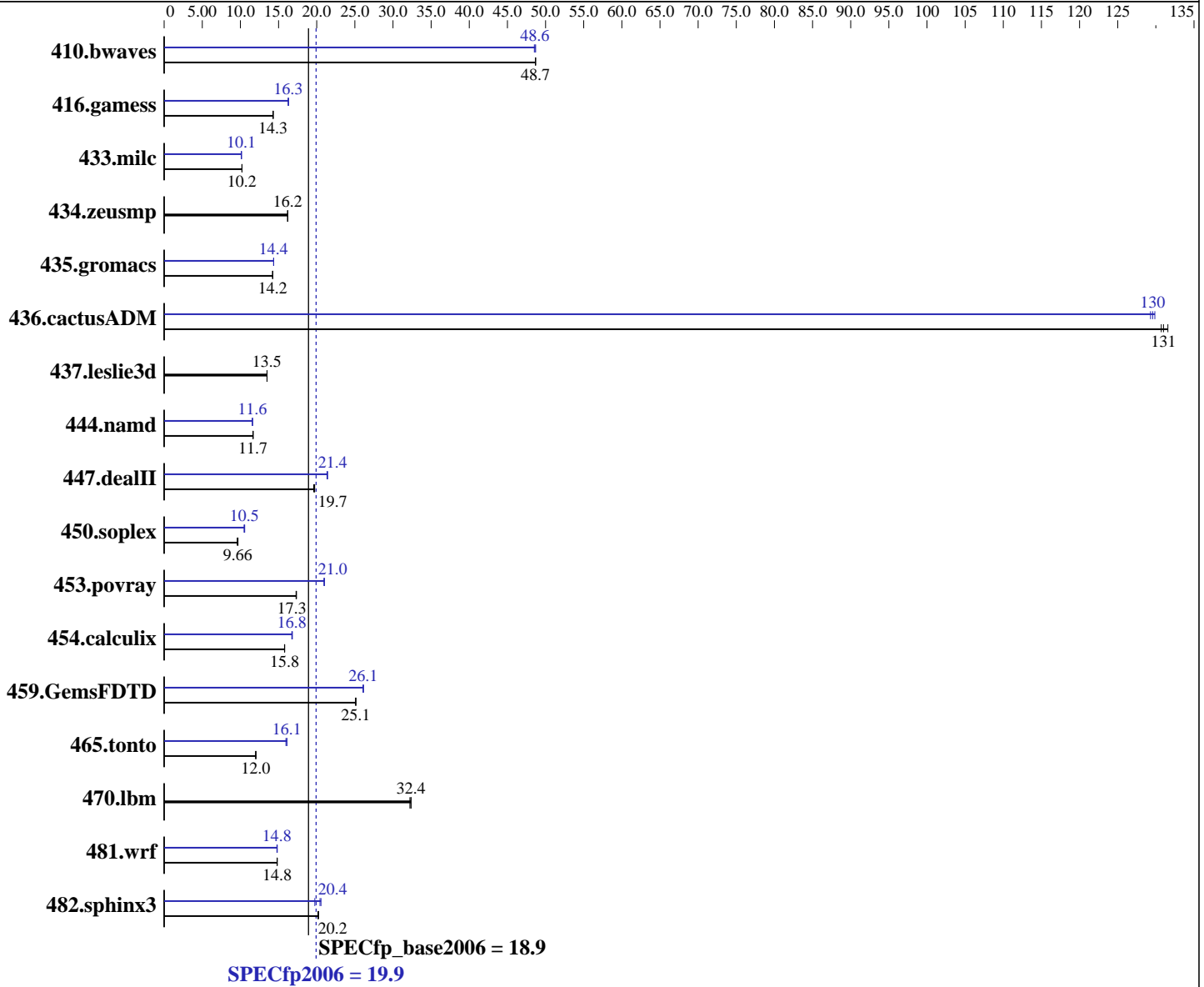
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Feb-2009

Hardware Availability: Nov-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E7430
 CPU Characteristics: 1066 MHz system bus
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 1,2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16 x 2GB DDR2-667 FBDIMM)
Disk Subsystem: 1x146 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	279	48.7	<u>279</u>	<u>48.7</u>	279	48.7	<u>279</u>	<u>48.7</u>	<u>280</u>	<u>48.6</u>	280	48.5
416.gamess	1372	14.3	<u>1371</u>	<u>14.3</u>	1369	14.3	<u>1203</u>	<u>16.3</u>	1200	16.3	1204	16.3
433.milc	900	10.2	899	10.2	<u>900</u>	<u>10.2</u>	<u>907</u>	<u>10.1</u>	908	10.1	905	10.1
434.zeusmp	562	16.2	<u>562</u>	<u>16.2</u>	562	16.2	<u>562</u>	<u>16.2</u>	<u>562</u>	<u>16.2</u>	562	16.2
435.gromacs	501	14.2	<u>502</u>	<u>14.2</u>	503	14.2	<u>497</u>	<u>14.4</u>	497	14.4	497	14.4
436.cactusADM	90.8	132	91.4	131	<u>91.2</u>	<u>131</u>	<u>92.2</u>	<u>130</u>	92.4	129	92.0	130
437.leslie3d	697	13.5	697	13.5	<u>697</u>	<u>13.5</u>	<u>697</u>	<u>13.5</u>	<u>697</u>	<u>13.5</u>	<u>697</u>	<u>13.5</u>
444.namd	688	11.7	<u>688</u>	<u>11.7</u>	688	11.6	<u>692</u>	<u>11.6</u>	<u>693</u>	<u>11.6</u>	693	11.6
447.dealII	<u>581</u>	<u>19.7</u>	583	19.6	580	19.7	<u>534</u>	<u>21.4</u>	536	21.3	534	21.4
450.soplex	861	9.68	<u>864</u>	<u>9.66</u>	871	9.58	<u>792</u>	<u>10.5</u>	790	10.6	796	10.5
453.povray	307	17.3	<u>307</u>	<u>17.3</u>	308	17.3	<u>253</u>	<u>21.0</u>	<u>254</u>	<u>21.0</u>	254	21.0
454.calculix	522	15.8	<u>523</u>	<u>15.8</u>	523	15.8	<u>491</u>	<u>16.8</u>	493	16.7	<u>492</u>	<u>16.8</u>
459.GemsFDTD	<u>422</u>	<u>25.1</u>	422	25.1	423	25.1	<u>406</u>	<u>26.1</u>	407	26.1	406	26.1
465.tonto	817	12.0	<u>819</u>	<u>12.0</u>	821	12.0	<u>616</u>	<u>16.0</u>	611	16.1	<u>612</u>	<u>16.1</u>
470.lbm	427	32.2	<u>425</u>	<u>32.4</u>	424	32.4	<u>427</u>	<u>32.2</u>	<u>425</u>	<u>32.4</u>	424	32.4
481.wrf	754	14.8	<u>754</u>	<u>14.8</u>	754	14.8	<u>755</u>	<u>14.8</u>	753	14.8	755	14.8
482.sphinx3	962	20.3	966	20.2	<u>966</u>	<u>20.2</u>	<u>948</u>	<u>20.6</u>	987	19.7	<u>954</u>	<u>20.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Enabled
Hardware Prefetcher = Enabled



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General Notes

The NEC Express5800/R140a-4(Intel Xeon E7430) and the Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) models are electronically equivalent. The results have been measured on a Bull NovaScale R480 E1(Intel Xeon E7430, 2.13 GHz) model.

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort
Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Continued on next page



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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.00.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.00.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.xml>

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 23:27:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 31 March 2009.