



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3500 (Intel Xeon E5410)

## SPECint\_rate\_base2006 = 112

CPU2006 license: 11

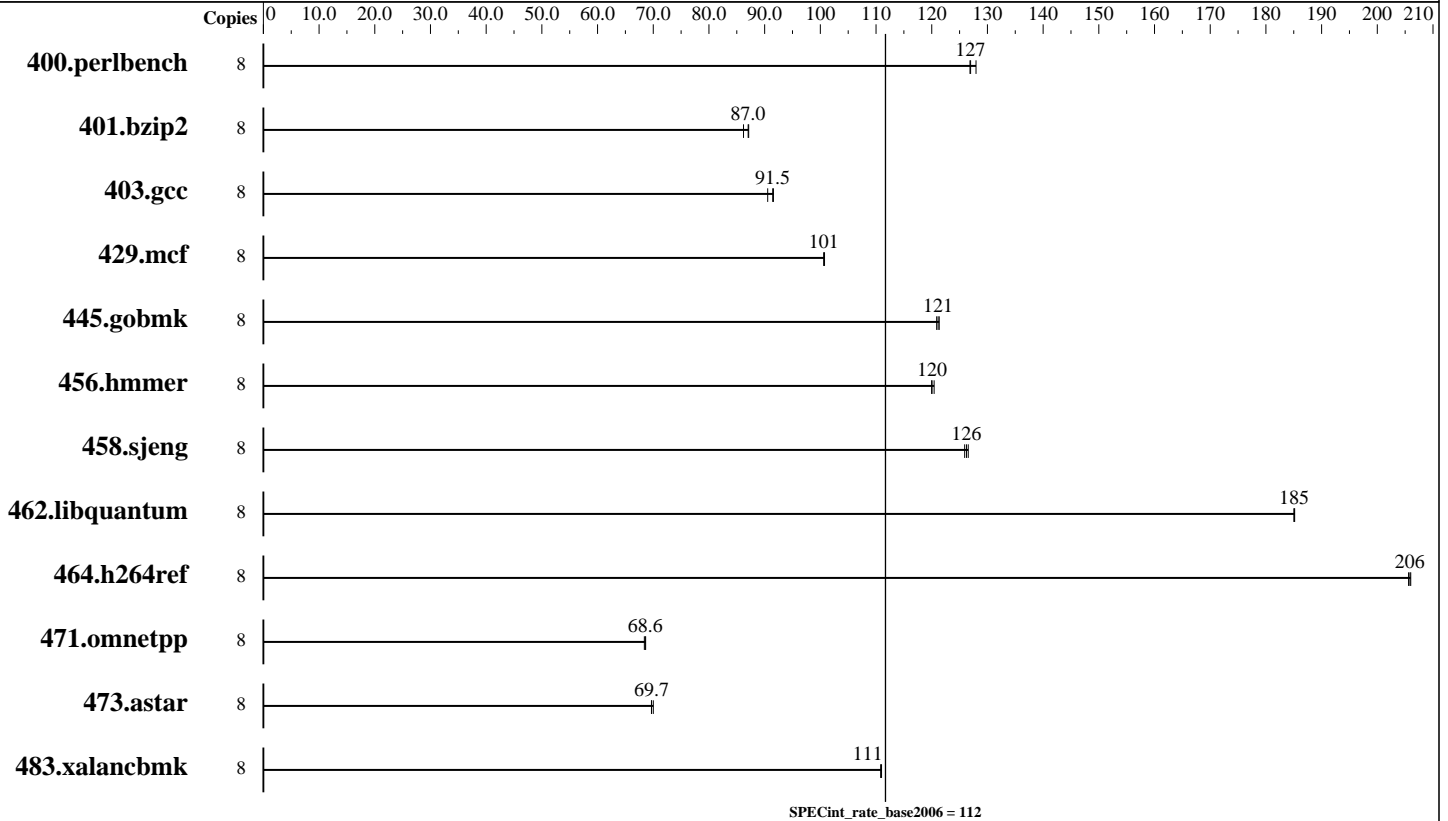
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2009

Hardware Availability: Jan-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	616	127	<b>616</b>	<b>127</b>	611	128							
401.bzip2	8	896	86.2	886	87.1	<b>887</b>	<b>87.0</b>							
403.gcc	8	711	90.5	<b>704</b>	<b>91.5</b>	703	91.6							
429.mcf	8	725	101	<b>725</b>	<b>101</b>	724	101							
445.gobmk	8	694	121	692	121	<b>693</b>	<b>121</b>							
456.hammer	8	620	120	622	120	<b>622</b>	<b>120</b>							
458.sjeng	8	<b>767</b>	<b>126</b>	765	127	769	126							
462.libquantum	8	896	185	<b>896</b>	<b>185</b>	895	185							
464.h264ref	8	861	206	<b>860</b>	<b>206</b>	859	206							
471.omnetpp	8	729	68.6	<b>729</b>	<b>68.6</b>	731	68.4							
473.astar	8	806	69.7	<b>806</b>	<b>69.7</b>	802	70.0							
483.xalancbmk	8	497	111	<b>498</b>	<b>111</b>	498	111							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores

## General Notes

Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
'ulimit -s unlimited' was used to set the stack size to unlimited prior to the run

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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