



SPEC[®] CINT2006 Result

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ACTION S.A.

SPECint[®]_rate2006 = 113

ACTINA SOLAR 222 X2 (Intel Xeon E5410, 2.33 GHz)

SPECint_rate_base2006 = 105

CPU2006 license: 9008

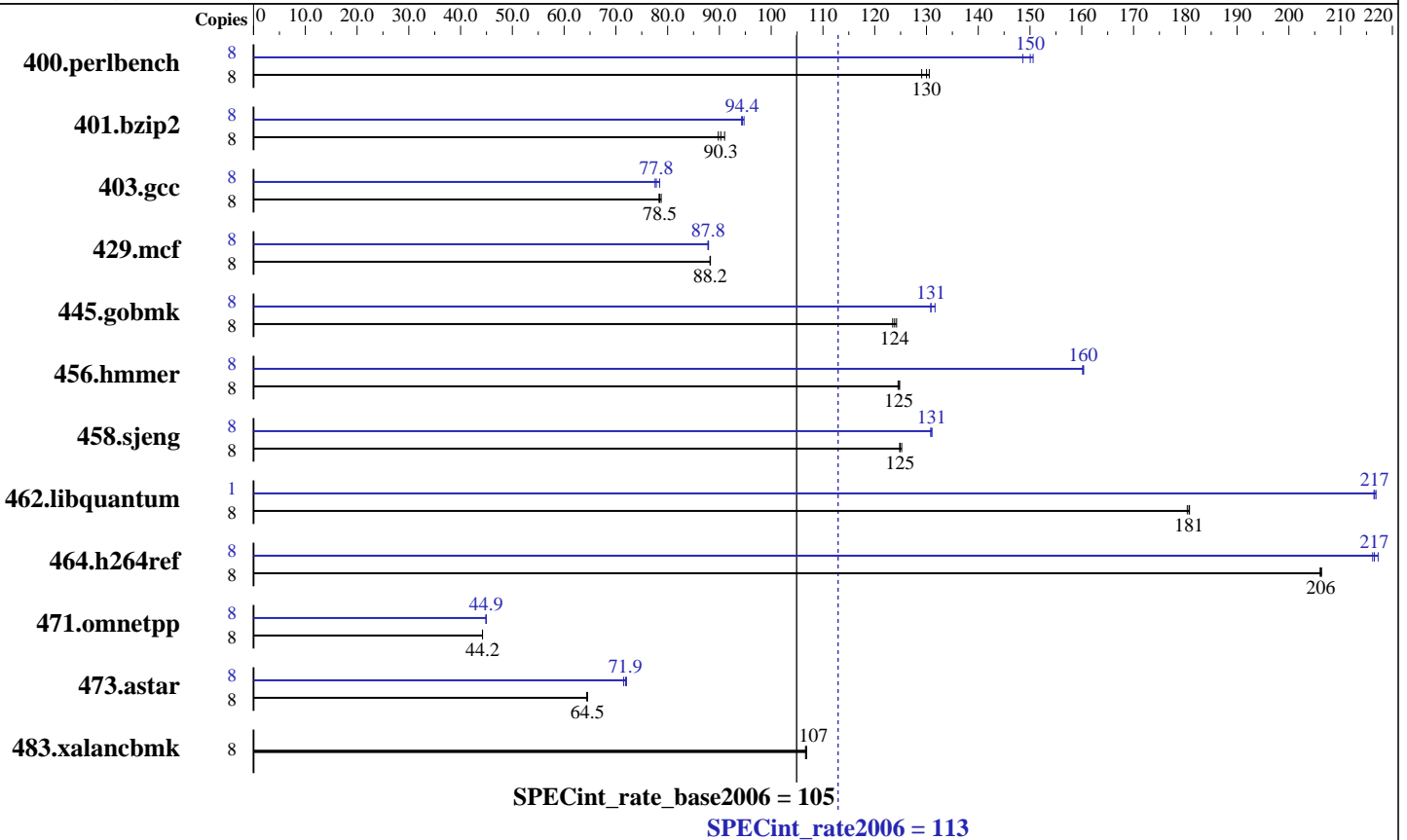
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5410
 CPU Characteristics: 1333 MHz System Bus
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x 2 GB, PC2-5300, CL 5-5-5, FB ECC)
 Disk Subsystem: 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64)
 SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	599	131	601	130	606	129	8	526	149	519	151	521	150
401.bzip2	8	848	91.0	860	89.8	855	90.3	8	815	94.8	817	94.4	819	94.3
403.gcc	8	821	78.5	822	78.3	818	78.8	8	821	78.5	830	77.6	828	77.8
429.mcf	8	827	88.2	827	88.3	827	88.2	8	831	87.8	831	87.8	830	87.9
445.gobmk	8	676	124	678	124	680	123	8	641	131	637	132	641	131
456.hammer	8	599	125	600	125	598	125	8	466	160	466	160	465	160
458.sjeng	8	773	125	775	125	776	125	8	739	131	740	131	739	131
462.libquantum	8	919	180	917	181	917	181	1	95.7	216	95.7	217	95.6	217
464.h264ref	8	859	206	859	206	858	206	8	818	217	815	217	819	216
471.omnetpp	8	1130	44.2	1130	44.2	1131	44.2	8	1112	45.0	1113	44.9	1113	44.9
473.astar	8	871	64.5	871	64.5	872	64.4	8	785	71.5	781	71.9	779	72.0
483.xalancbmk	8	517	107	518	107	517	107	8	517	107	518	107	517	107

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Platform Notes

This result is measured on ACTINA SOLAR 222 X2.
Note that the ACTINA SOLAR 222 X2 and ACTINA SOLAR 212 X2 are electrically equivalent.

General Notes

'taskset' was used to bind processes to cores except for 462.libquantum peak
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>

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For other inquiries, please contact webmaster@spec.org.

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