SPEC® CINT2006 Result

Fujitsu Siemens Computers

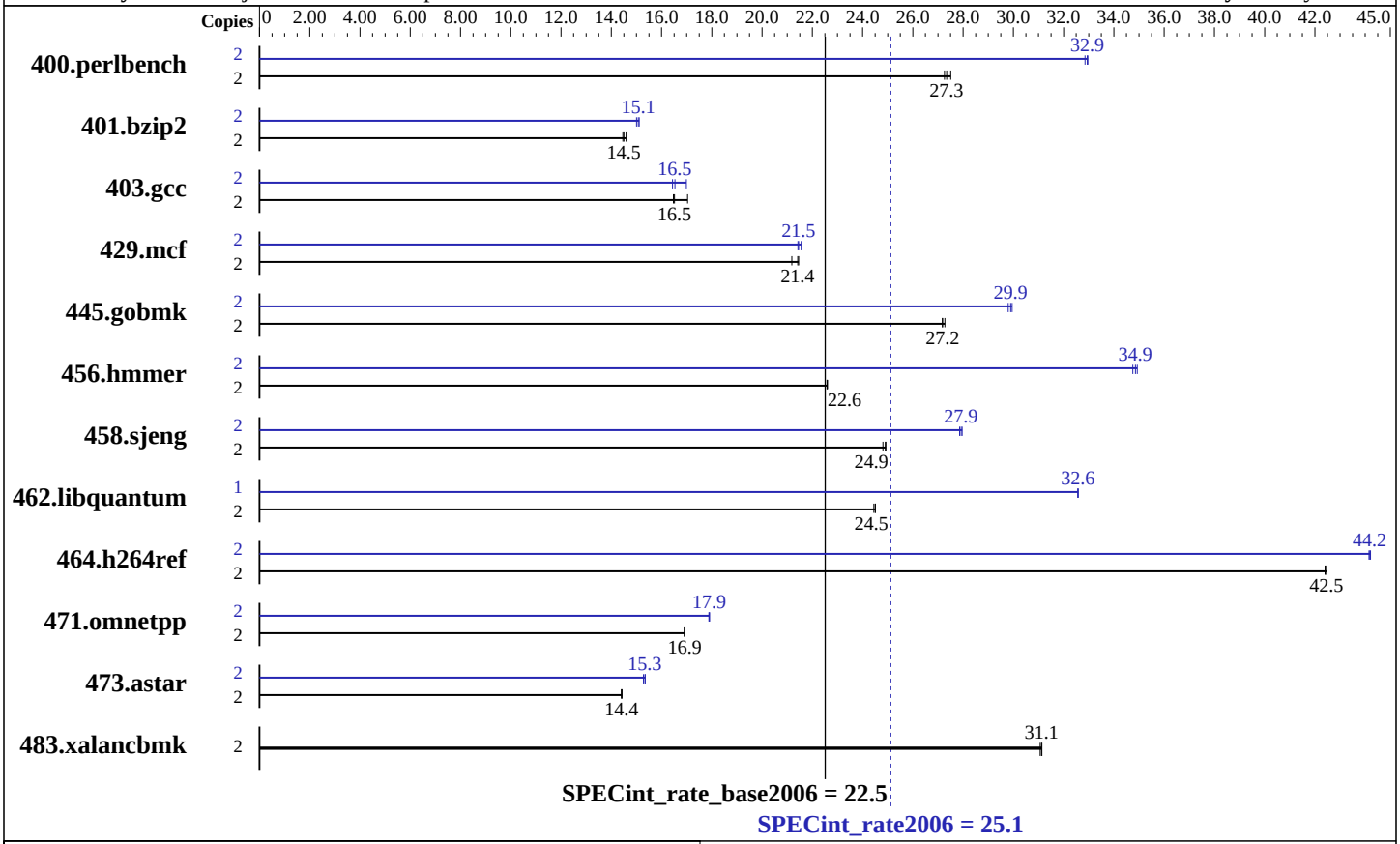
PRIMERGY RX100 S5, Intel Pentium Dual Core E2200, 2.20 GHz

SPECint®_rate2006 = 25.1

SPECint_rate_base2006 = 22.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: May-2008



Hardware

CPU Name:	Intel Pentium Dual Core E2200
CPU Characteristics:	800 MHz system bus
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL 6-6-6, ECC)
Disk Subsystem:	1x SATA, 160 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	710	27.5	**715**	**27.3**	717	27.3	2	595	32.9	**593**	**32.9**	593	33.0
401.bzip2	2	1335	14.5	1323	14.6	**1331**	**14.5**	2	**1280**	**15.1**	1286	15.0	1277	15.1
403.gcc	2	**975**	**16.5**	945	17.0	977	16.5	2	948	17.0	**974**	**16.5**	980	16.4
429.mcf	2	861	21.2	850	21.5	**852**	**21.4**	2	846	21.6	851	21.4	**850**	**21.5**
445.gobmk	2	769	27.3	772	27.2	**772**	**27.2**	2	700	30.0	704	29.8	**702**	**29.9**
456.hmmer	2	826	22.6	826	22.6	**826**	**22.6**	2	**535**	**34.9**	537	34.8	534	34.9
458.sjeng	2	975	24.8	971	24.9	**972**	**24.9**	2	865	28.0	869	27.9	**867**	**27.9**
462.libquantum	2	1691	24.5	**1691**	**24.5**	1695	24.4	1	636	32.6	636	32.6	**636**	**32.6**
464.h264ref	2	1044	42.4	1042	42.5	**1042**	**42.5**	2	**1002**	**44.2**	1001	44.2	1002	44.2
471.omnetpp	2	738	16.9	**739**	**16.9**	740	16.9	2	698	17.9	699	17.9	**698**	**17.9**
473.astar	2	**974**	**14.4**	973	14.4	975	14.4	2	919	15.3	**915**	**15.3**	915	15.3
483.xalancbmk	2	443	31.1	**443**	**31.1**	444	31.1	2	443	31.1	**443**	**31.1**	444	31.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

C++ benchmarks:

```
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

401.bzip2: icc

456.hmmer: icc

C++ benchmarks:
/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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