



SPEC® CINT2006 Result

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NEC Corporation

Express5800/110Ri-1
(Intel Xeon 3065)

SPECint®2006 = 19.9

SPECint_base2006 = 17.2

CPU2006 license: 9006

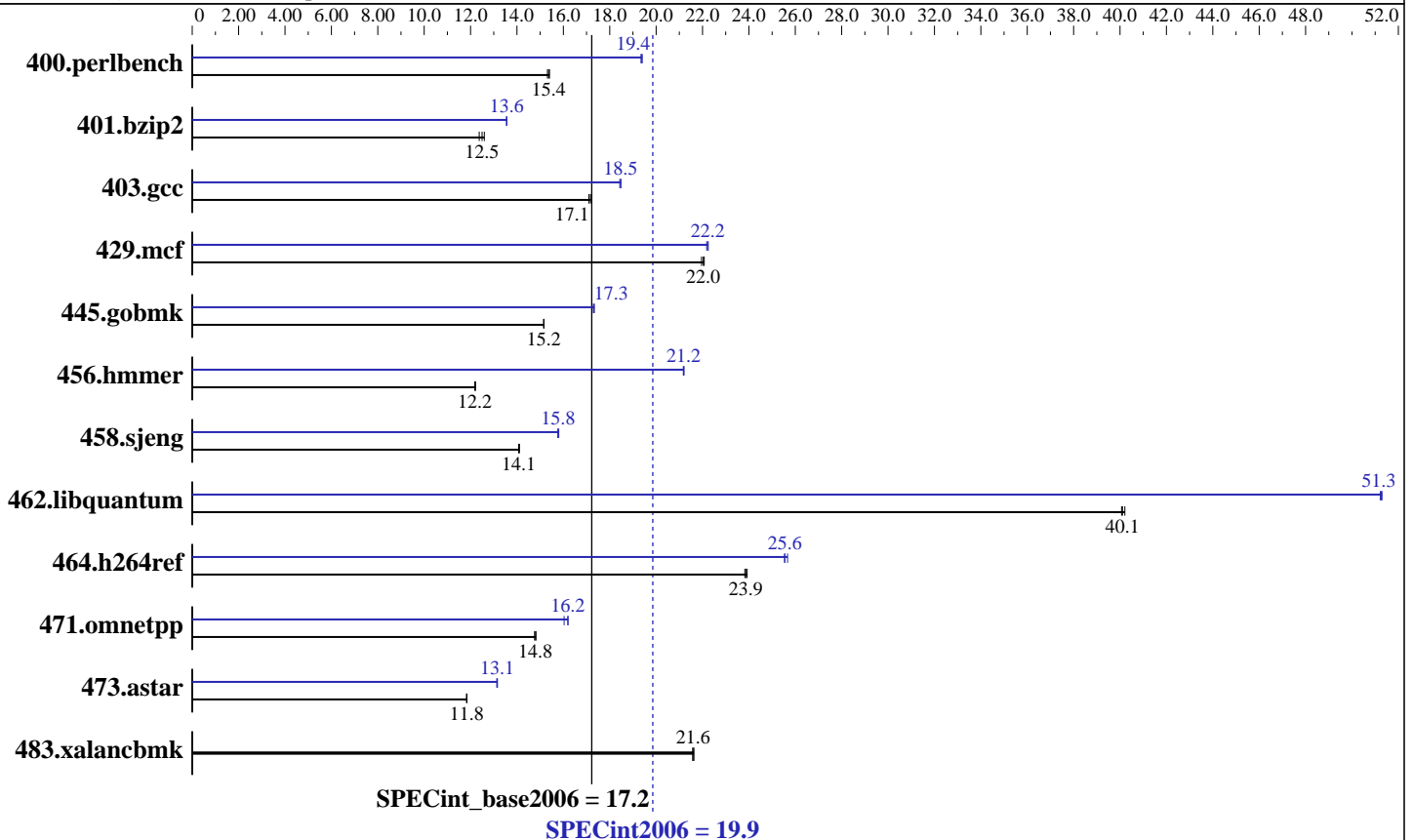
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 3065
 CPU Characteristics: 2.33 GHz, 4 MB L2, 1333 MHz bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
 Disk Subsystem: 1x80.0 GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	638	15.3	<u>636</u>	<u>15.4</u>	634	15.4	<u>504</u>	<u>19.4</u>	504	19.4	505	19.3
401.bzip2	780	12.4	766	12.6	<u>772</u>	<u>12.5</u>	712	13.6	712	13.6	<u>712</u>	<u>13.6</u>
403.gcc	469	17.2	<u>470</u>	<u>17.1</u>	471	17.1	437	18.4	<u>436</u>	<u>18.5</u>	436	18.5
429.mcf	<u>414</u>	<u>22.0</u>	415	22.0	413	22.1	<u>410</u>	<u>22.2</u>	411	22.2	410	22.2
445.gobmk	692	15.2	692	15.2	<u>692</u>	<u>15.2</u>	605	17.3	606	17.3	<u>606</u>	<u>17.3</u>
456.hmmer	765	12.2	<u>765</u>	<u>12.2</u>	764	12.2	441	21.2	440	21.2	<u>440</u>	<u>21.2</u>
458.sjeng	<u>859</u>	<u>14.1</u>	860	14.1	858	14.1	768	15.8	766	15.8	<u>767</u>	<u>15.8</u>
462.libquantum	517	40.1	515	40.2	<u>517</u>	<u>40.1</u>	<u>404</u>	<u>51.3</u>	405	51.2	404	51.3
464.h264ref	929	23.8	925	23.9	<u>927</u>	<u>23.9</u>	867	25.5	862	25.7	<u>866</u>	<u>25.6</u>
471.omnetpp	422	14.8	<u>422</u>	<u>14.8</u>	423	14.8	390	16.0	386	16.2	<u>386</u>	<u>16.2</u>
473.astar	<u>593</u>	<u>11.8</u>	593	11.8	593	11.8	534	13.2	<u>534</u>	<u>13.1</u>	535	13.1
483.xalancbmk	319	21.6	320	21.6	<u>320</u>	<u>21.6</u>	319	21.6	320	21.6	<u>320</u>	<u>21.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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