



# SPEC<sup>®</sup> CINT2006 Result

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## Acer Incorporated

## SPECint<sup>®</sup>\_rate2006 = 115

## Acer Altos R520 (Intel Xeon X5365)

## SPECint\_rate\_base2006 = 96.4

CPU2006 license: 97

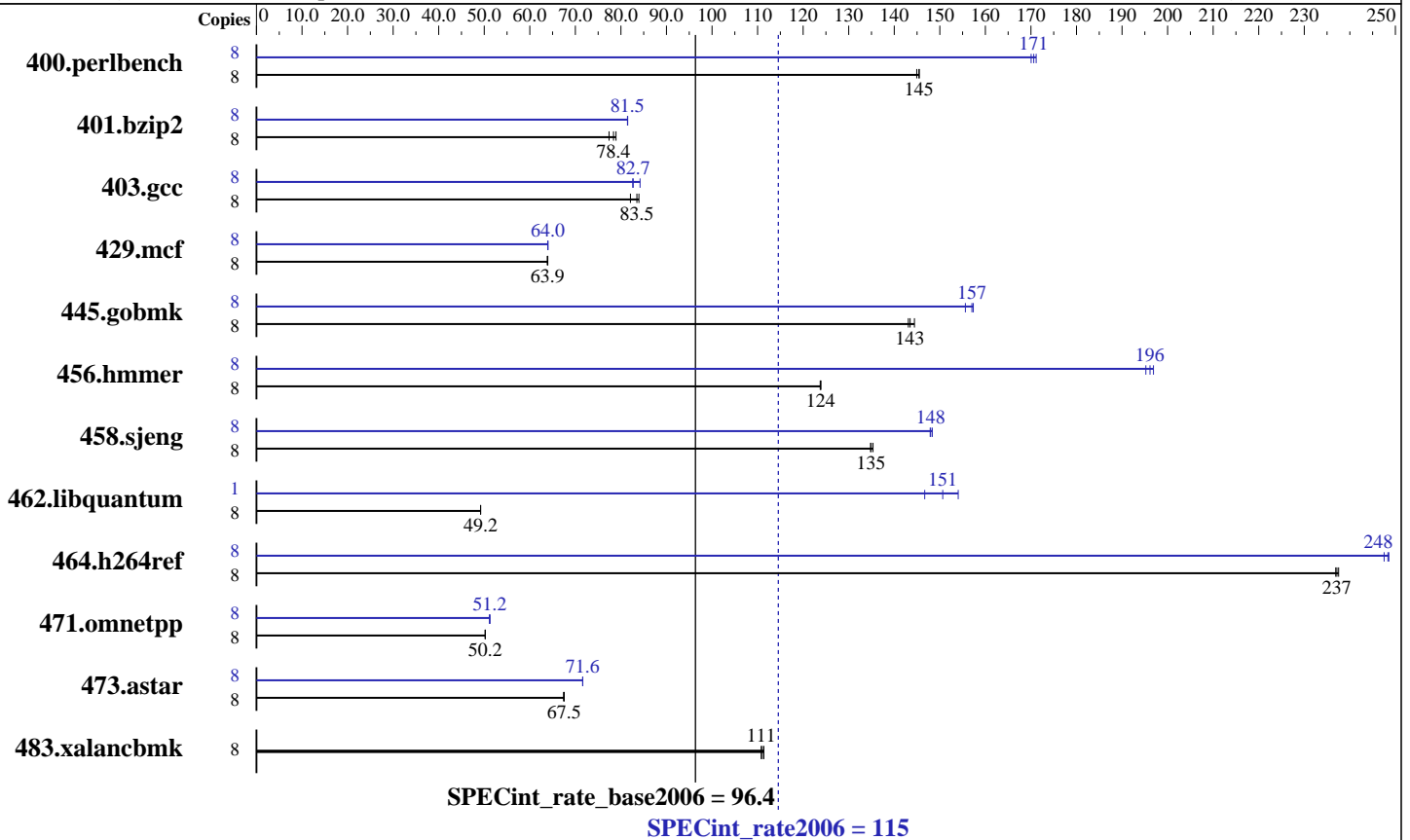
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b>538</b>	<b>145</b>	539	145	537	145	8	<b>458</b>	<b>171</b>	457	171	460	170
401.bzip2	8	<b>985</b>	<b>78.4</b>	978	78.9	998	77.4	8	947	81.5	948	81.4	<b>948</b>	<b>81.5</b>
403.gcc	8	<b>771</b>	<b>83.5</b>	767	83.9	785	82.1	8	780	82.6	765	84.2	<b>779</b>	<b>82.7</b>
429.mcf	8	<b>1142</b>	<b>63.9</b>	1143	63.8	1141	64.0	8	<b>1141</b>	<b>64.0</b>	1140	64.0	1141	63.9
445.gobmk	8	<b>585</b>	<b>143</b>	581	144	586	143	8	539	156	533	157	<b>534</b>	<b>157</b>
456.hmmer	8	602	124	<b>603</b>	<b>124</b>	603	124	8	<b>381</b>	<b>196</b>	382	195	379	197
458.sjeng	8	<b>717</b>	<b>135</b>	715	135	718	135	8	653	148	655	148	<b>654</b>	<b>148</b>
462.libquantum	8	<b>3369</b>	<b>49.2</b>	3369	49.2	3369	49.2	1	141	147	135	154	<b>138</b>	<b>151</b>
464.h264ref	8	<b>746</b>	<b>237</b>	747	237	745	238	8	<b>713</b>	<b>248</b>	712	249	715	248
471.omnetpp	8	996	50.2	995	50.3	<b>995</b>	<b>50.2</b>	8	978	51.1	<b>976</b>	<b>51.2</b>	976	51.2
473.astar	8	831	67.6	833	67.4	<b>832</b>	<b>67.5</b>	8	<b>784</b>	<b>71.6</b>	784	71.6	784	71.6
483.xalancbmk	8	496	111	498	111	<b>497</b>	<b>111</b>	8	496	111	498	111	<b>497</b>	<b>111</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

### BIOS Setting

Hardware Prefetcher set to ON

Adjacent Cache Line Prefetch set to OFF

Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.38.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.38.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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