



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5140,
2.33 GHz

SPECint®2006 = 15.9

SPECint_base2006 = 15.0

CPU2006 license: 22

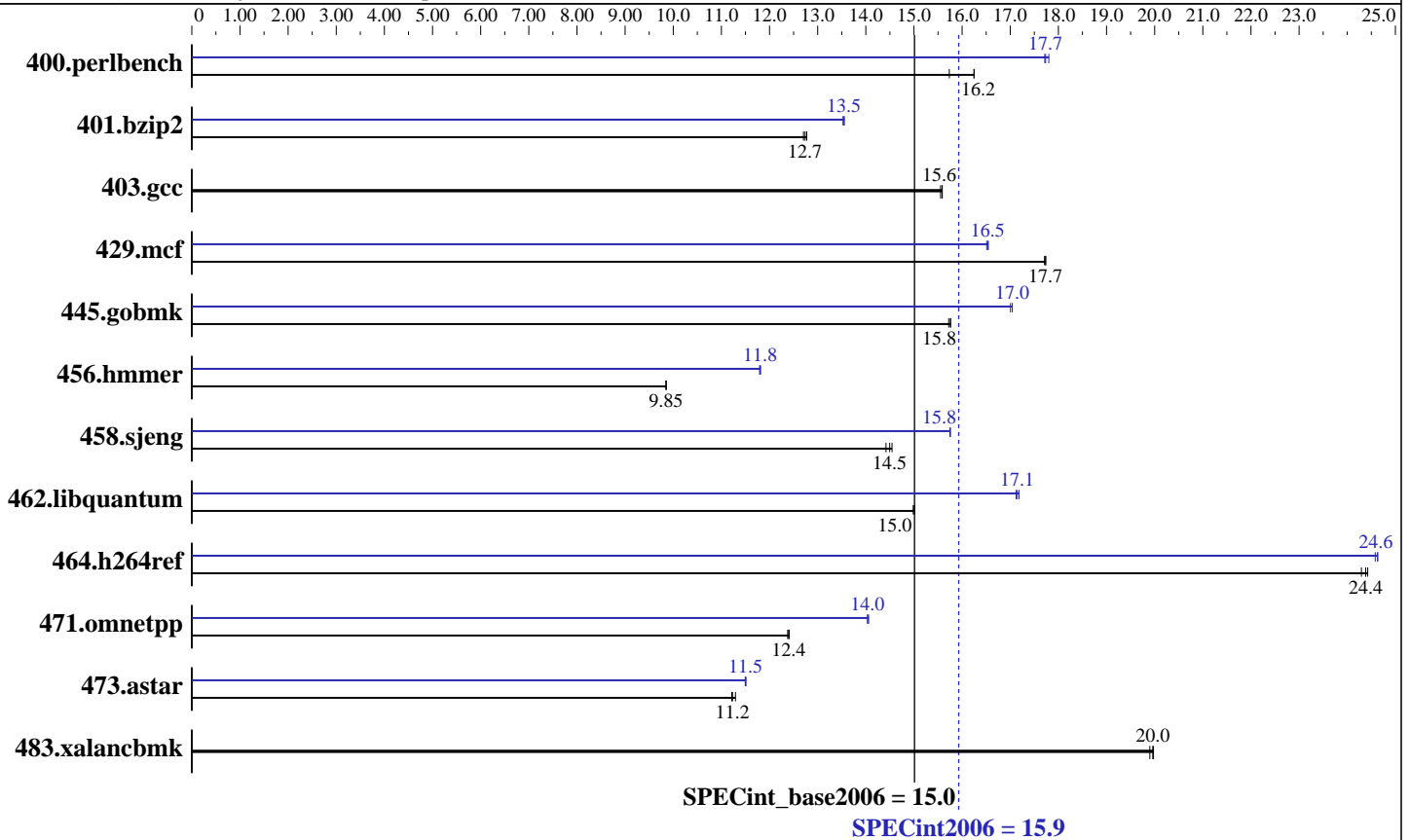
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jul-2006

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon 5140
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: SAS (73GB 15400 rpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|---------|-------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 621 | 15.7 | 601 | 16.2 | <u>601</u> | <u>16.2</u> | 551 | 17.7 | 549 | 17.8 | <u>551</u> | <u>17.7</u> |
| 401.bzip2 | 760 | 12.7 | 755 | 12.8 | <u>757</u> | <u>12.7</u> | <u>712</u> | <u>13.5</u> | 712 | 13.6 | 713 | 13.5 |
| 403.gcc | 518 | 15.6 | <u>517</u> | <u>15.6</u> | 516 | 15.6 | 518 | 15.6 | <u>517</u> | <u>15.6</u> | 516 | 15.6 |
| 429.mcf | 515 | 17.7 | 514 | 17.7 | <u>514</u> | <u>17.7</u> | <u>552</u> | <u>16.5</u> | 551 | 16.5 | 552 | 16.5 |
| 445.gobmk | 667 | 15.7 | <u>666</u> | <u>15.8</u> | 666 | 15.8 | 617 | 17.0 | 616 | 17.0 | <u>616</u> | <u>17.0</u> |
| 456.hmmmer | 947 | 9.85 | <u>947</u> | <u>9.85</u> | 947 | 9.85 | <u>790</u> | <u>11.8</u> | 790 | 11.8 | 791 | 11.8 |
| 458.sjeng | 839 | 14.4 | <u>835</u> | <u>14.5</u> | 832 | 14.5 | <u>768</u> | <u>15.8</u> | 768 | 15.8 | 768 | 15.7 |
| 462.libquantum | 1383 | 15.0 | <u>1381</u> | <u>15.0</u> | 1380 | 15.0 | 1206 | 17.2 | <u>1208</u> | <u>17.1</u> | 1210 | 17.1 |
| 464.h264ref | 911 | 24.3 | 906 | 24.4 | <u>908</u> | <u>24.4</u> | <u>898</u> | <u>24.6</u> | 900 | 24.6 | 898 | 24.6 |
| 471.omnetpp | 505 | 12.4 | 504 | 12.4 | <u>504</u> | <u>12.4</u> | <u>445</u> | <u>14.0</u> | 445 | 14.1 | 445 | 14.0 |
| 473.astar | 626 | 11.2 | <u>625</u> | <u>11.2</u> | 622 | 11.3 | <u>610</u> | <u>11.5</u> | 611 | 11.5 | 610 | 11.5 |
| 483.xalancbmk | 347 | 19.9 | 345 | 20.0 | <u>346</u> | <u>20.0</u> | 347 | 19.9 | 345 | 20.0 | <u>346</u> | <u>20.0</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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For other inquiries, please contact webmaster@spec.org.

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