SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY RX300 S3, Intel Xeon processor 5110, 1.60 GHz

SPECint®_rate2006 = 37.2
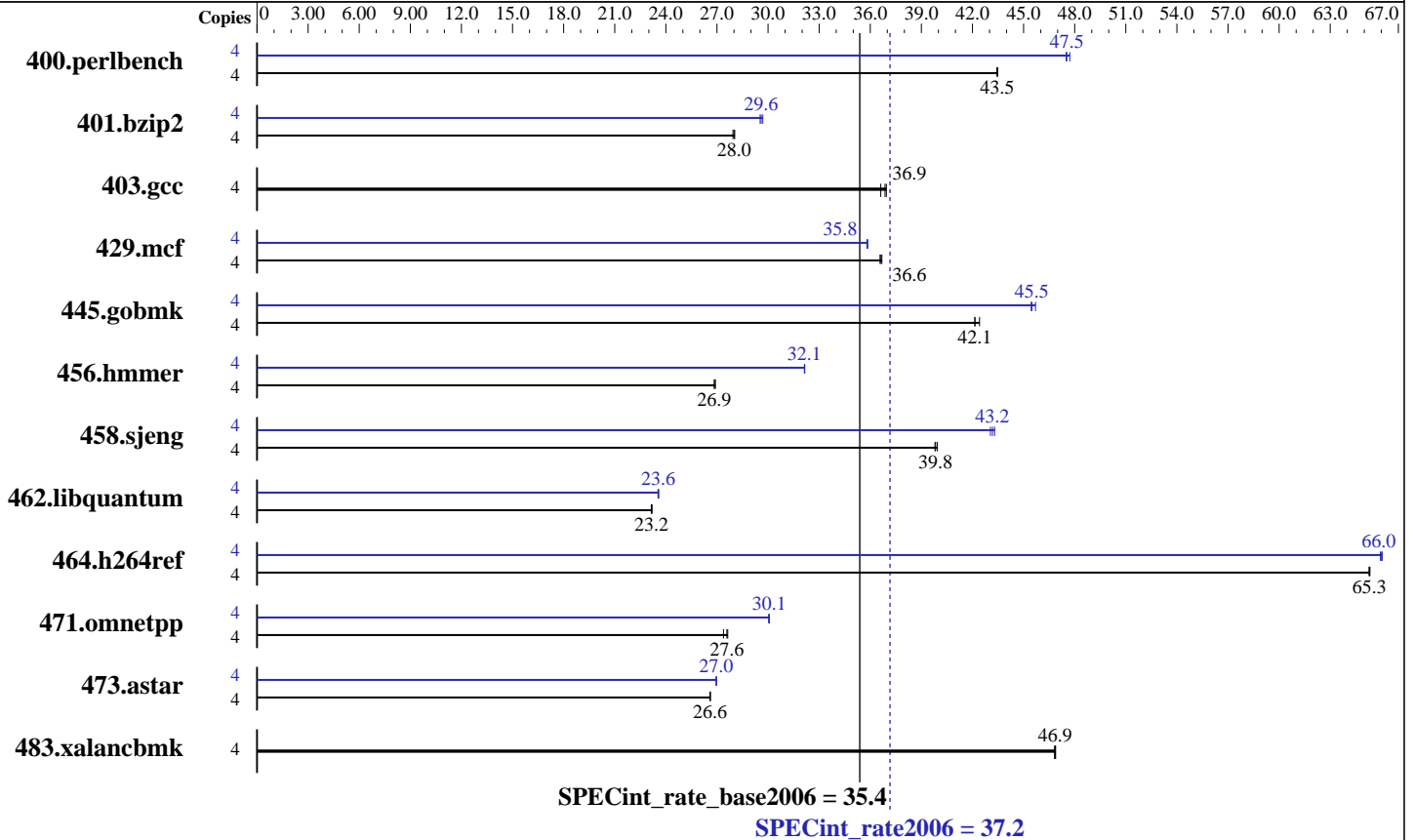
SPECint_rate_base2006 = 35.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007

Copies	Benchmark	Value
4	400.perlbench	47.5
4	400.perlbench	43.5
4	401.bzip2	29.6
4	401.bzip2	28.0
4	403.gcc	36.9
4	429.mcf	35.8
4	429.mcf	36.6
4	445.gobmk	45.5
4	445.gobmk	42.1
4	456.hmmer	32.1
4	456.hmmer	26.9
4	458.sjeng	43.2
4	458.sjeng	39.8
4	462.libquantum	23.6
4	462.libquantum	23.2
4	464.h264ref	66.0
4	464.h264ref	65.3
4	471.omnetpp	30.1
4	471.omnetpp	27.6
4	473.astar	27.0
4	473.astar	26.6
4	483.xalancbmk	46.9

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Hardware

CPU Name: Intel Xeon 5110
CPU Characteristics: 1067 MHz system bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	899	43.5	**899**	**43.5**	900	43.4	4	823	47.5	819	47.7	**822**	**47.5**
401.bzip2	4	**1378**	**28.0**	1376	28.0	1382	27.9	4	1307	29.5	1300	29.7	**1304**	**29.6**
403.gcc	4	879	36.6	872	36.9	**873**	**36.9**	4	879	36.6	872	36.9	**873**	**36.9**
429.mcf	4	994	36.7	**996**	**36.6**	998	36.6	4	**1019**	**35.8**	1017	35.9	1019	35.8
445.gobmk	4	989	42.4	**996**	**42.1**	996	42.1	4	**922**	**45.5**	918	45.7	923	45.4
456.hmmer	4	**1390**	**26.9**	1387	26.9	1390	26.8	4	1161	32.1	**1161**	**32.1**	1161	32.2
458.sjeng	4	**1215**	**39.8**	1216	39.8	1212	39.9	4	1124	43.1	**1121**	**43.2**	1118	43.3
462.libquantum	4	3581	23.1	**3580**	**23.2**	3573	23.2	4	3515	23.6	3521	23.5	**3517**	**23.6**
464.h264ref	4	1356	65.3	**1355**	**65.3**	1355	65.3	4	1340	66.1	1342	65.9	**1341**	**66.0**
471.omnetpp	4	913	27.4	**906**	**27.6**	905	27.6	4	832	30.0	831	30.1	**832**	**30.1**
473.astar	4	1056	26.6	**1056**	**26.6**	1055	26.6	4	1041	27.0	1042	27.0	**1041**	**27.0**
483.xalancbmk	4	589	46.9	**589**	**46.9**	590	46.8	4	589	46.9	**589**	**46.9**	590	46.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
        icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
  400.perlbench: -DSPEC_CPU_LINUX_X64
  462.libquantum: -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
    401.bzip2: /opt/intel/cce/9.1.047/bin/icc
             -I/opt/intel/cce/9.1.047/include
             -L/opt/intel/cce/9.1.047/lib

    456.hmmer: /opt/intel/cce/9.1.047/bin/icc
             -I/opt/intel/cce/9.1.047/include
             -L/opt/intel/cce/9.1.047/lib

  462.libquantum: /opt/intel/cce/9.1.047/bin/icc
             -I/opt/intel/cce/9.1.047/include
             -L/opt/intel/cce/9.1.047/lib
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
  400.perlbench: -DSPEC_CPU_LINUX_X64
     401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

       401.bzip2: -fast

         403.gcc: basepeak = yes

         429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

       445.gobmk: Same as 429.mcf

       456.hmmer: Same as 400.perlbench

       458.sjeng: Same as 429.mcf

   462.libquantum: Same as 400.perlbench

      464.h264ref: Same as 429.mcf
```

C++ benchmarks:

```
     471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
                  -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

        473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
                  -L/opt/SmartHeap_8_1/lib -lsmartheap

    483.xalancbmk: basepeak = yes
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

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