



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]_rate2006 = 75.1

Acer Altos R910 (Intel Xeon 7140M, 3.4GHz)

SPECint_rate_base2006 = 68.1

CPU2006 license: 97

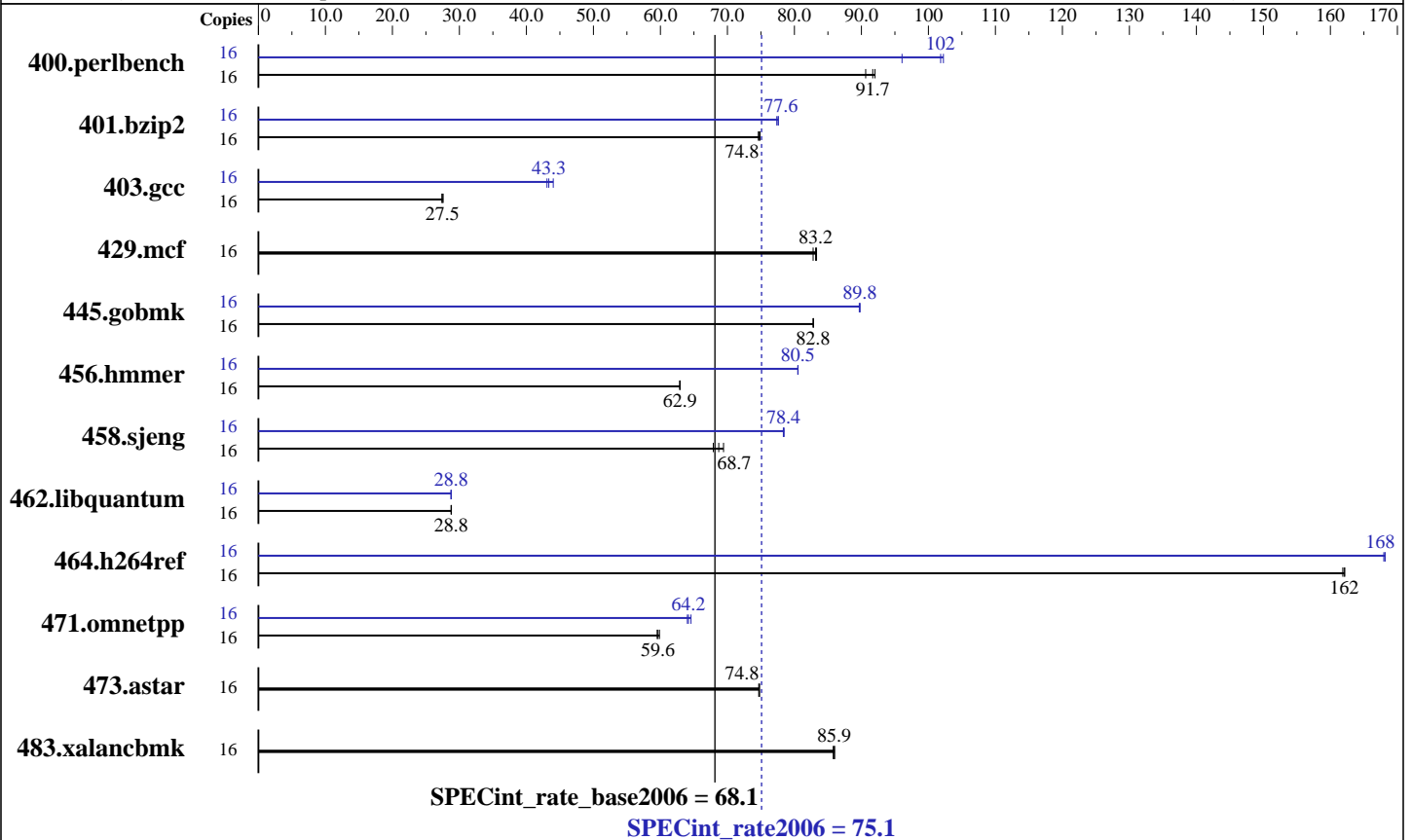
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2007

Hardware Availability: Aug-2006

Software Availability: Jul-2006



Hardware

CPU Name: Intel Xeon 7140M
 CPU Characteristics: 800MHz system bus
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,3,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (16 x 1024MB ECC DDR2-400 SDRAM CL3-3-3)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.028 Build 20060706Z
 Microsoft Visual Studio 2003 .Net (libr. & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill Smart Heap Library, Version 7.4



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1699	92.0	1725	90.6	1705	91.7	16	1535	102	1627	96.1	1529	102
401.bzip2	16	2062	74.9	2065	74.8	2069	74.6	16	1996	77.4	1991	77.6	1990	77.6
403.gcc	16	4704	27.4	4672	27.6	4691	27.5	16	2972	43.3	2991	43.1	2927	44.0
429.mcf	16	1755	83.2	1752	83.3	1762	82.8	16	1755	83.2	1752	83.3	1762	82.8
445.gobmk	16	2026	82.9	2027	82.8	2027	82.8	16	1870	89.8	1872	89.7	1869	89.8
456.hammer	16	2374	62.9	2373	62.9	2373	62.9	16	1853	80.5	1854	80.5	1854	80.5
458.sjeng	16	2852	67.9	2818	68.7	2789	69.4	16	2467	78.5	2471	78.3	2470	78.4
462.libquantum	16	11525	28.8	11522	28.8	11509	28.8	16	11535	28.7	11523	28.8	11518	28.8
464.h264ref	16	2185	162	2188	162	2184	162	16	2106	168	2108	168	2106	168
471.omnetpp	16	1671	59.9	1680	59.5	1677	59.6	16	1558	64.2	1562	64.0	1549	64.6
473.astar	16	1504	74.7	1502	74.8	1502	74.8	16	1504	74.7	1502	74.8	1502	74.8
483.xalancbmk	16	1285	85.9	1287	85.8	1284	86.0	16	1285	85.9	1287	85.8	1284	86.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Disable hardware prefetching and disable adjacent cache line prefetch BIOS setting

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:

`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:

`-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:

`icl -Qvc7.1 -Qc99`

C++ benchmarks:

`icl -Qvc7.1`

Peak Portability Flags

`403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

`400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE`

`401.bzip2: Same as 400.perlbench`

`403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE`

`429.mcf: basepeak = yes`

`445.gobmk: Same as 400.perlbench`

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Peak Optimization Flags (Continued)

456.hmmcr: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.xml>

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