



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers
CELSIUS M440, Intel dual-core Pentium D 960

SPECint2000 = 1814
SPECint_base2000 = 1810

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: Jun-2006 Hardware Avail: May-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	113	1238	113	1238	[Bar chart showing ratio 1238]			
175.vpr	1400	114	1227	113	1241	[Bar chart showing ratio 1241]			
176.gcc	1100	50.5	2179	50.5	2179	[Bar chart showing ratio 2179]			
181.mcf	1800	85.6	2104	85.6	2103	[Bar chart showing ratio 2103]			
186.crafty	1000	73.9	1353	73.3	1365	[Bar chart showing ratio 1365]			
197.parser	1800	119	1515	119	1518	[Bar chart showing ratio 1518]			
252.eon	1300	53.1	2448	53.1	2448	[Bar chart showing ratio 2448]			
253.perlbmk	1800	81.6	2207	81.5	2207	[Bar chart showing ratio 2207]			
254.gap	1100	54.2	2031	53.6	2051	[Bar chart showing ratio 2051]			
255.vortex	1900	60.0	3167	60.0	3167	[Bar chart showing ratio 3167]			
256.bzip2	1500	111	1347	111	1347	[Bar chart showing ratio 1347]			
300.twolf	3000	163	1846	163	1846	[Bar chart showing ratio 1846]			

Hardware

CPU: Intel Pentium D processor 960
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 12k micro-ops I + 16KBD per core (on chip)
Secondary Cache: 2 MB(I+D) per core (on chip)
L3 Cache: N/A
Other Cache: N/A
Memory: 4 GB (4 x 1GB PC2-4200 DDR2 ECC modules)
Disk Subsystem: Maxtor 6Y160M0 (Serial ATA 7200 RPM)
Other Hardware:

Software

Operating System: Windows XP Professional, Service Pack 2
Compiler: Intel(R) C++ Compiler for 32-bit app., Version 9.1,
- Build 20060519Z Package ID: W_CC_P_9.1.025
Microsoft Visual Studio .NET 2003 (for libraries)
MicroQuill SmartHeap Library, Version 8 (shlW32M.lib)
File System: NTFS
System State: Default

Notes/Tuning Information

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Feedback optimization:

+FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

Baseline Tuning Flags:

for C programs:
-fast +FDO shlW32M.lib
for C++ program 252.eon:
-fast -Qcxx_features +FDO

Peak Tuning Flags:

164.gzip: -fast +FDO
175.vpr: -fast +FDO
176.gcc: basepeak=yes
181.mcf: -fast +FDO shlW32M.lib



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers
CELSIUS M440, Intel dual-core Pentium D 960

SPECint2000 =	1814
SPECint_base2000 =	1810

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jun-2006 | Hardware Avail: May-2006 | Software Avail: May-2006

Notes/Tuning Information (Continued)

```
186.crafty: -fast +FDO
197.parser: -fast +FDO
252.eon: -fast -Qcxxx_features +FDO
253.perlbnk: -fast +FDO shlw32M.lib
254.gap: -fast +FDO
255.vortex -fast +FDO shlw32M.lib
256.bzip2: -fast +FDO
300.twolf: -fast +FDO shlw32M.lib
```

Extra Libraries:

```
shlw32M.lib = Microquill SmartHeap Library 8
see www.microquill.com
```

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>