



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

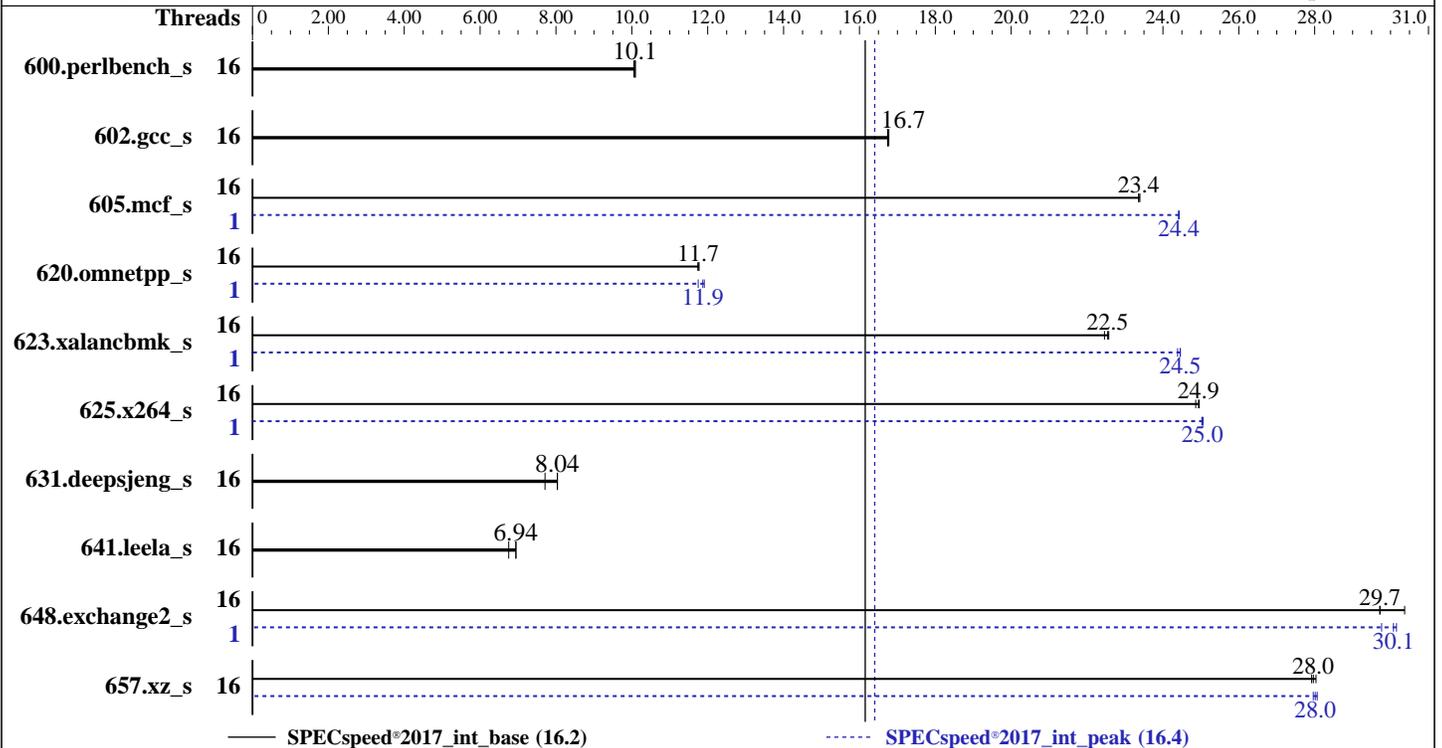
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023



Hardware

CPU Name: AMD EPYC 9174F
 Max MHz: 4400
 Nominal: 4100
 Enabled: 16 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 256 MB I+D on chip per chip,
 32 MB shared / 2 cores
 Other: None
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 480 GB SATA SSD
 Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
 Kernel 5.14.0-70.13.1.el9_0.x86_64
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: Yes
 Firmware: HPE BIOS Version v1.42 08/16/2023 released
 Aug-2023
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS and OS set to prefer performance at
 the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	176	10.1	177	10.1	176	10.1	16	176	10.1	177	10.1	176	10.1
602.gcc_s	16	238	16.7	238	16.7	237	16.8	16	238	16.7	238	16.7	237	16.8
605.mcf_s	16	202	23.4	202	23.4	202	23.4	1	193	24.4	193	24.4	193	24.4
620.omnetpp_s	16	139	11.7	139	11.7	139	11.8	1	137	11.9	137	11.9	139	11.7
623.xalancbmk_s	16	63.1	22.5	62.9	22.5	62.8	22.6	1	58.1	24.4	57.9	24.5	57.9	24.5
625.x264_s	16	70.7	24.9	70.7	25.0	70.9	24.9	1	70.5	25.0	70.4	25.1	70.5	25.0
631.deepsjeng_s	16	178	8.04	186	7.71	178	8.04	16	178	8.04	186	7.71	178	8.04
641.leela_s	16	246	6.94	253	6.75	246	6.95	16	246	6.94	253	6.75	246	6.95
648.exchange2_s	16	96.8	30.4	98.9	29.7	98.9	29.7	1	98.8	29.8	97.8	30.1	97.5	30.2
657.xz_s	16	221	28.0	221	28.0	221	27.9	16	221	28.0	221	28.0	220	28.1

SPECspeed®2017_int_base = **16.2**

SPECspeed®2017_int_peak = **16.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-15"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc400_znver4_A_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "16"
```

Environment variables set by runcpu during the 605.mcf_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 620.omnetpp_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 625.x264_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 648.exchange2_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 657.xz_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "8"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration

Workload Profile set to General Peak Frequency Compute

AMD SMT Option set to Disabled

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Last-Level Cache (LLC) as NUMA Node set to Enabled

NUMA memory domains per socket set to Four memory domains per socket

Memory PStates set to Disabled

ACPI CST C2 Latency set to 18 microseconds

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

The system ROM used for this result contains microcode version 0xaa00212 for the AMD EPYC 9nn4X family of processors. The reference code/AGESA version used in this ROM is version GenoaPI 1.0.0.8

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Tue Oct 17 15:12:05 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 250 (250-6.e19_0)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent_hugepage
- 18. /sys/kernel/mm/transparent_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
x86_64 x86_64 GNU/Linux
```

```
2. w
15:12:05 up 1 min, 1 user, load average: 0.00, 0.00, 0.00
USER      TTY      LOGIN@  IDLE   JCPU   PCPU WHAT
root     pts/0    15:11   12.00s  0.74s  0.02s /bin/bash ./amd_speed_aocc400_znver4_A1.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3094814
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

```

open files                (-n) 1024
pipe size                  (512 bytes, -p) 8
POSIX message queues      (bytes, -q) 819200
real-time priority        (-r) 0
stack size                 (kbytes, -s) unlimited
cpu time                   (seconds, -t) unlimited
max user processes        (-u) 3094814
virtual memory             (kbytes, -v) unlimited
file locks                 (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
python3 ./run_intspeed.py
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.006/templogs/preenv.intspeed.006.0.log --lognum 006.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

6. /proc/cpuinfo

```

model name      : AMD EPYC 9174F 16-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa10113e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 3584 4K pages
cpu cores      : 16
siblings       : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-15
physical id 0: apicids 0-15

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:   0-15
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       Advanced Micro Devices, Inc.
Model name:            AMD EPYC 9174F 16-Core Processor
BIOS Model name:      AMD EPYC 9174F 16-Core Processor
CPU family:            25

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

```

Model: 17
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 1
Stepping: 1
BogoMIPS: 8187.11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpelgb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes
xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core
perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single
hw_pstate ssbd mba ibrs ibpb stibp vmcall fsgsbase bmi1 avx2 smep bmi2
erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1
xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local avx512_bf16
clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d
Virtualization: AMD-V
L1d cache: 512 KiB (16 instances)
L1i cache: 512 KiB (16 instances)
L2 cache: 16 MiB (16 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0,1
NUMA node1 CPU(s): 2,3
NUMA node2 CPU(s): 4,5
NUMA node3 CPU(s): 6,7
NUMA node4 CPU(s): 8,9
NUMA node5 CPU(s): 10,11
NUMA node6 CPU(s): 12,13
NUMA node7 CPU(s): 14,15
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	512K	8	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	1M	16M	8	Unified	2	2048	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-1
node 0 size: 96521 MB

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

```

node 0 free: 96311 MB
node 1 cpus: 2-3
node 1 size: 96767 MB
node 1 free: 96517 MB
node 2 cpus: 4-5
node 2 size: 96767 MB
node 2 free: 96608 MB
node 3 cpus: 6-7
node 3 size: 96767 MB
node 3 free: 96640 MB
node 4 cpus: 8-9
node 4 size: 96730 MB
node 4 free: 96616 MB
node 5 cpus: 10-11
node 5 size: 96767 MB
node 5 free: 96644 MB
node 6 cpus: 12-13
node 6 size: 96767 MB
node 6 free: 96460 MB
node 7 cpus: 14-15
node 7 size: 96719 MB
node 7 free: 96465 MB

```

```

node distances:
node  0  1  2  3  4  5  6  7
0:  10 11 12 12 12 12 12 12
1:  11 10 12 12 12 12 12 12
2:  12 12 10 11 12 12 12 12
3:  12 12 11 10 12 12 12 12
4:  12 12 12 12 10 11 12 12
5:  12 12 12 12 11 10 12 12
6:  12 12 12 12 12 12 10 11
7:  12 12 12 12 12 12 11 10

```

```

-----
9. /proc/meminfo
   MemTotal:      792379196 kB

```

```

-----
10. who -r
    run-level 3 Oct 17 15:10

```

```

-----
11. Systemd service manager version: systemd 250 (250-6.el9_0)
    Default Target   Status
    multi-user       running

```

```

-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond
                  dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
                  nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
                  systemd-network-generator tuned udisks2 upower
    enabled-runtime systemd-remount-fs
    disabled       blk-availability canberra-system-bootup canberra-system-shutdown
                  canberra-system-shutdown-reboot chrony-wait chronyd console-getty cpupower debug-shell
                  hwloc-dump-hwdata ipsec kvm_stat man-db-restart-cache-update nftables powertop rdisc rhsm
                  rhsm-facts rpmdb-rebuild serial-getty@ sshd-keygen@ systemd-boot-check-no-failures
                  systemd-pstore systemd-sysex
    indirect       sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd2,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

14. cpupower frequency-info
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 4100MHz

15. tuned-adm active
Current active profile: throughput-performance

16. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1

17. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Oct-2023
Hardware Availability: Aug-2023
Software Availability: Apr-2023

Platform Notes (Continued)

```
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

19. OS release

```
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)
```

20. Disk information

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   372G  15G  357G   4% /home
```

21. /sys/devices/virtual/dmi/id

```
Vendor:      HPE
Product:     ProLiant DL325 Gen11
Product Family: ProLiant
Serial:      DL325GEN11-002
```

22. dmidecode

Additional information from dmidecode 3.3 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
Memory:
11x Hynix HMC94AEBRA103N 64 GB 2 rank 4800
1x Hynix HMC94MEBRA121N 64 GB 2 rank 4800
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      HPE
BIOS Version:     1.42
BIOS Date:        08/16/2023
BIOS Revision:    1.42
Firmware Revision: 1.40
```

Compiler Version Notes

```
=====  
C          | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)  
          | 657.xz_s(base, peak)  
=====
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin  
=====
```

```
C++        | 620.omnetpp_s(base, peak) 623.xalanbmk_s(base, peak) 631.deepsjeng_s(base, peak)  
          | 641.leela_s(base, peak)  
=====
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2023

Hardware Availability: Aug-2023

Software Availability: Apr-2023

Compiler Version Notes (Continued)

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

```

```

=====
Fortran | 648.exchange2_s(base, peak)

```

```

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

```

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

```

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2023

Hardware Availability: Aug-2023

Software Availability: Apr-2023

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2023

Hardware Availability: Aug-2023

Software Availability: Apr-2023

Peak Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

625.x264_s: Same as 605.mcf_s

657.xz_s: Same as 605.mcf_s

C++ benchmarks:

620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.2

SPECspeed®2017_int_peak = 16.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2023

Hardware Availability: Aug-2023

Software Availability: Apr-2023

Peak Optimization Flags (Continued)

620.omnetpp_s (continued):

-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang

623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast

-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp

-flto -finline-aggressive -mllvm -unroll-threshold=100

-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt

-mllvm -do-block-reorder=aggressive

-fvirtual-function-elimination -fvisibility=hidden

-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3

-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop

-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM

-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost

-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp

-lomp -lamdlibm -lamdalloc -lflang

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-X-rev1.5.html>

<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.html>



SPEC CPU[®]2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen11

(4.10 GHz, AMD EPYC 9174F)

SPECspeed[®]2017_int_base = 16.2

SPECspeed[®]2017_int_peak = 16.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Oct-2023

Hardware Availability: Aug-2023

Software Availability: Apr-2023

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-X-rev1.5.xml>

<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU[®]2017 v1.1.9 on 2023-10-17 05:42:04-0400.

Report generated on 2023-11-07 18:41:24 by CPU2017 PDF formatter v6716.

Originally published on 2023-11-07.