



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_fp_base = 707

PowerEdge R6625 (AMD EPYC 9254 24-Core Processor)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6573

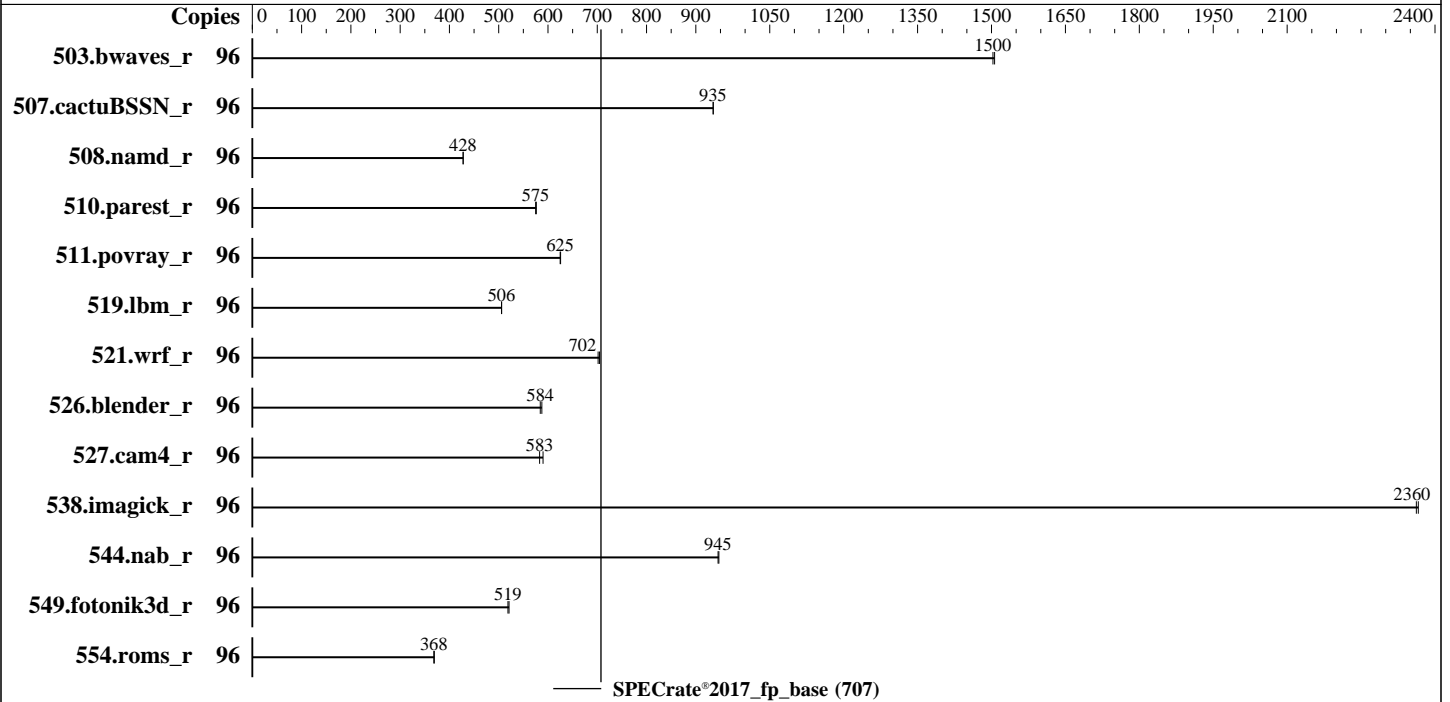
Test Date: Mar-2023

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2023

Tested by: Dell Inc.

Software Availability: Nov-2022



Hardware

CPU Name: AMD EPYC 9254
 Max MHz: 4150
 Nominal: 2900
 Enabled: 48 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 128 MB I+D on chip per chip, 32 MB shared / 6 cores
 Other: None
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 125 GB on tmpfs
 Other: None

Software

OS: Ubuntu 22.04.1 LTS
 5.15.0-46-generic
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: No
 Firmware: Version 1.3.7 released Mar-2023
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	639	1510	<u>641</u>	<u>1500</u>									
507.cactuBSSN_r	96	130	936	<u>130</u>	<u>935</u>									
508.namd_r	96	213	428	<u>213</u>	<u>428</u>									
510.parest_r	96	<u>437</u>	<u>575</u>	436	576									
511.povray_r	96	358	626	<u>359</u>	<u>625</u>									
519.lbm_r	96	<u>200</u>	<u>506</u>	200	506									
521.wrf_r	96	<u>306</u>	<u>702</u>	305	705									
526.blender_r	96	249	588	<u>250</u>	<u>584</u>									
527.cam4_r	96	285	590	<u>288</u>	<u>583</u>									
538.imagick_r	96	<u>101</u>	<u>2360</u>	101	2370									
544.nab_r	96	171	947	<u>171</u>	<u>945</u>									
549.fotonik3d_r	96	718	521	<u>721</u>	<u>519</u>									
554.roms_r	96	413	369	<u>414</u>	<u>368</u>									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-aocc400-B1e/amd_rate_aocc400_genoa_B_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-ao  
cc400-B1e/amd_rate_aocc400_genoa_B_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```
    DRAM Refresh Delay : Performance  
    DIMM Self Healing on  
Uncorrectable Memory Error : Disabled  
Virtualization Technology : Disabled  
    L1 Stride Prefetcher: : Disabled  
    NUMA Nodes per Socket : 4  
    L3 Cache as NUMA Domain : Enabled
```

```
    System Profile : Custom  
    Memory Patrol Scrub : Disabled  
    PCI ASPM L1 Link  
    Power Management : Disabled  
    Determinism Slider : Power Determinism
```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc400-B1e/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on genoa-sut Sat Mar 11 13:53:48 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w

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Platform Notes (Continued)

3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

```
-----
1. uname -a
Linux genoa-sut 5.15.0-46-generic #49-Ubuntu SMP Thu Aug 4 18:03:25 UTC 2022 x86_64 x86_64 x86_64 GNU/Linux
-----
```

```
-----
2. w
 13:53:48 up 20:01,  1 user,  load average:  68.49,  88.81,  92.16
USER   TTY      FROM             LOGIN@   IDLE   JCPU   PCPU   WHAT
root   ttyL    -                26Nov61  1:31m  1.77s  0.39s /bin/bash ./amd_rate_aocc400_genoa_B1.sh
-----
```

```
-----
3. Username
From environment variable $USER:  root
-----
```

```
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            6190469
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0
-----
```

```
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
-----
```

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Platform Notes (Continued)

```

/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-specrate.sh --output_format csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_EPYC-4.inc
python3 ./run_amd_rate_aocc400_genoa_B1.py
/bin/bash ./amd_rate_aocc400_genoa_B1.sh
runcpu --config amd_rate_aocc400_genoa_B1.cfg --tune base --reportable --iterations 2 --output_format
csv,html,pdf,txt -define Dell-BIOS-inc=Dell-BIOS_EPYC-4.inc fprate
runcpu --configfile amd_rate_aocc400_genoa_B1.cfg --tune base --reportable --iterations 2 --output_format
csv,html,pdf,txt --define Dell-BIOS-inc=Dell-BIOS_EPYC-4.inc --nopower --runmode rate --tune base --size
test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/temlogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc400-B1e

```

6. /proc/cpuinfo

```

model name      : AMD EPYC 9254 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa101116
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 24
siblings      : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 1: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
physical id 1: apicids 64-75,80-91,96-107,112-123

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:     52 bits physical, 57 bits virtual
Byte Order:       Little Endian
CPU(s):           96
On-line CPU(s) list: 0-95
Vendor ID:        AuthenticAMD
Model name:       AMD EPYC 9254 24-Core Processor
CPU family:       25
Model:           17
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s):        2
Stepping:         1
Frequency boost:  enabled
CPU max MHz:      4153.0000
CPU min MHz:      400.0000
BogoMIPS:         5801.30
Flags:            fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                  clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                  constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
                  pni pclmulqdq monitor ssse3 fma cxl6 pcid sse4_1 sse4_2 x2apic movbe

```

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Platform Notes (Continued)

popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld

AMD-V
1.5 MiB (48 instances)
1.5 MiB (48 instances)
48 MiB (48 instances)
256 MiB (8 instances)
8
8
0-5,48-53
12-17,60-65
18-23,66-71
6-11,54-59
24-29,72-77
36-41,84-89
42-47,90-95
30-35,78-83
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 32K 1.5M 8 Data 1 64 1 64
L1i 32K 1.5M 8 Instruction 1 64 1 64
L2 1M 48M 8 Unified 2 2048 1 64
L3 32M 256M 16 Unified 3 32768 1 64

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 8 nodes (0-7)
node 0 cpus: 0-5,48-53
node 0 size: 193078 MB
node 0 free: 192338 MB
node 1 cpus: 12-17,60-65
node 1 size: 193532 MB
node 1 free: 192836 MB
node 2 cpus: 18-23,66-71
node 2 size: 193532 MB
node 2 free: 192822 MB
node 3 cpus: 6-11,54-59

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Platform Notes (Continued)

```

node 3 size: 193480 MB
node 3 free: 192758 MB
node 4 cpus: 24-29,72-77
node 4 size: 193532 MB
node 4 free: 192806 MB
node 5 cpus: 36-41,84-89
node 5 size: 193532 MB
node 5 free: 192814 MB
node 6 cpus: 42-47,90-95
node 6 size: 193532 MB
node 6 free: 189237 MB
node 7 cpus: 30-35,78-83
node 7 size: 193506 MB
node 7 free: 192743 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

9. /proc/meminfo
   MemTotal:      1584873984 kB

```

```

10. who -r
    run-level 3 Nov 25 23:13

```

```

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.4)
    Default Target    Status
    multi-user        degraded

```

```

12. Failed units, from systemctl list-units --state=failed
    UNIT                                LOAD    ACTIVE SUB    DESCRIPTION
* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd

```

```

13. Services, from systemctl list-unit-files
    STATE      UNIT FILES
enabled      ModemManager blk-availability cloud-config cloud-final cloud-init cloud-init-local
              console-setup cron dmesg e2scrub_reap finalrd getty@ grub-common grub-initrd-fallback
              irqbalance keyboard-setup lm-sensors lvm2-monitor lxd-agent networkd-dispatcher open-iscsi
              open-vm-tools pollinate rsync rsyslog secureboot-db setvtrgb ssh systemd-networkd
              systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald
              tuned ua-reboot-cmds ubuntu-advantage udisks2 vgaauth
enabled-runtime netplan-ovs-cleanup rc-local systemd-remount-fs
disabled      apparmor console-getty debug-shell iscsid multipathd nftables powertop serial-getty@
              smartmontools sysstat systemd-boot-check-no-failures systemd-network-generator
              systemd-sysexit systemd-time-wait-sync ufw upower
generated     apport
indirect      uuid
masked        accounts-daemon alsa-utils atd cryptdisks cryptdisks-early gpu-manager hwclock lvm2
              multipath-tools-boot rc rcS screen-cleanup sudo x11-common

```

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Platform Notes (Continued)

```

-----
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-5.15.0-46-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro

```

```

-----
15. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 400 MHz and 4.15 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2900MHz

```

```

-----
16. tuned-adm active
  Current active profile: latency-performance

```

```

-----
17. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      3
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

```

```

-----
18. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

```

```

-----
19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64

```

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Platform Notes (Continued)

pages_to_scan 4096
scan_sleep_millisecs 10000

20. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.1 LTS

21. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc400-B1e
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 125G 3.5G 122G 3% /mnt/ramdisk

22. /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R6625
Product Family: PowerEdge
Serial: BGP4016

23. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
24x 802C0000802C MTC40F2046S1RC48BA1 64 GB 2 rank 4800

24. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Dell Inc.
BIOS Version: 1.3.7
BIOS Date: 03/06/2023
BIOS Revision: 1.3

Compiler Version Notes

=====
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

=====
C++ | 508.namd_r(base) 510.parest_r(base)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

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Compiler Version Notes (Continued)

=====
C++, C | 511.povray_r(base) 526.blender_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

=====
Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
=====

Base Compiler Invocation

C benchmarks:
clang

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SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

SPECrate®2017_fp_base = 707

PowerEdge R6625 (AMD EPYC 9254 24-Core Processor)

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2023

Hardware Availability: Mar-2023

Software Availability: Nov-2022

Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -lamdlibm -lamdalloc -lflang

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -Kieee -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang
```

Benchmarks using both C and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-zopt -mllvm -unroll-threshold=100 -finline-aggressive
```

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-mllvm -loop-unswitch-threshold=200000 -Kieee -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.0.xml>

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