



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR650 V3
(2.70 GHz, Intel Xeon Silver 4410T)

SPECspeed®2017_fp_base = 172

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

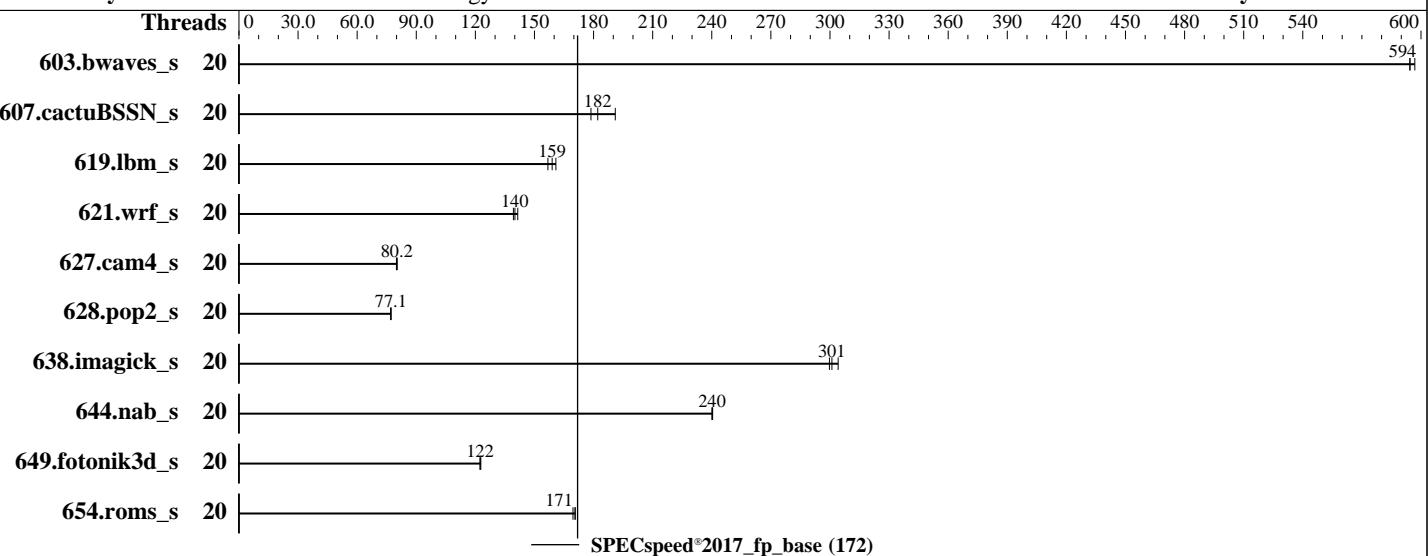
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Mar-2023

Hardware Availability: Feb-2023

Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Silver 4410T
Max MHz: 4000
Nominal: 2700
Enabled: 20 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 26.25 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R, running at 4000)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
Compiler: Kernel 5.14.21-150400.22-default
C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2023.0 of Intel Fortran Compiler
Classic for Linux;
C/C++: Version 2023.0 of Intel C/C++ Compiler
Classic for Linux
Parallel: Yes
Firmware: Lenovo BIOS Version ESE109L 1.10 released Jan-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds
603.bwaves_s	20	98.8	597	99.3	594		99.3	594								
607.cactuBSSN_s	20	93.3	179	91.5	182		87.3	191								
619.lbm_s	20	33.4	157	32.6	161	32.9	159									
621.wrf_s	20	94.4	140	93.5	142		94.9	139								
627.cam4_s	20	110	80.3	111	79.9	111	80.2									
628.pop2_s	20	154	77.1	154	77.0	154	77.1									
638.imagick_s	20	47.4	304	48.1	300	47.9	301									
644.nab_s	20	72.7	240	72.7	240		72.8	240								
649.fotonik3d_s	20	74.5	122	74.3	123		74.5	122								
654.roms_s	20	92.3	171	92.2	171		92.9	170								

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-ic2023.0/lib/intel64:/home/cpu2017-1.1.9-ic2023.0/j
    e5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

Operating Mode set to Custom Mode
CPU P-State Control set to Legacy
Hyper-Threading set to Disabled
DCU IP Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.9-ic2023.0/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Mar 21 19:31:16 2023

SUT (System Under Test) info as seen by some common utilities.

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Platform Notes (Continued)

1. uname -a

```
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
19:31:16 up 2 min, 1 user, load average: 0.03, 0.04, 0.01
USER      TTY      FROM             LOGIN@     IDLE    JCPU    PCPU WHAT
root      tty1          -           19:30    12.00s   1.31s   0.01s -bash
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals          (-i) 2062719
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes       (-u) 2062719
virtual memory            (-v) unlimited
file locks                (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=20 --tune base -o all --define drop_caches
fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=20 --tune base --output_format all --define
drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv --note-preenv
```

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Platform Notes (Continued)

```
--logfile $SPEC/tmp/CPU2017.132/templogs/preenv.fpspeed.132.0.log --lognum 132.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.0
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Silver 4410T
vendor_id       : GenuineIntel
cpu family     : 6
model          : 143
stepping        : 8
microcode       : 0x2b000161
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores       : 10
siblings        : 10
2 physical ids (chips)
20 processors (hardware threads)
physical id 0: core ids 0-9
physical id 1: core ids 0-9
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18
physical id 1: apicids 128,130,132,134,136,138,140,142,144,146
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                20
On-line CPU(s) list:  0-19
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) Silver 4410T
CPU family:             6
Model:                 143
Thread(s) per core:   1
Core(s) per socket:   10
Socket(s):             2
Stepping:              8
Frequency boost:      enabled
CPU max MHz:          2701.0000
CPU min MHz:          800.0000
BogoMIPS:              5400.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
```

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Platform Notes (Continued)

```

clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
   lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
avx2 smp bmi2 erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
    xsaveopt xsavexc xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities
    VT-x

```

Virtualization:

L1d cache:	960 KiB (20 instances)
L1i cache:	640 KiB (20 instances)
L2 cache:	40 MiB (20 instances)
L3 cache:	52.5 MiB (2 instances)

NUMA node(s):

NUMA node0 CPU(s):

NUMA node1 CPU(s):

Vulnerability Itlb multihit:

Vulnerability L1tf:

Vulnerability Mds:

Vulnerability Meltdown:

Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1: Mitigation: usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation: Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Srbds:

Vulnerability Tsx async abort:

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	960K	12	Data	1	64	1	64
L1i	32K	640K	8	Instruction	1	64	1	64
L2	2M	40M	16	Unified	2	2048	1	64
L3	26.3M	52.5M	15	Unified	3	28672	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-9

node 0 size: 257673 MB

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Platform Notes (Continued)

```
node 0 free: 256803 MB
node 1 cpus: 10-19
node 1 size: 258029 MB
node 1 free: 257547 MB
node distances:
node    0    1
  0: 10 21
  1: 21 10
```

```
-----9. /proc/meminfo
      MemTotal:      528080724 kB
```

```
-----10. who -r
      run-level 3 Mar 21 19:29
```

```
-----11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
      Default Target  Status
      multi-user     running
```

```
-----12. Services, from systemctl list-unit-files
      STATE          UNIT FILES
      enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
                      issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog
                      smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
      enabled-runtime systemd-remount-fs
      disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                      chronynd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                      firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd issue-add-ssh-keys kexec-load
                      lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd
                      sapconf serial-getty@ smartd_generate_opts snmpd snmptrapd sysstat
                      systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                      systemd-time-wait-sync systemd-timesyncd tuned
      indirect       uidd wickedd
```

```
-----13. Linux kernel boot-time arguments, from /proc/cmdline
      BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
      root=UUID=461ffbd6-8da0-4c20-adb7-d9d3143b6aa5
      splash=silent
      mitigations=auto
      quiet
      security=apparmor
```

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Platform Notes (Continued)

14. cpupower frequency-info

analyzing CPU 0:

current policy: frequency should be within 800 MHz and 2.70 GHz.
The governor "ondemand" may decide which speed to use
within this range.

boost state support:

Supported: yes

Active: yes

15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.

Preset profile: virtual-guest

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

17. /sys/kernel/mm/transparent_hugepage

defrag always defer defer+madvise [madvise] never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs 60000

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Platform Notes (Continued)

defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

20. Disk information

SPEC is set to: /home/cpu2017-1.1.9-ic2023.0

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	889G	112G	777G	13%	/

21. /sys/devices/virtual/dmi/id

Vendor:	Lenovo
Product:	ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U
Product Family:	ThinkSystem
Serial:	1234567890

22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
16x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800, configured at 4000
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	Lenovo
BIOS Version:	ESE109L-1.10
BIOS Date:	01/07/2023
BIOS Revision:	1.10
Firmware Revision:	1.0



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Compiler Version Notes

=====
C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 607.cactuBSSN_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

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Lenovo Global Technology

ThinkSystem SR650 V3
(2.70 GHz, Intel Xeon Silver 4410T)

SPECspeed®2017_fp_base = 172

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

Test Date: Mar-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Dec-2022

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-O.html>
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-O.xml>
<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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