



# SPEC CPU®2017 Integer Speed Result

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## Lenovo Global Technology

ThinkSystem SR645 V3  
(2.75 GHz,AMD EPYC 9454)

SPECspeed®2017\_int\_base = 14.2

SPECspeed®2017\_int\_peak = 14.4

CPU2017 License: 9017

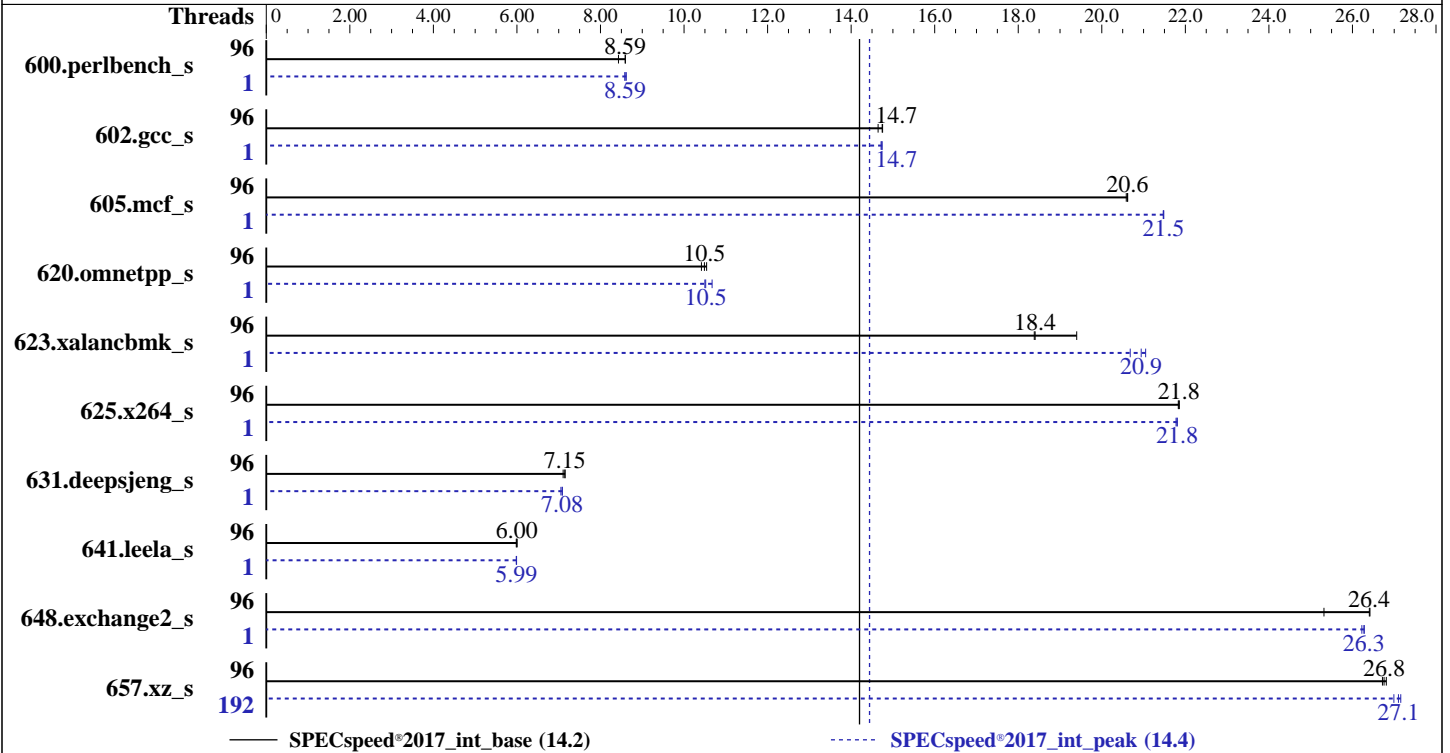
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2023

Hardware Availability: Feb-2023

Software Availability: Nov-2022



### Hardware

CPU Name: AMD EPYC 9454  
 Max MHz: 3800  
 Nominal: 2750  
 Enabled: 96 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 32 MB shared / 6 cores  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC5-4800B-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Ubuntu 22.04.1 LTS  
 Kernel 5.15.0-43-generic  
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version KAE105F 1.20 released Dec-2022  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	96	206	8.60	211	8.43	<u>207</u>	<u>8.59</u>	1	<u>207</u>	<u>8.59</u>	207	8.58	206	8.62
602.gcc_s	96	270	14.8	<u>270</u>	<u>14.7</u>	272	14.6	1	<u>270</u>	<u>14.7</u>	270	14.7	271	14.7
605.mcf_s	96	229	20.6	229	20.6	<u>229</u>	<u>20.6</u>	1	220	21.5	<u>220</u>	<u>21.5</u>	220	21.5
620.omnetpp_s	96	157	10.4	<u>156</u>	<u>10.5</u>	155	10.5	1	153	10.7	155	10.5	<u>155</u>	<u>10.5</u>
623.xalancbmk_s	96	73.0	19.4	<u>77.0</u>	<u>18.4</u>	77.1	18.4	1	<u>67.7</u>	<u>20.9</u>	68.5	20.7	67.3	21.0
625.x264_s	96	80.8	21.8	<u>80.7</u>	<u>21.8</u>	80.7	21.9	1	80.9	21.8	<u>80.9</u>	<u>21.8</u>	81.0	21.8
631.deepsjeng_s	96	<u>201</u>	<u>7.15</u>	202	7.11	200	7.15	1	203	7.05	<u>202</u>	<u>7.08</u>	202	7.09
641.leela_s	96	<u>284</u>	<u>6.00</u>	285	5.98	284	6.00	1	285	5.99	285	5.98	<u>285</u>	<u>5.99</u>
648.exchange2_s	96	<u>111</u>	<u>26.4</u>	116	25.3	111	26.4	1	<u>112</u>	<u>26.3</u>	112	26.3	112	26.2
657.xz_s	96	<u>231</u>	<u>26.8</u>	231	26.8	231	26.7	192	<u>228</u>	<u>27.1</u>	229	27.0	228	27.2

SPECspeed®2017\_int\_base = **14.2**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

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### Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-191"

LD\_LIBRARY\_PATH =

"/home/cpu2017-1.1.8-amd-aocc400-genoa-B1b/amd\_speed\_aocc400\_genoa\_B\_lib  
/lib:"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"

MALLOC\_CONF = "oversize\_threshold:0,retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "192"

Environment variables set by runcpu during the 600.perlbench\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 602.gcc\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 605.mcf\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 620.omnetpp\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalanbmk\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 625.x264\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 631.deepsjeng\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 641.leela\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

Environment variables set by runcpu during the 648.exchange2\_s peak run:

GOMP\_CPU\_AFFINITY = "15"

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## Environment Variables Notes (Continued)

Environment variables set by runcpu during the 657.xz\_s peak run:  
GOMP\_CPU\_AFFINITY = "0-191"  
LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "8"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
Operating Mode set to Maximum Performance and then set it to Custom Mode  
NUMA Nodes per Socket set to NPS4

Sysinfo program /home/cpu2017-1.1.8-amd-aocc400-genoa-Blb/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on perf Tue Jun 28 15:09:46 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : AMD EPYC 9454 48-Core Processor  
2 "physical id"s (chips)  
192 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 48  
siblings : 96  
physical 0: cores 0 1 2 3 4 5 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29 32 33  
34 35 36 37 40 41 42 43 44 45 48 49 50 51 52 53 56 57 58 59 60 61  
physical 1: cores 0 1 2 3 4 5 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29 32 33  
34 35 36 37 40 41 42 43 44 45 48 49 50 51 52 53 56 57 58 59 60 61

From lscpu from util-linux 2.37.2:  
Architecture: x86\_64

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### Platform Notes (Continued)

```

CPU op-mode(s):          32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 192
On-line CPU(s) list:   0-191
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 9454 48-Core Processor
CPU family:            25
Model:                  17
Thread(s) per core:    2
Core(s) per socket:    48
Socket(s):              2
Stepping:               1
Frequency boost:       enabled
CPU max MHz:            3812.0000
CPU min MHz:            400.0000
BogoMIPS:                5491.81
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat
npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq
la57 rdpid overflow_recov succor smca fsrm flush_lld
Virtualization:        AMD-V
L1d cache:             3 MiB (96 instances)
L1i cache:             3 MiB (96 instances)
L2 cache:              96 MiB (96 instances)
L3 cache:              512 MiB (16 instances)
NUMA node(s):          8
NUMA node0 CPU(s):    0-11,96-107
NUMA node1 CPU(s):    12-23,108-119
NUMA node2 CPU(s):    24-35,120-131
NUMA node3 CPU(s):    36-47,132-143
NUMA node4 CPU(s):    48-59,144-155
NUMA node5 CPU(s):    60-71,156-167
NUMA node6 CPU(s):    72-83,168-179
NUMA node7 CPU(s):    84-95,180-191
Vulnerability Itlb multihit: Not affected

```

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### Platform Notes (Continued)

Vulnerability L1tf: Not affected  
 Vulnerability Mds: Not affected  
 Vulnerability Meltdown: Not affected  
 Vulnerability Mmio stale data: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
 Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS\_FW, STIBP always-on, RSB filling  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	3M	8	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	8	Unified	2	2048	1	64
L3	32M	512M	16	Unified	3	32768	1	64

/proc/cpuinfo cache data  
cache size : 1024 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)  
 node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 96 97 98 99 100 101 102 103 104 105 106 107  
 node 0 size: 96379 MB  
 node 0 free: 95075 MB  
 node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 108 109 110 111 112 113 114 115 116  
 117 118 119  
 node 1 size: 96719 MB  
 node 1 free: 95638 MB  
 node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 120 121 122 123 124 125 126 127 128  
 129 130 131  
 node 2 size: 96752 MB  
 node 2 free: 95473 MB  
 node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 132 133 134 135 136 137 138 139 140  
 141 142 143  
 node 3 size: 96752 MB  
 node 3 free: 95591 MB  
 node 4 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 144 145 146 147 148 149 150 151 152  
 153 154 155  
 node 4 size: 96752 MB  
 node 4 free: 95763 MB  
 node 5 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 156 157 158 159 160 161 162 163 164  
 165 166 167

(Continued on next page)



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### Platform Notes (Continued)

```

node 5 size: 96752 MB
node 5 free: 95755 MB
node 6 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 168 169 170 171 172 173 174 175 176
177 178 179
node 6 size: 96752 MB
node 6 free: 95749 MB
node 7 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 180 181 182 183 184 185 186 187 188
189 190 191
node 7 size: 96689 MB
node 7 free: 95681 MB
node distances:
node   0   1   2   3   4   5   6   7
  0:  10  12  12  12  32  32  32  32
  1:  12  10  12  12  32  32  32  32
  2:  12  12  10  12  32  32  32  32
  3:  12  12  12  10  32  32  32  32
  4:  32  32  32  32  10  12  12  12
  5:  32  32  32  32  12  10  12  12
  6:  32  32  32  32  12  12  10  12
  7:  32  32  32  32  12  12  12  10

```

```

From /proc/meminfo
MemTotal:      792116876 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

```

```

/usr/bin/lsb_release -d
Ubuntu 22.04.1 LTS

```

```

From /etc/*release* /etc/*version*
debian_version: bookworm/sid
os-release:
  PRETTY_NAME="Ubuntu 22.04.1 LTS"
  NAME="Ubuntu"
  VERSION_ID="22.04"
  VERSION="22.04.1 LTS (Jammy Jellyfish)"
  VERSION_CODENAME=jammy
  ID=ubuntu
  ID_LIKE=debian
  HOME_URL="https://www.ubuntu.com/"

```

```

uname -a:
Linux perf 5.15.0-43-generic #46-Ubuntu SMP Tue Jul 12 10:30:17 UTC 2022 x86_64 x86_64
x86_64 GNU/Linux

```

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### Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
mmio_stale_data:	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Retpolines, IBPB: conditional, IBRS_FW, STIBP: always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):	Not affected
CVE-2019-11135 (TSX Asynchronous Abort):	Not affected

run-level 5 Jun 27 18:29

SPEC is set to: /home/cpu2017-1.1.8-amd-aocc400-genoa-Blb

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	ext4	878G	20G	814G	3%	/

From /sys/devices/virtual/dmi/id

```
Vendor:      Lenovo
Product:    ThinkSystem SR645 V3 MB,Genoa,DDR5,Oahu,1U
Product Family: ThinkSystem
Serial:     1234567890
```

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
7x SK Hynix HMC88AEBRA115N 32 GB 2 rank 4800
17x SK Hynix HMC88AEBRA168N 32 GB 2 rank 4800
```

BIOS:

```
BIOS Vendor:      Lenovo
BIOS Version:    KAE105F-1.20
BIOS Date:       12/01/2022
BIOS Revision:   1.20
Firmware Revision: 1.20
```

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### Platform Notes (Continued)

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,  
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)  
-----
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on  
LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----
```

```
=====  
C++    | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)  
      | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)  
-----
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on  
LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----
```

```
=====  
Fortran | 648.exchange2_s(base, peak)  
-----
```

```
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on  
LLVM Mirror.Version.14.0.6)  
Target: x86_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin  
-----
```

### Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

(Continued on next page)



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## Base Compiler Invocation (Continued)

Fortran benchmarks:

flang

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc
```

C++ benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
```

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# SPEC CPU®2017 Integer Speed Result

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**Lenovo Global Technology**

ThinkSystem SR645 V3  
(2.75 GHz,AMD EPYC 9454)

SPECspeed®2017\_int\_base = 14.2

SPECspeed®2017\_int\_peak = 14.4

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2023

**Hardware Availability:** Feb-2023

**Software Availability:** Nov-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

## Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

(Continued on next page)



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## Peak Optimization Flags (Continued)

```
600.perlbench_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

```
602.gcc_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -z muldefs -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

605.mcf\_s: Same as 600.perlbench\_s

625.x264\_s: Same as 600.perlbench\_s

657.xz\_s: Same as 600.perlbench\_s

C++ benchmarks:

```
620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

```
623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang
```

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## Peak Optimization Flags (Continued)

```
631.deepsjeng_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

641.leela\_s: Same as 631.deepsjeng\_s

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

## Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-0.html>  
<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-0.xml>  
<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>



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