SPEC CPU®2017 Floating Point Speed Result

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Dell Inc.
(Test Sponsor: Dell Inc)
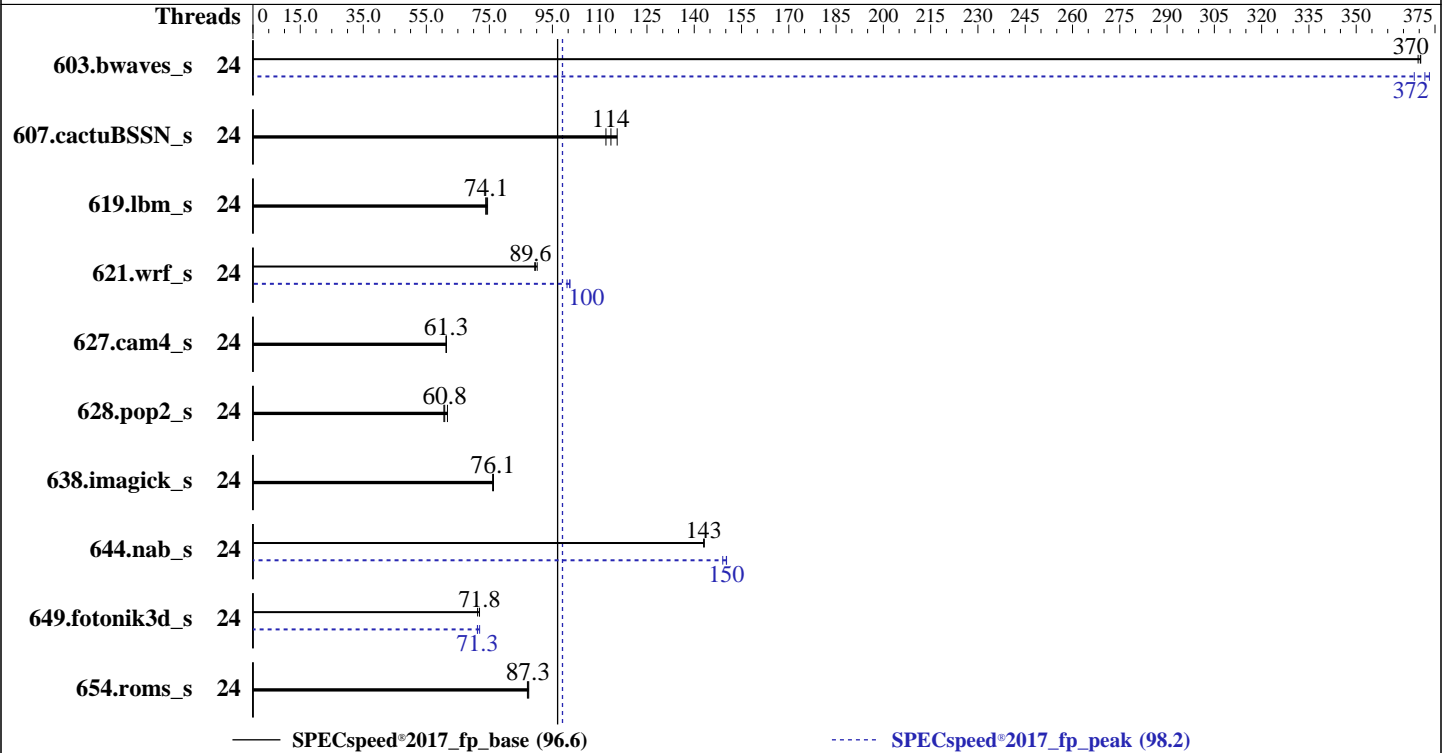
$SPECspeed®2017\_fp\_base = 96.6$

PowerEdge R440 (Intel Xeon Silver 4214R, 2.4 GHz)

$SPECspeed®2017\_fp\_peak = 98.2$

CPU2017 License: 55   **Test Date:** Sep-2020
Test Sponsor: Dell Inc   **Hardware Availability:** Jul-2020
Tested by: Dell Inc.   **Software Availability:** Apr-2020

Threads scale: 0 15.0 35.0 55.0 75.0 95.0 110 125 140 155 170 185 200 215 230 245 260 275 290 305 320 335 350 375

Benchmark	Threads	base	peak
603.bwaves_s	24	370	372
607.cactuBSSN_s	24	114	
619.lbm_s	24	74.1	
621.wrf_s	24	89.6	100
627.cam4_s	24	61.3	
628.pop2_s	24	60.8	
638.imagick_s	24	76.1	
644.nab_s	24	143	150
649.fotonik3d_s	24	71.8	71.3
654.roms_s	24	87.3	

—— SPECspeed®2017_fp_base (96.6) ----- SPECspeed®2017_fp_peak (98.2)

Hardware

CPU Name: Intel Xeon Silver 4214R
 Max MHz: 3500
 Nominal: 2400
Enabled: 24 cores, 2 chips
Orderable: 1, 2
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 16.5 MB I+D on chip per chip
 Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-3200AA-R, running at 2400)
Storage: 1 x 1.92TB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux 8.1
 kernel 4.18.0-147.el8.x86_64
Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux;
 Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux
Parallel: Yes
Firmware: Version 2.8.1 released Jun-2020
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	24	160	370	159	370	**159**	**370**	24	**159**	**372**	160	368	158	373
607.cactuBSSN_s	24	149	112	144	116	**147**	**114**	24	149	112	144	116	**147**	**114**
619.lbm_s	24	70.4	74.4	**70.7**	**74.1**	70.9	73.8	24	70.4	74.4	**70.7**	**74.1**	70.9	73.8
621.wrf_s	24	147	90.2	148	89.4	**148**	**89.6**	24	132	101	**132**	**100**	133	99.6
627.cam4_s	24	**145**	**61.3**	144	61.3	145	61.3	24	**145**	**61.3**	144	61.3	145	61.3
628.pop2_s	24	196	60.6	192	61.7	**195**	**60.8**	24	196	60.6	192	61.7	**195**	**60.8**
638.imagick_s	24	190	76.1	189	76.3	**189**	**76.1**	24	190	76.1	189	76.3	**189**	**76.1**
644.nab_s	24	122	143	**122**	**143**	122	143	24	116	150	117	149	**116**	**150**
649.fotonik3d_s	24	128	71.3	**127**	**71.8**	127	71.9	24	128	71.2	**128**	**71.3**	127	71.9
654.roms_s	24	**180**	**87.3**	180	87.5	181	87.1	24	**180**	**87.3**	180	87.5	181	87.1
SPECspeed®2017_fp_base =		**96.6**												
SPECspeed®2017_fp_peak =		**98.2**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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$SPECspeed®2017\_fp\_base = 96.6$

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CPU2017 License:	55	**Test Date:**	Sep-2020
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Tested by:	Dell Inc.	**Software Availability:**	Apr-2020

General Notes (Continued)

```
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub set to standard
Logical Processor disabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
UPI Prefetch enabled
LLC Prefetch disabled
Dead Line LLC Alloc enabled
Directory AtoS disabled


Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on localhost.localdomain Tue Sep 15 12:22:17 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Silver 4214R CPU @ 2.40GHz
      2  "physical id"s (chips)
      24 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 12
      siblings  : 12
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
```

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Platform Notes (Continued)

```
        Byte Order:          Little Endian
        CPU(s):              24
        On-line CPU(s) list: 0-23
        Thread(s) per core:  1
        Core(s) per socket:  12
        Socket(s):           2
        NUMA node(s):        2
        Vendor ID:           GenuineIntel
        CPU family:          6
        Model:               85
        Model name:          Intel(R) Xeon(R) Silver 4214R CPU @ 2.40GHz
        Stepping:            7
        CPU MHz:             1878.470
        CPU max MHz:         3500.0000
        CPU min MHz:         1000.0000
        BogoMIPS:            4800.00
        Virtualization:      VT-x
        L1d cache:           32K
        L1i cache:           32K
        L2 cache:            1024K
        L3 cache:            16896K
        NUMA node0 CPU(s):   0,2,4,6,8,10,12,14,16,18,20,22
        NUMA node1 CPU(s):   1,3,5,7,9,11,13,15,17,19,21,23
        Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
    avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
    invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
    flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
    cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
    avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
    cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
    arch_capabilities

 /proc/cpuinfo cache data
    cache size : 16896 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22
    node 0 size: 192049 MB
    node 0 free: 184491 MB
    node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23
    node 1 size: 193532 MB
```

(Continued on next page)

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CPU2017 License: 55   **Test Date:**   Sep-2020
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Platform Notes (Continued)

```
   node 1 free: 192460 MB
   node distances:
   node   0   1
     0:  10  21
     1:  21  10

 From /proc/meminfo
    MemTotal:       394836832 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux"
       VERSION="8.1 (Ootpa)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="8.1"
       PLATFORM_ID="platform:el8"
       PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
       ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga

 uname -a:
    Linux localhost.localdomain 4.18.0-147.el8.x86_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019
    x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 CVE-2018-3620 (L1 Terminal Fault):         Not affected
 Microarchitectural Data Sampling:          Not affected
 CVE-2017-5754 (Meltdown):                  Not affected
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                    via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):      Mitigation: usercopy/swapgs barriers and __user
                                    pointer sanitization
 CVE-2017-5715 (Spectre variant 2):      Mitigation: Enhanced IBRS, IBPB: conditional,
                                    RSB filling


 run-level 3 Sep 15 07:31 last=5

 SPEC is set to: /home/cpu2017
    Filesystem           Type   Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs    1.7T   29G  1.7T   2% /home
```

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Platform Notes (Continued)

```
  From /sys/devices/virtual/dmi/id
      BIOS:    Dell Inc. 2.8.1 06/30/2020
      Vendor:  Dell Inc.
      Product: PowerEdge R440
      Product Family: PowerEdge
      Serial:  F9TD613

  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    Memory:
      12x 002C069D002C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200
      4x Not Specified Not Specified

  (End of data from sysinfo program)
Memory running at 2400
```

Compiler Version Notes

```
==============================================================================
C                   | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
                    | 644.nab_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
Fortran             | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
                    | 654.roms_s(base, peak)
```

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Compiler Version Notes (Continued)

```
-------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------


=========================================================================
Fortran, C       | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                 | 628.pop2_s(base, peak)
-------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation.  All rights reserved.
-------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`

Base Portability Flags

603.bwaves_s: `-DSPEC_LP64`
607.cactuBSSN_s: `-DSPEC_LP64`
619.lbm_s: `-DSPEC_LP64`
621.wrf_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
627.cam4_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
628.pop2_s: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
`-assume byterecl`
638.imagick_s: `-DSPEC_LP64`
644.nab_s: `-DSPEC_LP64`

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Base Portability Flags (Continued)

649.fotonik3d_s: `-DSPEC_LP64`
654.roms_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -std=c11 -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
```

Fortran benchmarks:
```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-nostandard-realloc-lhs -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:
```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
ifort icc
```

Benchmarks using Fortran, C, and C++:
```
icpc icc ifort
```

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

  619.lbm_s: basepeak = yes

  638.imagick_s: basepeak = yes

  644.nab_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

  603.bwaves_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX2
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

  649.fotonik3d_s: Same as 603.bwaves_s

  654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

  621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX2 -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

  627.cam4_s: basepeak = yes

  628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

(Continued on next page)

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Peak Optimization Flags (Continued)

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE12.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE12.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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