



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55

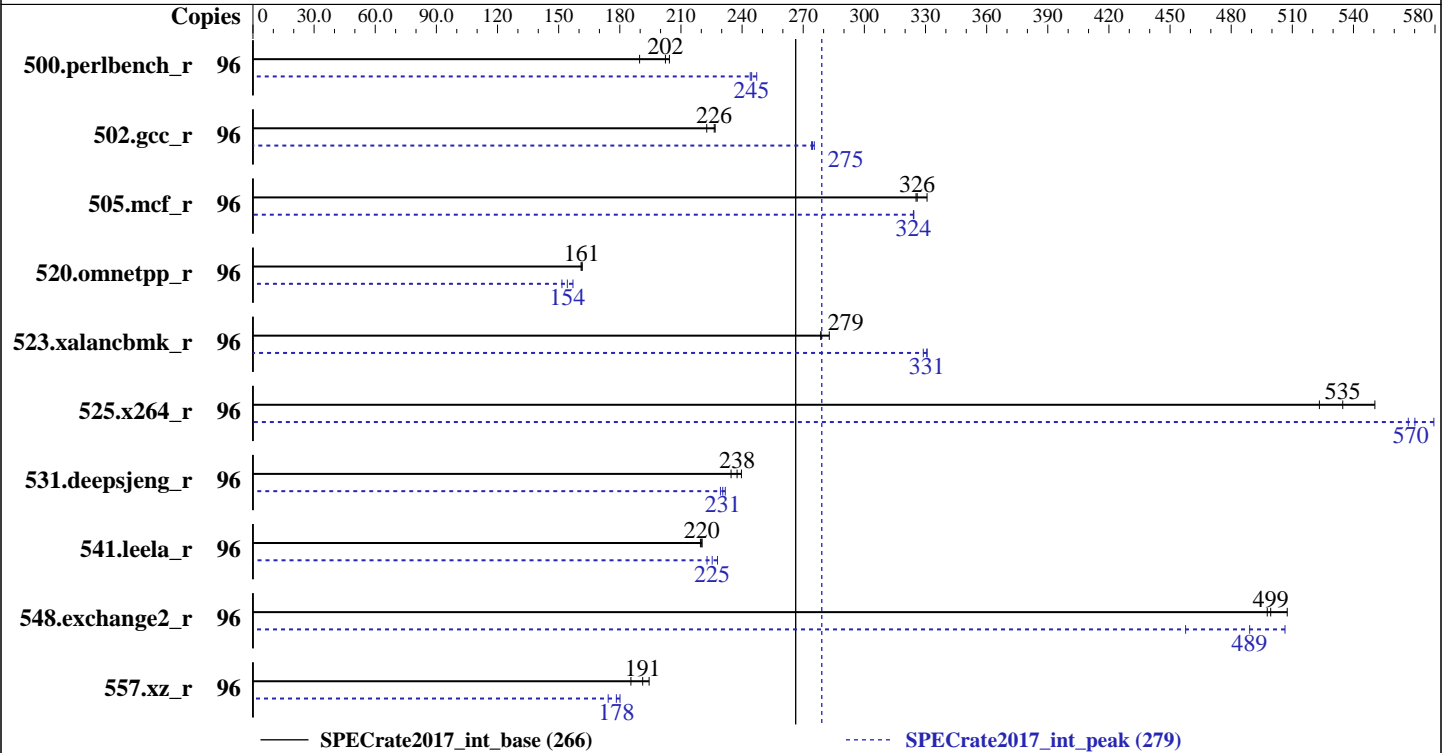
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2018

Hardware Availability: Sep-2018

Software Availability: Apr-2018



### Hardware

CPU Name: Intel Xeon Gold 6146  
 Max MHz.: 4200  
 Nominal: 3200  
 Enabled: 48 cores, 4 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
 Storage: 960 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 12 SP3  
 4.4.126-94.22-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran  
 Compiler for Linux  
 Parallel: No  
 Firmware: Version 0.4.5 released May-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator library, version 5.0.1



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jun-2018  
Hardware Availability: Sep-2018  
Software Availability: Apr-2018

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	806	190	<b><u>755</u></b>	<b><u>202</u></b>	748	204	96	626	244	<b><u>625</u></b>	<b><u>245</u></b>	618	247
502.gcc_r	96	611	223	<b><u>601</u></b>	<b><u>226</u></b>	599	227	96	494	275	496	274	<b><u>495</u></b>	<b><u>275</u></b>
505.mcf_r	96	<b><u>476</u></b>	<b><u>326</u></b>	469	331	477	325	96	479	324	<b><u>479</u></b>	<b><u>324</u></b>	478	324
520.omnetpp_r	96	<b><u>780</u></b>	<b><u>161</u></b>	782	161	780	162	96	802	157	831	152	<b><u>816</u></b>	<b><u>154</u></b>
523.xalancbmk_r	96	358	283	364	279	<b><u>364</u></b>	<b><u>279</u></b>	96	<b><u>307</u></b>	<b><u>331</u></b>	306	331	308	329
525.x264_r	96	321	523	<b><u>314</u></b>	<b><u>535</u></b>	305	551	96	290	579	<b><u>295</u></b>	<b><u>570</u></b>	297	567
531.deepsjeng_r	96	<b><u>463</u></b>	<b><u>238</u></b>	469	235	459	240	96	480	229	<b><u>477</u></b>	<b><u>231</u></b>	475	232
541.leela_r	96	724	220	721	220	<b><u>722</u></b>	<b><u>220</u></b>	96	714	223	<b><u>706</u></b>	<b><u>225</u></b>	697	228
548.exchange2_r	96	496	507	505	498	<b><u>504</u></b>	<b><u>499</u></b>	96	550	458	<b><u>514</u></b>	<b><u>489</u></b>	497	506
557.xz_r	96	533	194	<b><u>542</u></b>	<b><u>191</u></b>	559	185	96	595	174	<b><u>581</u></b>	<b><u>178</u></b>	576	180

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2017/lib/ia32:/root/cpu2017/lib/intel64:/root/cpu2017/je5.0.1-32:/root/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jun-2018

**Hardware Availability:** Sep-2018

**Software Availability:** Apr-2018

## General Notes (Continued)

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS settings:

Sub NUMA Cluster Disabled

Virtualization Technology Disabled

System Profile set to Custom

CPU Performance set to Maximum Performance

C States set to Autonomous

ClEE Disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Performance

Memory Patrol Scrub Disabled

Logical Processor Enabled

CPU Interconnect Bus Link Power Management Disabled

PCI ASPM L1 Link Power Management Disabled

Sysinfo program /root/cpu2017/bin/sysinfo

Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f

running on linux-0x7z Tue Jun 12 18:10:51 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz

4 "physical id"s (chips)

96 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 12

siblings : 24

physical 0: cores 0 1 2 3 4 9 10 16 18 19 25 26

physical 1: cores 0 1 2 3 4 9 10 16 18 19 25 26

physical 2: cores 0 1 2 3 4 9 10 16 18 19 25 26

physical 3: cores 0 1 2 3 4 9 10 16 18 19 25 26

From lscpu:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 96

On-line CPU(s) list: 0-95

Thread(s) per core: 2

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2018

Hardware Availability: Sep-2018

Software Availability: Apr-2018

## Platform Notes (Continued)

```

Core(s) per socket:      12
Socket(s):               4
NUMA node(s):           8
Vendor ID:               GenuineIntel
CPU family:              6
Model:                   85
Model name:              Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
Stepping:                4
CPU MHz:                  3192.526
BogoMIPS:                 6385.05
Virtualization:          VT-x
L1d cache:               32K
L1i cache:               32K
L2 cache:                 1024K
L3 cache:                 25344K
NUMA node0 CPU(s):      0, 8, 16, 24, 32, 40, 48, 56, 64, 72, 80, 88
NUMA node1 CPU(s):      1, 9, 17, 25, 33, 41, 49, 57, 65, 73, 81, 89
NUMA node2 CPU(s):      2, 10, 18, 26, 34, 42, 50, 58, 66, 74, 82, 90
NUMA node3 CPU(s):      3, 11, 19, 27, 35, 43, 51, 59, 67, 75, 83, 91
NUMA node4 CPU(s):      4, 12, 20, 28, 36, 44, 52, 60, 68, 76, 84, 92
NUMA node5 CPU(s):      5, 13, 21, 29, 37, 45, 53, 61, 69, 77, 85, 93
NUMA node6 CPU(s):      6, 14, 22, 30, 38, 46, 54, 62, 70, 78, 86, 94
NUMA node7 CPU(s):      7, 15, 23, 31, 39, 47, 55, 63, 71, 79, 87, 95
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx fl16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 8 16 24 32 40 48 56 64 72 80 88
node 0 size: 95361 MB
node 0 free: 95163 MB
node 1 cpus: 1 9 17 25 33 41 49 57 65 73 81 89
node 1 size: 96762 MB
node 1 free: 96606 MB
node 2 cpus: 2 10 18 26 34 42 50 58 66 74 82 90

```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2018

Hardware Availability: Sep-2018

Software Availability: Apr-2018

## Platform Notes (Continued)

```

node 2 size: 96762 MB
node 2 free: 96634 MB
node 3 cpus: 3 11 19 27 35 43 51 59 67 75 83 91
node 3 size: 96762 MB
node 3 free: 96629 MB
node 4 cpus: 4 12 20 28 36 44 52 60 68 76 84 92
node 4 size: 96761 MB
node 4 free: 96630 MB
node 5 cpus: 5 13 21 29 37 45 53 61 69 77 85 93
node 5 size: 96761 MB
node 5 free: 96632 MB
node 6 cpus: 6 14 22 30 38 46 54 62 70 78 86 94
node 6 size: 96761 MB
node 6 free: 96642 MB
node 7 cpus: 7 15 23 31 39 47 55 63 71 79 87 95
node 7 size: 96759 MB
node 7 free: 96629 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 21 21 21 11 21 21 21
  1: 21 10 21 21 21 11 21 21
  2: 21 21 10 21 21 21 11 21
  3: 21 21 21 10 21 21 21 11
  4: 11 21 21 21 10 21 21 21
  5: 21 11 21 21 21 10 21 21
  6: 21 21 11 21 21 21 10 21
  7: 21 21 21 11 21 21 21 10

```

From /proc/meminfo

```

MemTotal:      791237720 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 3

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP3"

VERSION\_ID="12.3"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP3"

ID="sles"

ANSI\_COLOR="0;32"

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2018

Hardware Availability: Sep-2018

Software Availability: Apr-2018

## Platform Notes (Continued)

CPE\_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:

```
Linux linux-0x7z 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 11 19:08

SPEC is set to: /root/cpu2017

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   927G  25G  902G   3% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.4.5 05/31/2018

Memory:

```
26x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666
2x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
20x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666
```

(End of data from sysinfo program)

## Compiler Version Notes

```
=====  
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
525.x264_r(base, peak) 557.xz_r(base, peak)  
-----
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CC 500.perlbench_r(peak) 502.gcc_r(peak)  
-----
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
541.leela_r(base)  
-----
```

```
icpc (ICC) 18.0.0 20170811
```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jun-2018

**Hardware Availability:** Sep-2018

**Software Availability:** Apr-2018

## Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CXXC 520.omnetpp\_r(peak) 523.xalancbmk\_r(peak) 531.deepsjeng\_r(peak)  
541.leela\_r(peak)

-----  
icpc (ICC) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
FC 548.exchange2\_r(base, peak)

-----  
ifort (IFORT) 18.0.0 20170811

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502 gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146,  
3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jun-2018

**Hardware Availability:** Sep-2018

**Software Availability:** Apr-2018

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

## Peak Compiler Invocation

C benchmarks:

```
icc
```

C++ benchmarks:

```
icpc
```

Fortran benchmarks:

```
ifort
```

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
```

```
502.gcc_r: -D_FILE_OFFSET_BITS=64
```

(Continued on next page)





# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146,  
3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

**CPU2017 License:** 55

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jun-2018

**Hardware Availability:** Sep-2018

**Software Availability:** Apr-2018

## Peak Portability Flags (Continued)

```
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

```
505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
557.xz_r: Same as 505.mcf_r
```

C++ benchmarks:

```
520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

**Dell Inc.**

PowerEdge MX840c (Intel Xeon Gold 6146, 3.20GHz)

SPECrate2017\_int\_base = 266

SPECrate2017\_int\_peak = 279

CPU2017 License: 55

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jun-2018

Hardware Availability: Sep-2018

Software Availability: Apr-2018

## Peak Optimization Flags (Continued)

531.deepsjeng\_r: Same as 520.omnetpp\_r

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Peak Other Flags

C benchmarks (except as noted below):

```
-m64 -std=c11
```

502.gcc\_r: -m32 -std=c11

C++ benchmarks (except as noted below):

```
-m64
```

523.xalancbmk\_r: -m32

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-06-12 19:10:50-0400.

Report generated on 2018-10-31 19:04:05 by CPU2017 PDF formatter v6067.

Originally published on 2018-10-16.