



# SPEC<sup>®</sup> CFP2006 Result

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## Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Gold 6146, 3.20 GHz)

SPECfp<sup>®</sup>2006 = **Not Run**

SPECfp\_base2006 = **156**

CPU2006 license: 9019

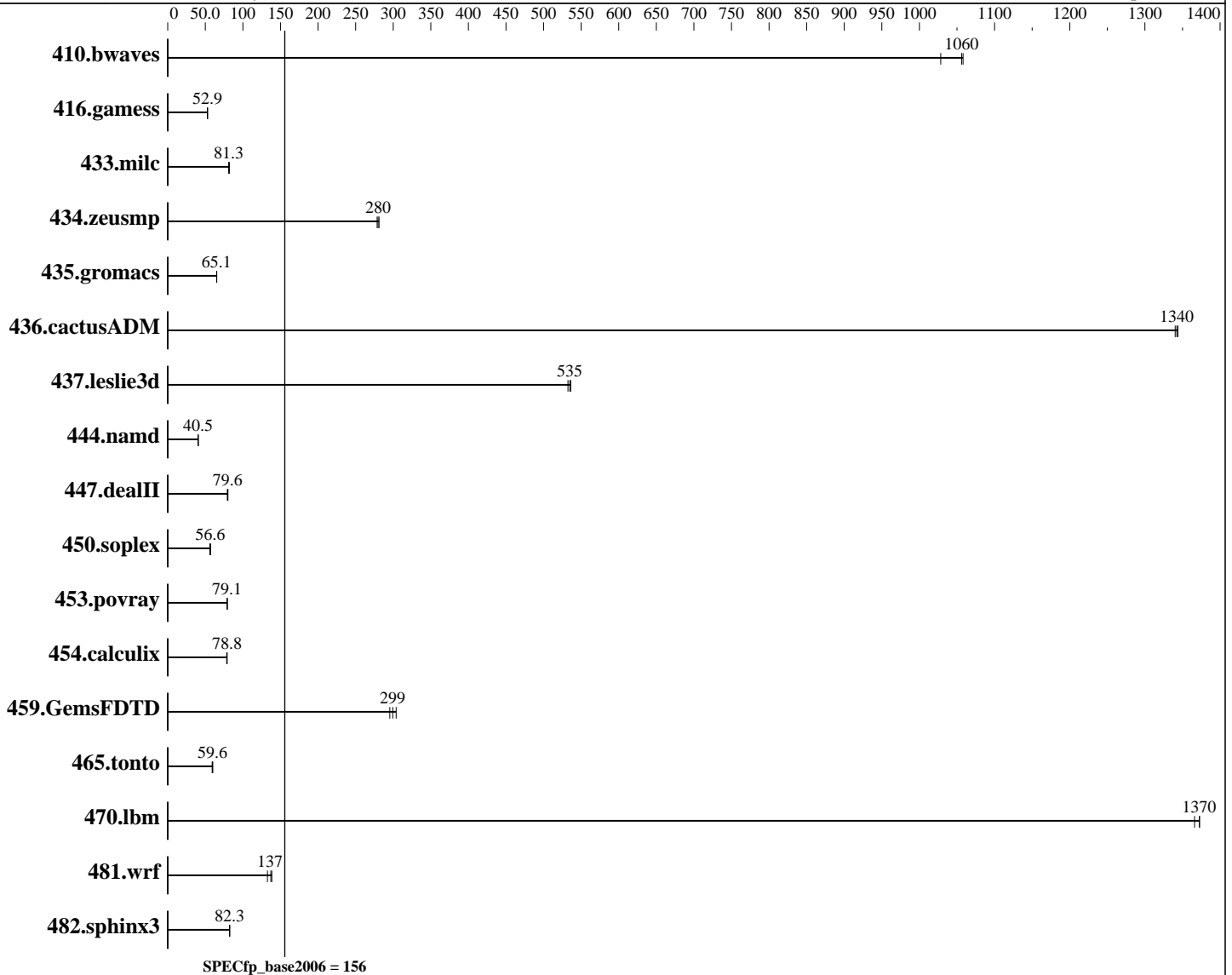
Test sponsor: Cisco Systems

Tested by: Cisco Systems

Test date: Sep-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



### Hardware

CPU Name: Intel Xeon Gold 6146  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 24.75 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)  
Disk Subsystem: 1 x 600 GB SAS HDD, 10K RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	13.2	1030	<u>12.9</u>	<u>1060</u>	12.8	1060						
416.gamess	370	52.9	<u>370</u>	<u>52.9</u>	370	52.9						
433.milc	112	82.0	<u>113</u>	<u>81.3</u>	113	81.2						
434.zeusmp	32.4	281	32.7	279	<u>32.5</u>	<u>280</u>						
435.gromacs	109	65.3	<u>110</u>	<u>65.1</u>	110	65.1						
436.cactusADM	8.92	1340	<u>8.90</u>	<u>1340</u>	8.89	1340						
437.leslie3d	17.7	532	<u>17.6</u>	<u>535</u>	17.5	536						
444.namd	198	40.5	<u>198</u>	<u>40.5</u>	198	40.5						
447.dealII	<u>144</u>	<u>79.6</u>	143	80.1	144	79.5						
450.soplex	<u>147</u>	<u>56.6</u>	146	57.0	149	56.0						
453.povray	<u>67.3</u>	<u>79.1</u>	67.1	79.3	67.4	78.9						
454.calculix	104	79.0	<u>105</u>	<u>78.8</u>	105	78.5						
459.GemsFDTD	<u>35.4</u>	<u>299</u>	34.9	304	35.9	295						
465.tonto	166	59.1	164	59.9	<u>165</u>	<u>59.6</u>						
470.lbm	<u>10.0</u>	<u>1370</u>	10.1	1370	10.0	1370						
481.wrf	80.7	138	<u>81.4</u>	<u>137</u>	84.2	133						
482.sphinx3	<u>237</u>	<u>82.3</u>	236	82.5	237	82.2						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Intel HyperThreading Technology set to Disabled  
CPU performance set to Enterprise  
Power Performance Tuning set to OS  
SNC set to Disabled  
IMC Interleaving set to Auto  
Patrol Scrub set to Disabled  
Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-djj4 Sat Sep 9 13:50:38 2017

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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings  : 12
  physical 0: cores 0 1 2 3 8 9 10 11 18 19 24 27
  physical 1: cores 0 1 2 3 4 8 10 11 18 24 25 27
cache size : 25344 KB
```

```
From /proc/meminfo
MemTotal:      394667772 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-djj4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 2 00:36

```
SPEC is set to: /home/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        xfs   559G   32G  528G   6% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
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### Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017

Memory:

24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"

OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

### Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

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info@spec.org

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## Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch`

Fortran benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

Benchmarks using both Fortran and C:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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