



SPEC[®] CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp[®]2006 = 146

SPECfp_base2006 = 139

CPU2006 license: 9017

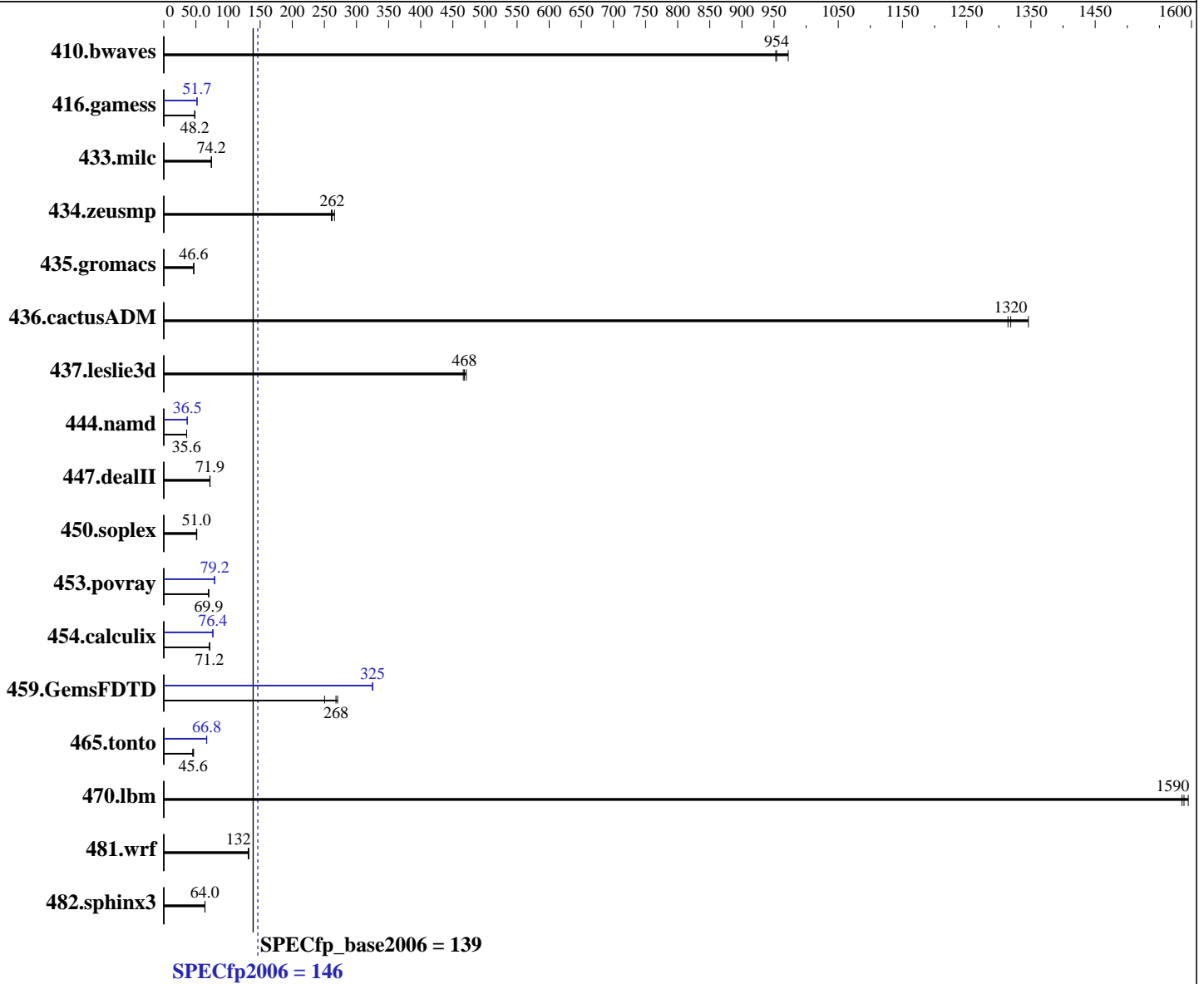
Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Jul-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6152
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64)
 Kernel 4.4.21-69-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: btrfs
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = **146**

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp_base2006 = **139**

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

L3 Cache: 30.25 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 800 GB SAS SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 14.0 | 972 | 14.2 | 954 | 14.3 | 953 | 14.0 | 972 | 14.2 | 954 | 14.3 | 953 |
| 416.gamess | 406 | 48.3 | 406 | 48.2 | 406 | 48.2 | 378 | 51.8 | 379 | 51.6 | 379 | 51.7 |
| 433.milc | 124 | 74.0 | 124 | 74.2 | 124 | 74.2 | 124 | 74.0 | 124 | 74.2 | 124 | 74.2 |
| 434.zeusmp | 34.7 | 262 | 34.2 | 266 | 34.9 | 261 | 34.7 | 262 | 34.2 | 266 | 34.9 | 261 |
| 435.gromacs | 153 | 46.7 | 153 | 46.6 | 153 | 46.6 | 153 | 46.7 | 153 | 46.6 | 153 | 46.6 |
| 436.cactusADM | 9.09 | 1310 | 8.88 | 1350 | 9.06 | 1320 | 9.09 | 1310 | 8.88 | 1350 | 9.06 | 1320 |
| 437.leslie3d | 20.0 | 471 | 20.1 | 468 | 20.2 | 466 | 20.0 | 471 | 20.1 | 468 | 20.2 | 466 |
| 444.namd | 225 | 35.6 | 225 | 35.6 | 225 | 35.6 | 220 | 36.5 | 220 | 36.5 | 220 | 36.5 |
| 447.dealII | 159 | 71.9 | 159 | 72.0 | 160 | 71.7 | 159 | 71.9 | 159 | 72.0 | 160 | 71.7 |
| 450.soplex | 164 | 51.0 | 162 | 51.4 | 164 | 51.0 | 164 | 51.0 | 162 | 51.4 | 164 | 51.0 |
| 453.povray | 76.4 | 69.6 | 76.1 | 69.9 | 76.1 | 69.9 | 67.4 | 78.9 | 67.2 | 79.2 | 67.1 | 79.2 |
| 454.calculix | 115 | 71.4 | 116 | 71.2 | 116 | 71.1 | 108 | 76.2 | 108 | 76.5 | 108 | 76.4 |
| 459.GemsFDTD | 39.2 | 271 | 39.6 | 268 | 42.4 | 250 | 32.6 | 325 | 32.6 | 325 | 32.7 | 325 |
| 465.tonto | 212 | 46.4 | 216 | 45.6 | 218 | 45.1 | 147 | 66.8 | 147 | 66.8 | 148 | 66.5 |
| 470.lbm | 8.61 | 1590 | 8.67 | 1590 | 8.65 | 1590 | 8.61 | 1590 | 8.67 | 1590 | 8.65 | 1590 |
| 481.wrf | 84.7 | 132 | 84.5 | 132 | 85.0 | 131 | 84.7 | 132 | 84.5 | 132 | 85.0 | 131 |
| 482.sphinx3 | 304 | 64.0 | 304 | 64.2 | 305 | 64.0 | 304 | 64.0 | 304 | 64.2 | 305 | 64.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on Cable-SPECcpu2017-SUSE12SP2 Wed Jul 12 22:52:09 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 146

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp_base2006 = 139

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz

2 "physical id"s (chips)

44 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 22

siblings : 22

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

cache size : 30976 KB

From /proc/meminfo

MemTotal: 395892392 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 2

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION_ID="12.2"

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux Cable-SPECcpu2017-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25

10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 12 22:51

SPEC is set to: /home/cpu2006-1.2-ic17.0u3

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda2 btrfs 744G 35G 708G 5% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 146

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp_base2006 = 139

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE109Q-1.00]- 06/28/2017

Memory:

24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

OMP_NUM_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages disabled with:

echo never > /sys/kernel/mm/transparent_hugepage/enabled

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 146

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp_base2006 = 139

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Base Portability Flags (Continued)

```

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

```

Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECfp2006 = 146

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp_base2006 = 139

CPU2006 license: 9017

Test date: Jul-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6152)

SPECfp2006 = 146

SPECfp_base2006 = 139

CPU2006 license: 9017

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Jul-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017

Peak Optimization Flags (Continued)

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Oct 4 12:35:48 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 October 2017.