# SPEC® CFP2006 Result



## Acer Incorporated

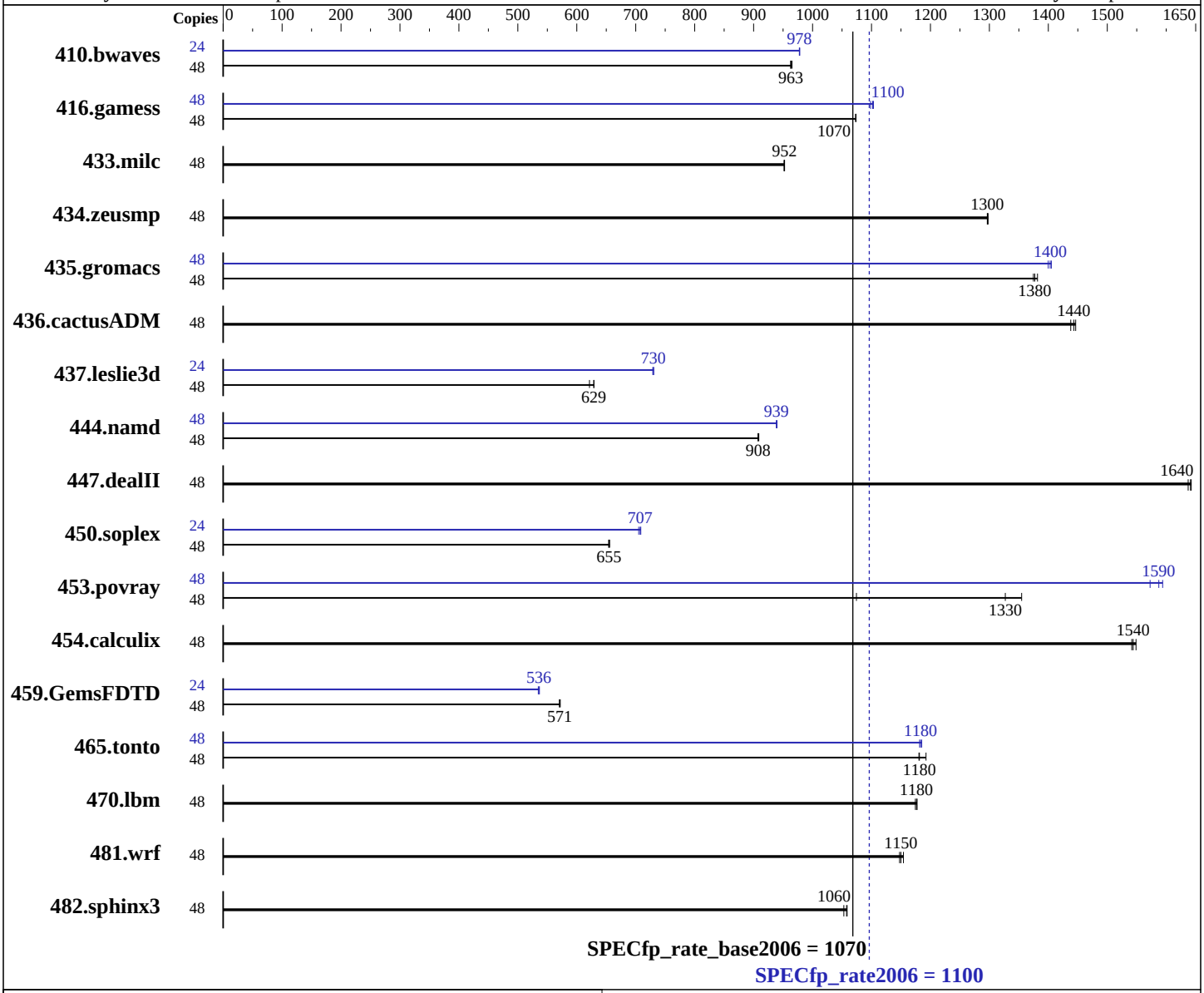
Altos W2000h-W370h F4 (Intel Xeon Gold 6126)

SPECfp®_rate2006 = 1100

SPECfp_rate_base2006 = 1070

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Sep-2017
**Hardware Availability:** Oct-2017
**Software Availability:** Apr-2017




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Gold 6126 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.70 GHz |
| CPU MHz: | 2600 |
| FPU: | Integrated |
| CPU(s) enabled: | 24 cores, 2 chips, 12 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default |
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux |
| Auto Parallel: | Yes |
| File System: | btrfs |
| System State: | Run level 3 (multi-user) |

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L3 Cache: 19.25 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 48 | 676 | 965 | **677** | **963** | 678 | 963 | 24 | **334** | **978** | 334 | 978 | 333 | 978 |
| 416.gamess | 48 | **876** | **1070** | 876 | 1070 | 875 | 1070 | 48 | 856 | 1100 | **853** | **1100** | 852 | 1100 |
| 433.milc | 48 | 463 | 953 | 463 | 952 | **463** | **952** | 48 | 463 | 953 | 463 | 952 | **463** | **952** |
| 434.zeusmp | 48 | 337 | 1300 | **337** | **1300** | 337 | 1300 | 48 | 337 | 1300 | **337** | **1300** | 337 | 1300 |
| 435.gromacs | 48 | 248 | 1380 | **249** | **1380** | 249 | 1370 | 48 | 244 | 1410 | **244** | **1400** | 245 | 1400 |
| 436.cactusADM | 48 | **397** | **1440** | 399 | 1440 | 397 | 1450 | 48 | **397** | **1440** | 399 | 1440 | 397 | 1450 |
| 437.leslie3d | 48 | 717 | 629 | **718** | **629** | 726 | 622 | 24 | **309** | **730** | 309 | 730 | 310 | 729 |
| 444.namd | 48 | **424** | **908** | 424 | 908 | 424 | 909 | 48 | **410** | **939** | 410 | 940 | 410 | 939 |
| 447.dealII | 48 | 334 | 1640 | **335** | **1640** | 335 | 1640 | 48 | 334 | 1640 | **335** | **1640** | 335 | 1640 |
| 450.soplex | 48 | 612 | 654 | 610 | 656 | **612** | **655** | 24 | **283** | **707** | 284 | 705 | 282 | 709 |
| 453.povray | 48 | 188 | 1350 | **192** | **1330** | 238 | 1070 | 48 | 160 | 1590 | **161** | **1590** | 162 | 1570 |
| 454.calculix | 48 | 256 | 1550 | 257 | 1540 | **256** | **1540** | 48 | 256 | 1550 | 257 | 1540 | **256** | **1540** |
| 459.GemsFDTD | 48 | 890 | 572 | **892** | **571** | 893 | 571 | 24 | 476 | 535 | 475 | 537 | **475** | **536** |
| 465.tonto | 48 | 400 | 1180 | 396 | 1190 | **400** | **1180** | 48 | **399** | **1180** | 399 | 1190 | 400 | 1180 |
| 470.lbm | 48 | 562 | 1170 | **561** | **1180** | 560 | 1180 | 48 | 562 | 1170 | **561** | **1180** | 560 | 1180 |
| 481.wrf | 48 | 467 | 1150 | 465 | 1150 | **466** | **1150** | 48 | 467 | 1150 | 465 | 1150 | **466** | **1150** |
| 482.sphinx3 | 48 | **884** | **1060** | 884 | 1060 | 888 | 1050 | 48 | **884** | **1060** | 884 | 1060 | 888 | 1050 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Configuration:
CPU Power and Performance Policy set to Performance
IMC set to 1-way interleaving
```

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## Platform Notes (Continued)

```
Sub_NUMA Cluster set to enabled
Set Fan Profile set to Performance
Sysinfo program /usr/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-irn9 Fri Sep  8 02:39:16 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6126 CPU @ 2.60GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 3 4 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 11 12 13
   cache size : 19712 KB

From /proc/meminfo
   MemTotal:       394675016 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-irn9 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
   (9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

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## Platform Notes (Continued)

```
 run-level 3 Sep 7 17:10

 SPEC is set to: /usr/cpu2006
    Filesystem     Type   Size  Used Avail Use% Mounted on
    /dev/sda3      btrfs  3.7T  202G  3.5T   6% /
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Intel Corporation SE5C620.86B.00.01.0004.071220170215 07/12/2017
   Memory:
    4x Empty/NO DIMM NO DIMM
    12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz

 (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/lib/ia32:/usr/cpu2006/lib/intel64:/usr/cpu2006/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled by default
 Filesystem page cache cleared with:
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

Continued on next page

## Peak Optimization Flags (Continued)

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-malloc-options=3 -qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -auto -inline-calloc -qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2) -par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32 -qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.3-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.3-revC.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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