



# SPEC® CINT2006 Result

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## ACTION S.A.

ACTINA SOLAR 222 S6 (Intel Xeon E5-2650 v4, 2.20 GHz)

**SPECint®2006 = 60.1**

**SPECint\_base2006 = 57.9**

CPU2006 license: 9008

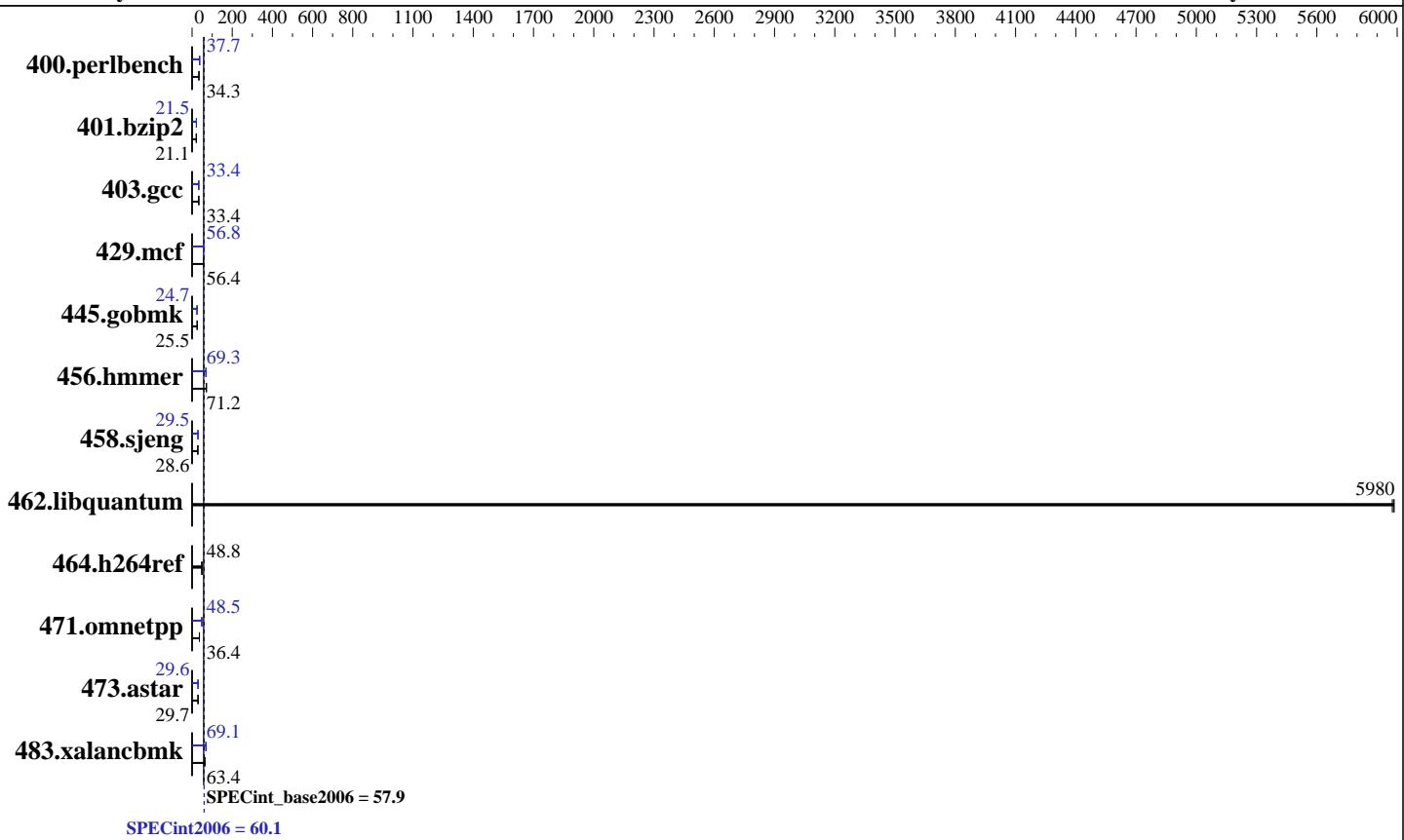
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

**Test date:** Jul-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Mar-2016



### Hardware

CPU Name: Intel Xeon E5-2650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 240 GB SATA II SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Compiler: 3.10.0-327.18.2.el7.x86\_64  
 Auto Parallel: C/C++: Version 16.0.3.210 of Intel C++ Studio XE for Linux  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>285</b>	<b>34.3</b>	285	34.2	285	34.3	<b>259</b>	<b>37.7</b>	<b>259</b>	<b>37.7</b>	260	37.6
401.bzip2	<b>458</b>	<b>21.1</b>	456	21.2	459	21.0	<b>449</b>	<b>21.5</b>	<b>449</b>	<b>21.5</b>	449	21.5
403.gcc	241	33.4	<b>241</b>	<b>33.4</b>	240	33.5	<b>240</b>	<b>33.5</b>	243	33.2	<b>241</b>	<b>33.4</b>
429.mcf	<b>162</b>	<b>56.4</b>	163	56.0	161	56.5	<b>160</b>	<b>56.9</b>	<b>161</b>	<b>56.8</b>	161	56.5
445.gobmk	412	25.5	<b>412</b>	<b>25.5</b>	412	25.4	<b>425</b>	<b>24.7</b>	425	24.7	<b>425</b>	<b>24.7</b>
456.hmmer	131	71.1	<b>131</b>	<b>71.2</b>	131	71.3	<b>135</b>	<b>69.3</b>	135	69.1	135	69.3
458.sjeng	422	28.7	422	28.6	<b>422</b>	<b>28.6</b>	<b>410</b>	<b>29.5</b>	410	29.5	410	29.5
462.libquantum	3.47	5980	3.46	5990	<b>3.46</b>	<b>5980</b>	3.47	5980	3.46	5990	<b>3.46</b>	<b>5980</b>
464.h264ref	455	48.6	<b>453</b>	<b>48.8</b>	453	48.9	<b>455</b>	<b>48.6</b>	<b>453</b>	<b>48.8</b>	453	48.9
471.omnetpp	170	36.8	172	36.3	<b>172</b>	<b>36.4</b>	<b>129</b>	<b>48.5</b>	129	48.6	129	48.4
473.astar	235	29.8	237	29.7	<b>236</b>	<b>29.7</b>	237	29.6	<b>237</b>	<b>29.6</b>	237	29.6
483.xalancbmk	109	63.6	109	63.0	<b>109</b>	<b>63.4</b>	<b>99.9</b>	<b>69.1</b>	100	69.0	99.5	69.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

### Bios Settings

Hyper-Threading (All) = Disable  
Power Technology = Energy Efficient  
Enforce POR = Disabled  
Memory Frequency = 2133  
COD Enable = Disable

### BMC Setting

Fan Mode = Full Speed

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 ## e86d102572650a6e4d596a3cee98f191  
running on SUT Thu Jul 21 15:49:01 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
Continued on next page



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<b>CPU2006 license:</b> 9008	<b>Test date:</b>	Jul-2016
<b>Test sponsor:</b> ACTION S.A.	<b>Hardware Availability:</b>	Mar-2016
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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.20GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 12
  siblings : 12
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

From /proc/meminfo
MemTotal:      263860060 kB
HugePages_Total:       1
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
os-release.rpmsave:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux SUT 3.10.0-327.18.2.el7.x86_64 #2 SMP Wed Jun 1 17:37:13 CEST 2016
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jul 21 15:48

```
SPEC is set to: /cpu2006.1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal      ext4  212G  131G  70G  66% /
Continued on next page
```



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## Platform Notes (Continued)

Additional information from dmidecode:

BIOS American Megatrends Inc. 2.0 12/17/2015

Memory:

16x 16 GB

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2400 MHz 2 rank

(End of data from sysinfo program)

dmidecode does not properly detect memory modules

16 modules of 16 GB were used to run the test (256 GB total)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

OMP\_NUM\_THREADS = "24"

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

Binaries compiled on a system with 2x Xeon E5-2650 v4 chips + 256 GB memory  
using RedHat EL 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

```
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

C++ benchmarks:

```
471.omnetpp: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

```
473.astar: icpc -m64
```

```
483.xalancbmk: icpc -m32 -L/opt/intel/lib/ia32
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -DSPEC_CPU_LP64
```

```
429.mcf: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -DSPEC_CPU_LP64
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32
             -ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes
```

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
            -auto-p32 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                -ansi-alias -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap
```

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevD-aug-2015-For-Supermicro-Platform.xml>

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