



# SPEC® CINT2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen9

(2.60 GHz, Intel Xeon E5-2697A v4)

**SPECint®2006 = 72.5**

**SPECint\_base2006 = 69.6**

**CPU2006 license:** 3

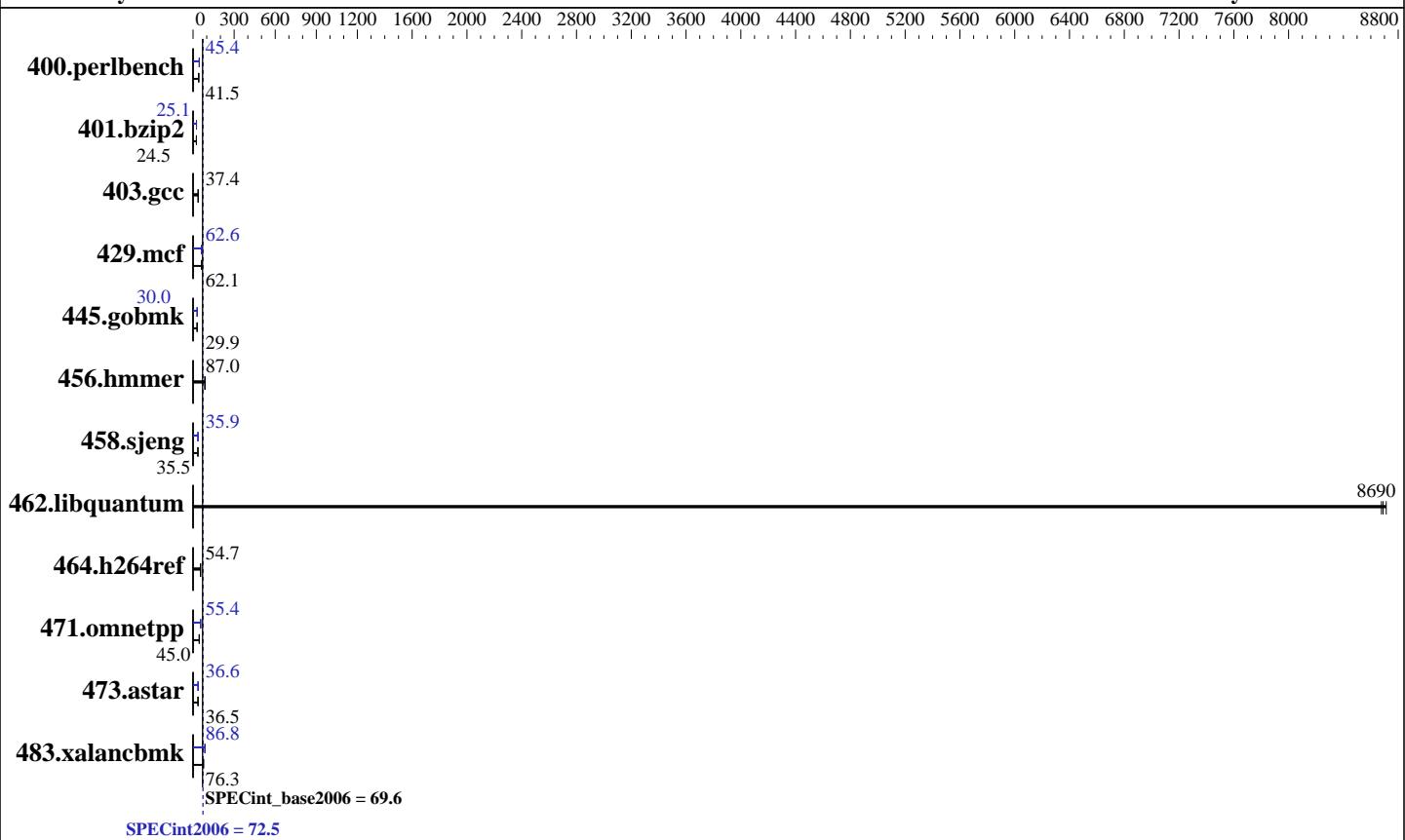
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jun-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2697A v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 40 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 2 x 400 GB SAS SSD, RAID 1  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Compiler: Kernel 3.10.0-327.el7.x86\_64  
 C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	236	41.4	<u>235</u>	<b>41.5</b>	235	41.5	<u>215</u>	<b>45.4</b>	215	45.4	216	45.3
401.bzip2	394	24.5	<u>394</u>	<b>24.5</b>	392	24.6	<u>384</u>	<b>25.1</b>	384	25.1	<u>384</u>	<b>25.1</b>
403.gcc	215	37.4	215	37.4	<u>215</u>	<b>37.4</b>	<u>215</u>	<b>37.4</b>	215	37.4	<u>215</u>	<b>37.4</b>
429.mcf	<u>147</u>	<b>62.1</b>	148	61.6	147	62.2	<u>145</u>	<b>62.9</b>	147	62.2	<u>146</u>	<b>62.6</b>
445.gobmk	351	29.9	351	29.9	<u>351</u>	<b>29.9</b>	<u>350</u>	<b>30.0</b>	351	29.9	350	30.0
456.hmmer	108	86.7	107	87.0	<u>107</u>	<b>87.0</b>	108	86.7	107	87.0	<u>107</u>	<b>87.0</b>
458.sjeng	341	35.5	<u>341</u>	<b>35.5</b>	340	35.5	<u>337</u>	<b>35.9</b>	337	35.9	336	36.0
462.libquantum	2.38	8710	<u>2.38</u>	<b>8690</b>	2.39	8680	2.38	8710	<u>2.38</u>	<b>8690</b>	2.39	8680
464.h264ref	406	54.5	404	54.8	<u>404</u>	<b>54.7</b>	406	54.5	404	54.8	<u>404</u>	<b>54.7</b>
471.omnetpp	136	45.9	139	45.0	<u>139</u>	<b>45.0</b>	<u>113</u>	<b>55.4</b>	112	55.7	113	55.1
473.astar	193	36.4	192	36.5	<u>192</u>	<b>36.5</b>	192	36.7	193	36.4	<u>192</u>	<b>36.6</b>
483.xalancbmk	90.6	76.2	<u>90.5</u>	<b>76.3</b>	90.4	76.3	79.6	86.7	<u>79.5</u>	<b>86.8</b>	79.4	86.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Intel Hyperthreading set to Enabled

Sysinfo program /cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date::: 2014-06-25 ## e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Thu Jun 9 17:49:56 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page



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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697A v4 @ 2.60GHz
        2 "physical id"s (chips)
        64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 40960 KB
```

```
From /proc/meminfo
MemTotal:      528062744 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 9 17:49
```

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   368G  9.8G  358G   3% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 02/22/2016

Memory:

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## Platform Notes (Continued)

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/cpu2006/lib32/32:/cpu2006/lib32/64:/cpu2006/sh"

OMP\_NUM\_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

```
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks (except as noted below):

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

```
473.astar: icpc -m64
```

## Peak Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -DSPEC_CPU_LP64
```

```
429.mcf: -DSPEC_CPU_LP64
```

```
445.gobmk: -D_FILE_OFFSET_BITS=64
```

```
456.hmmr: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -DSPEC_CPU_LP64
```

```
471.omnetpp: -D_FILE_OFFSET_BITS=64
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
               -ansi-alias
```

```
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div
            -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
            -opt-prefetch -ansi-alias
```

403.gcc: basepeak = yes

```
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
          -opt-prefetch -auto-p32
```

```
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
```

456.hmmr: basepeak = yes

```
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
```

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2)
              -opt-ra-region-strategy=block           -ansi-alias
              -Wl,-z,muldefs -L/sh -lsmartheap
```

```
473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
            -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64
```

```
483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
                -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap
```

## Peak Other Flags

C benchmarks:

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## Peak Other Flags (Continued)

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

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