



# SPEC® CINT2006 Result

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## NEC Corporation

**SPECint®2006 = 60.5**

Express5800/B120g-h (Intel Xeon E5-2650 v4)

**SPECint\_base2006 = 57.9**

CPU2006 license: 9006

Test date: Mar-2016

Test sponsor: NEC Corporation

Hardware Availability: Jun-2016

Tested by: NEC Corporation

Software Availability: Jan-2016



### Hardware

CPU Name: Intel Xeon E5-2650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 400 GB SATA, SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.4.5.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>287</b>	<b>34.0</b>	297	32.9	285	34.3	263	37.1	262	37.2	<b>263</b>	<b>37.2</b>
401.bzip2	<b>459</b>	<b>21.0</b>	460	21.0	459	21.0	452	21.3	<b>452</b>	<b>21.3</b>	452	21.4
403.gcc	246	32.7	<b>246</b>	<b>32.7</b>	247	32.7	<b>246</b>	<b>32.8</b>	247	32.6	245	32.8
429.mcf	157	58.0	160	57.0	<b>158</b>	<b>57.8</b>	161	56.8	<b>158</b>	<b>57.8</b>	157	58.2
445.gobmk	418	25.1	<b>418</b>	<b>25.1</b>	418	25.1	418	25.1	<b>418</b>	<b>25.1</b>	418	25.1
456.hammer	<b>130</b>	<b>71.5</b>	130	71.6	131	71.3	<b>130</b>	<b>71.5</b>	130	71.6	131	71.3
458.sjeng	415	29.1	416	29.1	<b>416</b>	<b>29.1</b>	<b>411</b>	<b>29.5</b>	411	29.5	411	29.4
462.libquantum	3.49	5940	<b>3.45</b>	<b>6000</b>	3.43	6040	3.49	5940	<b>3.45</b>	<b>6000</b>	3.43	6040
464.h264ref	460	48.1	<b>460</b>	<b>48.1</b>	462	47.9	460	48.1	<b>460</b>	<b>48.1</b>	462	47.9
471.omnetpp	177	35.4	177	35.2	<b>177</b>	<b>35.2</b>	<b>133</b>	<b>47.0</b>	134	46.7	133	47.0
473.astar	229	30.7	227	30.9	<b>228</b>	<b>30.7</b>	<b>228</b>	<b>30.9</b>	227	30.9	229	30.7
483.xalancbmk	107	64.7	<b>107</b>	<b>64.6</b>	107	64.6	96.1	71.8	<b>96.0</b>	<b>71.9</b>	95.8	72.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
 Energy Performance: Performance  
 Patrol Scrub: Disabled  
 Snoop Mode: Home Snoop with Directory  
 Hyper-Threading: Disabled

## General Notes

Environment variables set by runspec before the start of the run:  
 KMP\_AFFINITY = "granularity=fine,compact"  
 LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"  
 OMP\_NUM\_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
 Transparent Huge Pages enabled with:  
 echo always > /sys/kernel/mm/transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalanbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
 -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
 -opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

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<b>CPU2006 license:</b> 9006	<b>Test date:</b> Mar-2016
<b>Test sponsor:</b> NEC Corporation	<b>Hardware Availability:</b> Jun-2016
<b>Tested by:</b> NEC Corporation	<b>Software Availability:</b> Jan-2016

## Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2)  
 -opt-ra-region-strategy=block -ansi-alias  
 -Wl,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -auto-p32 -Wl,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
 -ansi-alias -Wl,-z,muldefs -L/sh -lsmarheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-B120g-RevB.xml>

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 For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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