



SPEC® CINT2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant XL450 Gen9

(2.20 GHz, Intel Xeon E5-2630 v4)

SPECint®2006 =

63.2

SPECint_base2006 =

60.3

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2630 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 2 x 480 GB SATA SSD, RAID 1
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)
 Kernel 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	267	36.6	<u>267</u>	<u>36.6</u>	266	36.7	245	39.8	<u>245</u>	<u>39.8</u>	245	39.9
401.bzip2	<u>425</u>	<u>22.7</u>	425	22.7	423	22.8	416	23.2	<u>416</u>	<u>23.2</u>	417	23.2
403.gcc	230	35.0	230	35.0	<u>230</u>	<u>35.0</u>	228	35.3	228	35.3	<u>228</u>	<u>35.3</u>
429.mcf	147	62.2	<u>146</u>	<u>62.4</u>	145	63.0	<u>145</u>	<u>63.1</u>	144	63.2	145	63.1
445.gobmk	396	26.5	<u>396</u>	<u>26.5</u>	396	26.5	394	26.7	<u>394</u>	<u>26.7</u>	393	26.7
456.hammer	123	75.6	<u>123</u>	<u>76.1</u>	123	76.1	123	75.6	<u>123</u>	<u>76.1</u>	123	76.1
458.sjeng	391	31.0	<u>391</u>	<u>30.9</u>	391	30.9	<u>386</u>	<u>31.3</u>	386	31.3	386	31.3
462.libquantum	<u>4.03</u>	<u>5140</u>	4.03	5140	4.09	5070	<u>4.03</u>	<u>5140</u>	4.03	5140	4.09	5070
464.h264ref	434	51.0	<u>434</u>	<u>51.0</u>	433	51.1	434	51.0	<u>434</u>	<u>51.0</u>	433	51.1
471.omnetpp	189	33.0	<u>186</u>	<u>33.5</u>	186	33.7	134	46.7	<u>134</u>	<u>46.8</u>	133	46.8
473.astar	214	32.9	<u>211</u>	<u>33.3</u>	211	33.3	214	32.9	<u>211</u>	<u>33.3</u>	211	33.3
483.xalancbmk	<u>100</u>	<u>69.0</u>	100	68.9	99.9	69.1	<u>90.2</u>	<u>76.5</u>	<u>90.2</u>	<u>76.5</u>	90.3	76.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled

Power Profile set to Maximum Performance

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on XL450-G9-node2 Sun May 1 10:21:26 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

Continued on next page

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Page 2



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Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz

2 "physical id"s (chips)

20 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 10

siblings : 10

physical 0: cores 0 2 3 4 8 9 10 11 12

physical 1: cores 0 2 3 4 8 9 10 11 12

cache size : 25600 KB

From /proc/meminfo

MemTotal: 263712764 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.2 (Maipo)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="7.2"

PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"

ANSI_COLOR="0;31"

CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:

Linux XL450-G9-node2 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT

2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 1 10:15

SPEC is set to: /cpu2006

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda4 xfs 438G 20G 419G 5% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U21 03/10/2016

Memory:

16x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32

-Wl,-z,muldefs -L/sh -lsmarthheap64

Base Other Flags

C benchmarks:

Continued on next page

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Page 4



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Base Other Flags (Continued)

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

Continued on next page



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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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