



SPEC® CINT2006 Result

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Huawei

Huawei RH2288H V3 (Intel Xeon E5-2699 v4)

SPECint®2006 =

Not Run

SPECint_base2006

NC

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not up policy on SPEC CPU run

400.perlbench |

401.bzip2 |

403.gcc |

429.mcf |

445.gobmk |

456.hmmer |

458.sjeng |

462.libquantum |

464.h264ref |

471.omnetpp |

473.astar |

483.xalancbmk |

Hardware

CPU Name: Intel Xeon E5-2699 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Operating System:

Red Hat Enterprise Linux Server release 7.0 (Maipo)

3.10.0-123.el7.x86_64

C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux

Yes

xfs

Run level 3 (multi-user)

32/64-bit

32/64-bit

Microquill SmartHeap V10.2

Software



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|---------|-------|---------|-------|---------|-------|---------|-------|---------|-------|---------|-------|
| | Seconds | Ratio |
| 400.perlbench | NC | NC |
| 401.bzip2 | NC | NC |
| 403.gcc | NC | NC |
| 429.mcf | NC | NC |
| 445.gobmk | NC | NC |
| 456.hmmer | NC | NC |
| 458.sjeng | NC | NC |
| 462.libquantum | NC | NC |
| 464.h264ref | NC | NC |
| 471.omnetpp | NC | NC |
| 473.astar | NC | NC |
| 483.xalancbmk | NC | NC |

Results appear in the column in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Green Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Mar 7 11:40:03 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
2 "physical id"s (chips)

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Platform Notes (Continued)

44 "processors"
cores, siblings (Caution: counting these is hw and sys dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 22
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
cache size : 56320 KB

From /proc/meminfo
MemTotal: 26356798 kB
HugePages_Total:
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 7 11:35

SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 449G 13G 437G 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

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Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016

Memory:

8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/spec16/lib32:/spec16/libs/64:/spec16/sh"

OMP_NUM_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=11 runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L./sh -fsmartheap64

Base Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



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Non-Compliant

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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