



# SPEC® CINT2006 Result

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## NEC Corporation

**SPECint®2006 = 68.1**

### Express5800/T110h-S (Intel Core i3-6300)

**SPECint\_base2006 = 65.9**

CPU2006 license: 9006

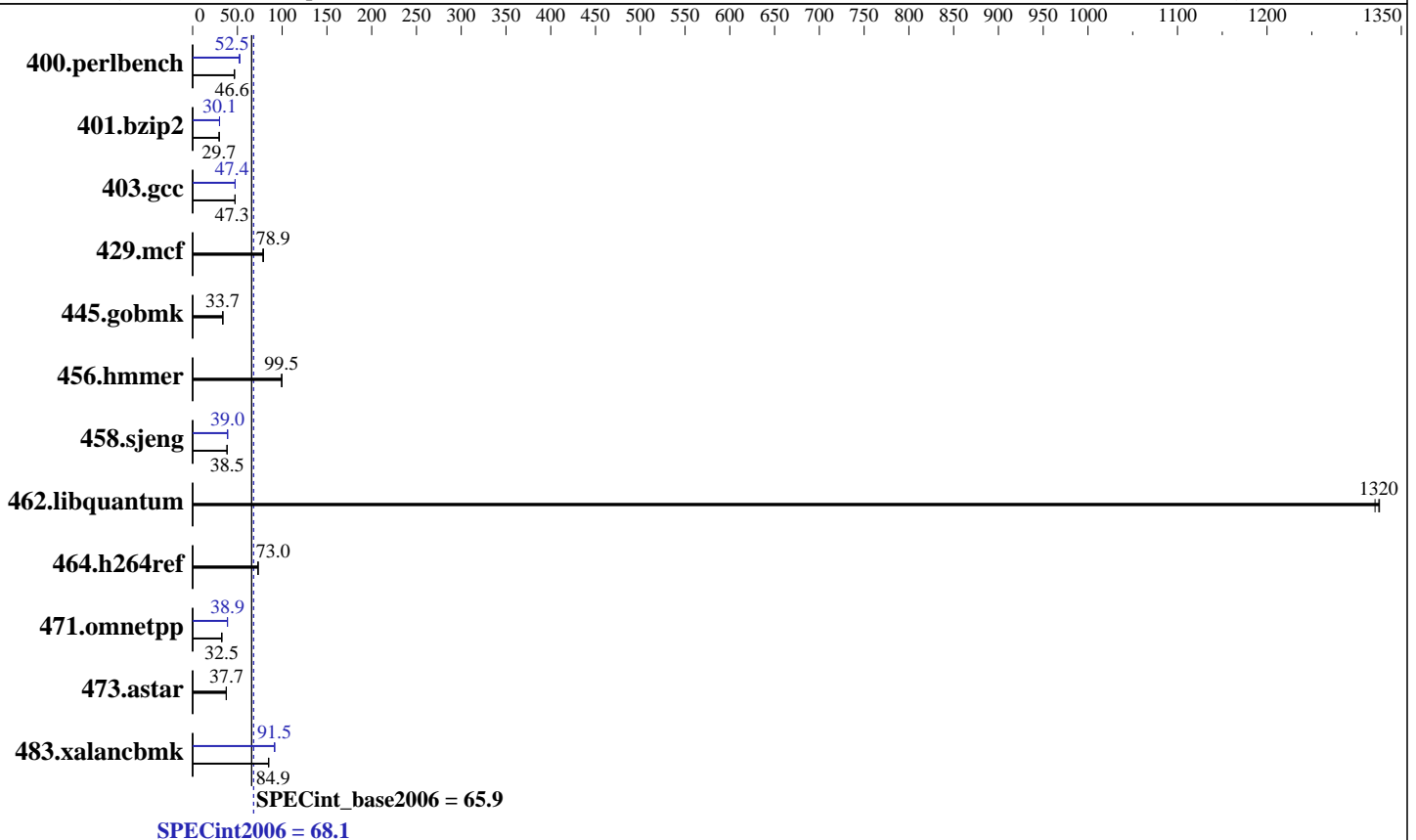
Test date: Dec-2015

Test sponsor: NEC Corporation

Hardware Availability: Mar-2016

Tested by: NEC Corporation

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Core i3-6300  
 CPU Characteristics: 3800  
 CPU MHz: 3800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>210</u></b>	<b><u>46.6</u></b>	210	46.4	208	46.9	<b><u>186</u></b>	<b><u>52.5</u></b>	186	52.6	187	52.2
401.bzip2	325	29.7	326	29.6	<b><u>325</u></b>	<b><u>29.7</u></b>	321	30.1	<b><u>321</u></b>	<b><u>30.1</u></b>	322	30.0
403.gcc	170	47.5	<b><u>170</u></b>	<b><u>47.3</u></b>	170	47.3	<b><u>170</u></b>	<b><u>47.4</u></b>	170	47.5	170	47.4
429.mcf	116	78.4	115	79.2	<b><u>116</u></b>	<b><u>78.9</u></b>	116	78.4	115	79.2	<b><u>116</u></b>	<b><u>78.9</u></b>
445.gobmk	312	33.7	<b><u>312</u></b>	<b><u>33.7</u></b>	312	33.7	312	33.7	<b><u>312</u></b>	<b><u>33.7</u></b>	312	33.7
456.hammer	<b><u>93.8</u></b>	<b><u>99.5</u></b>	93.8	99.4	93.8	99.5	<b><u>93.8</u></b>	<b><u>99.5</u></b>	93.8	99.4	93.8	99.5
458.sjeng	314	38.5	<b><u>315</u></b>	<b><u>38.5</u></b>	315	38.5	<b><u>310</u></b>	<b><u>39.0</u></b>	311	38.9	310	39.0
462.libquantum	15.6	1330	<b><u>15.6</u></b>	<b><u>1320</u></b>	15.7	1320	15.6	1330	<b><u>15.6</u></b>	<b><u>1320</u></b>	15.7	1320
464.h264ref	<b><u>303</u></b>	<b><u>73.0</u></b>	303	73.0	302	73.2	<b><u>303</u></b>	<b><u>73.0</u></b>	303	73.0	302	73.2
471.omnetpp	192	32.5	192	32.6	<b><u>192</u></b>	<b><u>32.5</u></b>	161	38.9	<b><u>161</u></b>	<b><u>38.9</u></b>	161	38.9
473.astar	186	37.7	<b><u>186</u></b>	<b><u>37.7</u></b>	188	37.4	186	37.7	<b><u>186</u></b>	<b><u>37.7</u></b>	188	37.4
483.xalancbmk	81.3	84.9	81.4	84.7	<b><u>81.3</u></b>	<b><u>84.9</u></b>	<b><u>75.4</u></b>	<b><u>91.5</u></b>	75.6	91.3	75.3	91.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Power Management Policy: Custom  
Energy Performance: Performance  
Hyper-Threading: Disabled

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "2"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -DSPEC\_CPU\_LP64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalanbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
 -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div  
 -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
 -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmmer: basepeak = yes

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<b>CPU2006 license:</b> 9006	<b>Test date:</b> Dec-2015
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<b>Tested by:</b> NEC Corporation	<b>Software Availability:</b> Nov-2015

## Peak Optimization Flags (Continued)

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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