



SPEC® CFP2006 Result

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Huawei

SPECfp®_rate2006 = 3500

Huawei RH8100 V3 (Intel Xeon E7-8860 v3)

SPECfp_rate_base2006 = 3410

CPU2006 license: 3175

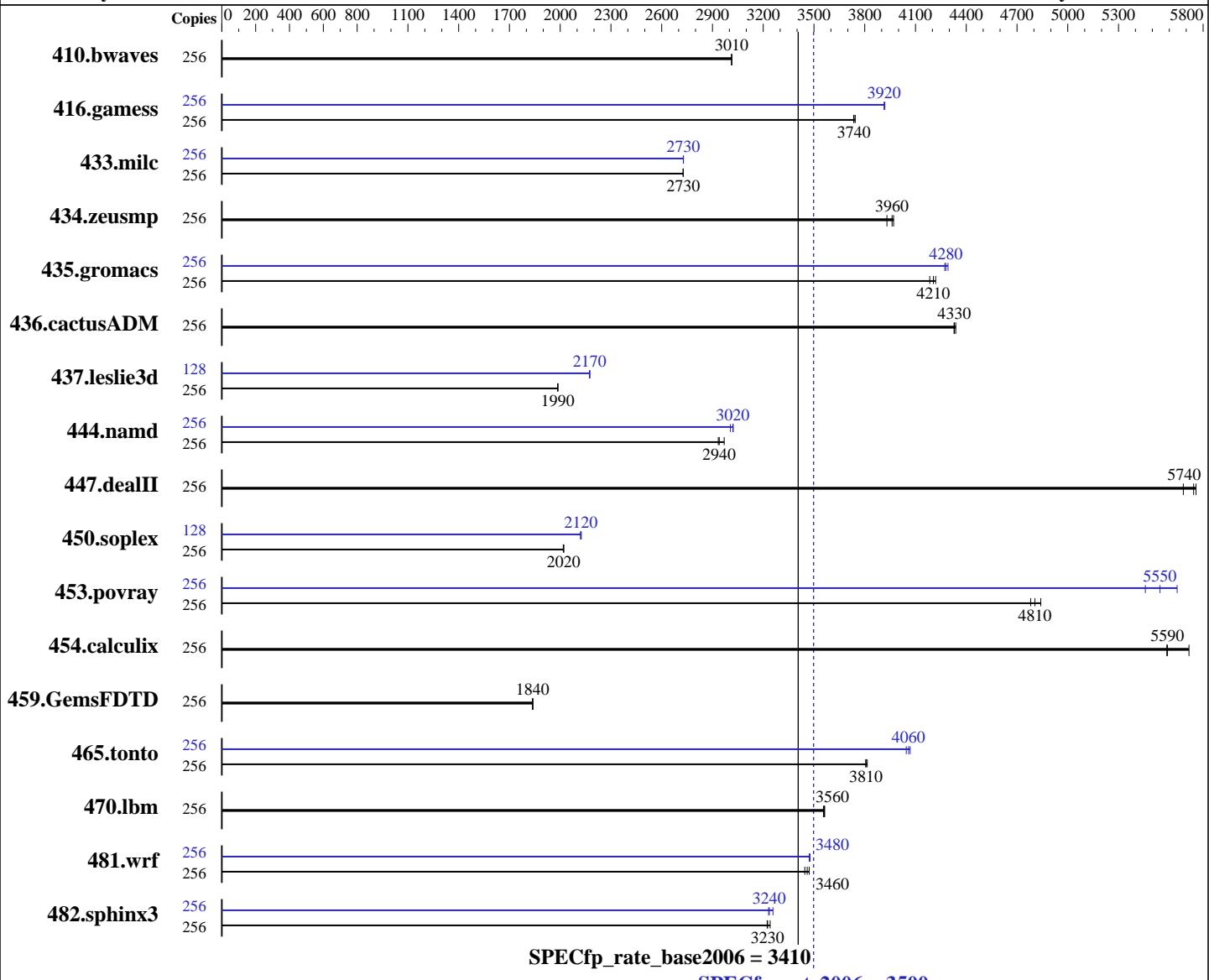
Test date: Dec-2015

Test sponsor: Huawei

Hardware Availability: May-2015

Tested by: Huawei

Software Availability: Mar-2015



Hardware

CPU Name: Intel Xeon E7-8860 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 128 cores, 8 chips, 16 cores/chip, 2 threads/core
 CPU(s) orderable: 4,6,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.1 (Maipo)
 Compiler: 3.10.0-229.el7.x86_64
 C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: xfs

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L3 Cache: 40 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R,
 running at 1600 MHz)
 Disk Subsystem: 3 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	256	1154	3010	1155	3010	1154	3010	256	1154	3010	1155	3010	1154	3010
416.gamess	256	1342	3730	1339	3740	1341	3740	256	1280	3920	1279	3920	1280	3920
433.milc	256	862	2730	862	2730	862	2730	256	862	2730	861	2730	861	2730
434.zeusmp	256	588	3960	587	3970	593	3930	256	588	3960	587	3970	593	3930
435.gromacs	256	435	4210	433	4220	437	4180	256	428	4270	427	4280	426	4290
436.cactusADM	256	707	4330	705	4340	706	4330	256	707	4330	705	4340	706	4330
437.leslie3d	256	1212	1990	1212	1990	1212	1990	128	553	2180	554	2170	553	2170
444.namd	256	698	2940	691	2970	700	2930	256	679	3020	683	3010	680	3020
447.dealII	256	509	5760	515	5680	510	5740	256	509	5760	515	5680	510	5740
450.soplex	256	1056	2020	1057	2020	1058	2020	128	503	2120	504	2120	503	2120
453.povray	256	281	4840	283	4810	285	4780	256	246	5550	250	5460	241	5650
454.calculix	256	378	5590	378	5590	369	5720	256	378	5590	378	5590	369	5720
459.GemsFDTD	256	1479	1840	1479	1840	1477	1840	256	1479	1840	1479	1840	1477	1840
465.tonto	256	662	3810	662	3810	660	3820	256	619	4070	623	4050	621	4060
470.lbm	256	989	3560	988	3560	987	3560	256	989	3560	988	3560	987	3560
481.wrf	256	824	3470	830	3450	826	3460	256	823	3480	823	3480	824	3470
482.sphinx3	256	1539	3240	1548	3220	1547	3230	256	1532	3260	1544	3230	1542	3240

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Lock_step to disabled

Baseboard Management Controller used to adjust the fan speed to 100%

Set Memory Power Saving to disabled

Sysinfo program /home/spec/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on RH8100 Wed Dec 2 06:44:55 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8860 v3 @ 2.20GHz
        8 "physical id"s (chips)
        256 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 16
        siblings   : 32
        physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 4: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 5: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 6: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
        physical 7: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 40960 KB
```

```
From /proc/meminfo
MemTotal:      1056449132 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.1 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.1"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

uname -a:

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Platform Notes (Continued)

```
Linux RH8100 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 2 06:04
```

```
SPEC is set to: /home/spec
```

```
Filesystem           Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel00-home xfs   781G   16G  766G   2% /home
```

```
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS American Megatrends Inc. BLISV623 09/16/2015
```

```
Memory:
```

```
4x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

```
1x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

```
128x NO DIMM NO DIMM
```

```
59x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have three lines reading as:

```
4x Hynix HMA42GR7MFR4N-TFTD 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

```
128x NO DIMM NO DIMM
```

```
60x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh"
```

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m64

Fortran benchmarks:

fort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2)
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
-auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
           -auto-ilp32
```

```
447.dealII: basepeak = yes
```

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14
           -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
           -inline-level=0 -scalar-rep-
```

```
434.zeusmp: basepeak = yes
```

```
437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

```
459.GemsFDTD: basepeak = yes
```

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll14
           -auto -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2)
           -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
           -opt-prefetch -auto-ilp32
```

```
436.cactusADM: basepeak = yes
```

```
454.calculix: basepeak = yes
```

```
481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
```



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-HSW-RevG.xml>

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